

**TESTING FOR THE VERIFICATION OF COMPLIANCE
OF PV INVERTER WITH:
EN 50549-1: 2019:
REQUIREMENTS FOR GENERATING PLANTS TO BE
CONNECTED IN PARALLEL WITH DISTRIBUTION
NETWORKS - PART 1: CONNECTION TO A LV DISTRIBUTION
NETWORK - GENERATING PLANTS UP TO AND INCLUDING
TYPE B
(REQUIREMENTS FOR PLANTS TYPE B)**

Procedure: PE.T-LE-62

Test Report Number : 2221 / 0052

Type : Hybrid Inverter

Tested Model..... : HPS50

Trade Mark..... :  **ATESS**

Variant Models..... : HPS30

APPLICANT

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28042 Madrid (Spain)

HIRED BY

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SGS Tecnos, S.A.
Laboratorio de Ensayos E&E

Date of issue : 2021/04/05

Number of pages : 202

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Test Report Historical Revision:

Test Report Version	Date	Resume
2221 / 0052	2021/04/05	First issuance

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1. SCOPE

SGS Tecnos, S.A. (Electrical Testing Laboratory) has been contracted by SGS Tecnos, S.A. (Certification Body) to perform the testing according the EN 50549 – 1: 2019: Requirements for generating plants to be connected in parallel with distribution networks - part 1: connection to a LV distribution network - generating plants up to and including type B.

The tests offered at this test report evaluate the EUT compliance with the requirements of **Type B**.

2. GENERAL INFORMATION

2.1. TESTING PERIOD AND CLIMATIC CONDITIONS

The necessary testing has been performed along the period 20th November of 2020 and 30th March of 2021.

All the tests and checks have been performed at 25 ± 5 °C, 96 ± 10 kPa and 50 ± 10 %RH.

SITE TEST

Name : Shenzhen BALUN Technology Co., Ltd.
 Address : Room 104, 204, 205, Building 1, No. 6, Industrial South Road, Songshan Lake District, Dongguan, Guangdong, P.R. China

2.2. EQUIPMENT UNDER TESTING

Apparatus type : Hybrid Inverter
 Installation : Fixed installation
 Manufacturer : Shenzhen Ateess Power Technology Co., Ltd.
 Trade mark : 
 Model / Type reference : HPS50
 Serial Number : TMD094602A
 Software Version : DSP: HPS30K_150K_HV3_SV4.1.14_APP50549;
 LCD: HPS30K_150K_WEINVIEWScreen_HV1.0_SV1.0.8
 Rated Characteristics : PV input: 480-800 V_{mppt} (Max. 1000 V input), 136A max;
 Battery input: 352-600 Vdc, Charge current 150 Adc,
 Discharge current 156 Adc;
 AC output: 3P/PE, 400 Vac, 50 Hz, 3*72 A, 50 kW
 Date of manufacturing: 2020

Test item particulars

Input : DC
 Output : AC
 Class of protection against electric shock... : Class I
 Degree of protection against moisture : IP20
 Type of connection to the main supply : Three phase – Fixed installation
 Cooling group : Fans
 Modular : No
 Internal Transformer : Yes

Copy of marking plate (representative):

ATESS Hybrid Power Systems	
Model	HPS50
PV Max generating power	75kW
Max PV Open-circuit voltage	1000Vdc
PV MPPT voltage range	480-800Vdc
PV Max input current	136A
ISC PV	170.5A
Battery voltage range	352-600Vdc
Battery Max charge/discharge current	150/156A
Battery Max charge/discharge power	75kW/55kW
AC Rated voltage	400Vac
AC Rated frequency	50/60Hz
AC Rated current	72A
AC Rated output power	50kW
Max AC output power	55kVA
Max AC input power	100kVA
PF Range	0.8lagging--0.8leading
Protective Class	Class I
Ingress Protection	IP20
Communication Port	RS485/CAN
Inverter Topology	Isolated
Operating Temp.Range	-25°C to +55°C
DATE OF MADE	
S/N:	
 www.atesspower.com MADE IN CHINA	

ATESS Hybrid Power Systems	
Model	HPS30
PV Max generating power	45kW
Max PV Open-circuit voltage	1000Vdc
PV MPPT voltage range	480-800Vdc
PV Max input current	90A
ISC PV	100A
Battery voltage range	352-600Vdc
Battery Max charge/discharge current	100/93A
Battery Max charge/discharge power	45kW/33kW
AC Rated voltage	400Vac
AC Rated frequency	50/60Hz
AC Rated current	43A
AC Rated output power	30kW
Max AC output power	33kVA
Max AC input power	60kVA
PF Range	0.8lagging--0.8leading
Protective Class	Class I
Ingress Protection	IP20
Communication Port	RS485/CAN
Inverter Topology	Isolated
Operating Temp.Range	-25°C to +55°C
DATE OF MADE	
S/N:	
 www.atesspower.com MADE IN CHINA	

Note:

1. The above markings are the minimum requirements required by the safety standard. For the final production samples, the additional markings which do not give rise to misunderstanding may be added.
2. Label is attached on the side surface of enclosure and visible after installation.

Equipment under testing:

- **HPS50**

The variant models are:

- **HPS30**

The variant models have been included in this test report without tests because the following features don't change regarding to the tested model:

- Same connection system and hardware topology
- Same control algorithm
- Output power within 1/√10 and 2 times of the rated output power of the EUT or Modular inverters.
- Same Firmware Version

Following table shows the full ratings of the variant models:

Model Number	HPS30	HPS50
PV parameters		
PV rated Power	33 kW	55 kW
Max. PV Power	45 kW	75 kW
Max. PV input voltage	1000 Vd.c.	
PV MPPT voltage range	480-800 Vd.c.	
Battery parameters		
Battery operating voltage range	352-600 Vd.c.	
Max.Battery charge current	100 A	150 A
Max.Battery discharge current	93 A	156 A
Max.Battery charge power	45 kW	75 kW
Max.Battery discharge power	33 kW	55 kW
AC output parameter		
Rated voltage	400 Vac	
Rated current	3*43 A	3*72 A
Rated output power	30 kW	50 kW
Rated frequency	50 Hz	
Frequency Range	45-55 Hz	
Max.output power	33 kVA	55 kVA
Max. input power	60 kVA	100 kVA
Power factor	1 default (+/- 0.8 adjustable)	
Ambient temperature	-25 °C ~ 55 °C	
Ingress protection	IP20	
Protective class	Class I	

The results obtained apply only to the particular sample tested that is the subject of the present test report. The most unfavorable result values of the verifications and tests performed are contained herein.

Throughout this report a point (comma) is used as the decimal separator.

2.3. REFERENCE VALUES

The values presented in the following table have been used for calculation of referenced values (p.u.; %) though the report.

Reference Values: Model HPS50	
Design active power, P_D in kW	50
Rated apparent power, S_n in kVA	55
Rated wind speed (only WT), v_n in m/s	N/A
Rated current (determined), I_n in A	3*72
Rated output voltage, (phase to phase) U_n in Vac	400
Note: In this report p.u. values are calculated as follows: -For Active & Reactive Power p.u values are reference to S_n -For Currents p.u values, the reference is always I_n -For Voltages p.u values, the reference is always U_n	

2.4. TEST EQUIPMENT LIST

Test date from 20/11/2020 to 13/01/2021.

From	No.	Equipment Name	MARK/Model No.	Equipment No.	Equipment calibration due date
BALUN	1	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-1	2020-03-04 to 2021-03-03
	2	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-2	2020-03-04 to 2021-03-03
	3	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-3	2020-03-04 to 2021-03-03
	4	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-4	2020-03-04 to 2021-03-03
	5	Power analyzer	HIOKI / PW6001-16	BZ-DGD-L025	2020-03-04 to 2021-03-03
	6	Power analyzer	DEWETRON / DEWE2-A4	BZ-DGD-L119	2020-03-04 to 2021-03-03
	7	Temperature and Humidity meter	CEM/DT-322	BZ-DGD-L005	2020-03-07 to 2021-03-06
	8	Power analyzer	ZhiYuan / PA6000H	BZ-DGD-L059	2020-11-18 to 2021-11-17
	9	Power analyzer	DEWEsoft/SIRIUSI-HS-4XIIV-4XLV	BZ-DGD-L163	2020-03-04 to 2021-03-03
SGS	10	True RMS Multimeter	Fluke / 289C	GZE012-53	2020-02-21 to 2021-02-20

Test date in 30/03/2021.

From	No.	Equipment Name	MARK/Model No.	Equipment No.	Equipment calibration due date
BALUN	1	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-1	2021-03-01 to 2022-02-28
	2	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-2	2021-03-01 to 2022-02-28
	3	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-3	2021-03-01 to 2022-02-28
	4	Current clamp	HIOKI / CT6863-05	BZ-DGD-L026-4	2021-03-01 to 2022-02-28
	5	Power analyzer	HIOKI / PW6001-16	BZ-DGD-L025	2021-03-16 to 2022-03-15
	6	Temperature and Humidity meter	CEM/DT-322	BZ-DGD-L005	2021-02-25 to 2022-02-24
SGS	7	True RMS Multimeter	Fluke /187	GZE012-8	2020-12-04 to 2021-12-03

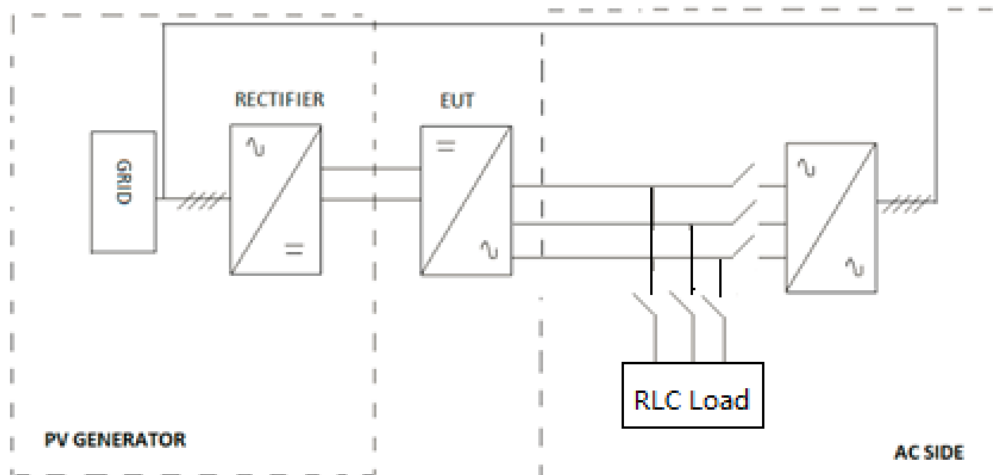
2.5. MEASUREMENT UNCERTAINTY

Associated uncertainties through measurements showed in this report are the maximum allowable uncertainties.

Magnitude	Uncertainty
Voltage measurement	±1.5 %
Current measurement	±2.0 %
Frequency measurement	±0.2 %
Time measurement	±0.2 %
Power measurement	±2.5 %
Phase Angle	±1 °
Temperature	±3 °C
Note1: Measurements uncertainties showed in this table are maximum allowable uncertainties. The measurement uncertainties associated with other parameters measured during the tests are in the laboratory at disposal of the petitioner. Note2: Where the standard requires lower uncertainties that those in this table. Most restrictive uncertainty has been considered.	

2.6. TEST SET UP OF THE DIFFERENT STANDARD

Below is the simplified construction of the test set up.



Different equipments have been used to take measures as shown in chapter 2.3. Current and voltage clamps have been connected to the inverter input / output for all the tests.

All the tests described in the following pages have used this specified test setup.

The test bench used includes:

EQUIPMENT	MARK / MODEL	RATED CHARACTERISTICS	OWNER / ID.CODE
AC source	Wogo / WLPA-33-1000kVA	1000 kVA 5-400 Vrms 44.5-65.5 Hz	--
DC source	Wogo / WDGC-1000kW	0 – 1500 Vdc (0.01 V step) 0 – 1333 A (0.01 A step)	--
RLC load	Qunlin / ACLT3816H	563.3kW, 563.3kVAr	--

2.7. DEFINITIONS

EUT	Equipment Under Testing	Hz	Hertz
A	Ampere	V	Volt
Un	Nominal Voltage	p.u	Per unit
In	Nominal Current	Pn	Rated Active Power
Ia	Active Current	Qn	Rated Reactive Power
Ir	Reactive Current	Sn	Rated Apparent Power
MV	Medium Voltage	THC	Total Harmonic Current
LV	Low Voltage	TDD	Total Demand Distortion
UVRT	Under-Voltage Ride Through	I _h	Harmonic Current
OVRT	Over-Voltage Ride Through	Plt	Severity of Flicker Long-Term
Pst	Severity of Flicker Short-Term	ms	Millisecond
dc	Maximum Variation of Voltage	s	Second
d max	Maximum Absolute Value of Voltage Variation	min	Minute
fn	Nominal frequency	P	Active Power
IGBT	Insulated-Gate Bipolar Transistor	Q	Reactive Power
RMS	Root Mean Square	PF	Power Factor
S _{k, fic}	Short-circuit apparent power	Nr.	Number
AC	Alternating Current	POC	Point of Connection
DC	Direct Current	Meas.	Measured
DSO	Distribution System Operator	Des.	Desired
EESS	Electrical energy storage system	PGU	Power Generating Unit
EES	Electrical energy storage	P _D	Design active power
Pmax	Maximum active power	P _M	Momentary active power
P _A	Available active power	Smax	Maximum apparent power

3. RESUME OF TEST RESULTS

INTERPRETATION KEYS

Test object does meet the requirement **P** Pass
 Test object does not meet the requirement **F** Fails
 Test case does not apply to the test object..... **N/A** Not applicable
 To make a reference to a table or an annex. See additional sheet
 To indicate that the test has not been realized **N/R** Not realized

EN 50549-1:2019 – Requirements for plant category Type B have been considered.				
REPORT SECTION	STANDARD SECTION	CHAPTER OF THE STANDARD	Plant category	Result
4.1	4.4	Normal operating range	--	--
4.1.1	4.4.2	Operating frequency range	≥A	P
4.1.2	4.4.3	Minimal requirement for active power delivery at underfrequency	≥A	P
4.1.3	4.4.4	Continuous operating voltage range	≥A	P
4.2	4.5	Immunity to disturbances	≥A	P
4.2.1	4.5.2	Rate of change of frequency (ROCOF) immunity	≥A	P
4.2.2	4.5.3	Under-voltage ride through (UVRT)	B	P
4.2.3	4.5.4	Over-voltage ride through (OVRT)	≥A	P
4.3	4.6	Active response to frequency deviation	≥A	P
4.3.1	4.6.1	Power response to overfrequency	≥A	P
4.3.2	4.6.2	Power response to underfrequency	≥A	P
4.4	4.7	Power response to voltage changes	≥A	P
4.4.1 and 4.4.2	4.7.2	Voltage support by reactive power	≥A	P
4.4.3	4.7.3	Voltage related active power reduction	≥A	P
4.4.4	4.7.4	Short circuit current requirements on generating plants	B	P
4.5	4.8	EMC and power quality	≥A	N/R (*)
4.5.1	4.8	Harmonic emissions	≥A	P
4.5.2	4.8	Flicker and voltage fluctuations	≥A	P
4.6	4.9	Interface protection	≥A	P
4.6.1	4.9.3	Requirements on voltage and frequency protection	≥A	P
4.6.2	4.9.4	Means to detect island situation	≥A	N/R (**)
4.6.3	4.9.5	Digital input to the interface protection	≥A	P
4.7	4.10	Connection and starting to generate electrical power	≥A	P
4.7.1	4.10.2	Automatic reconnection after tripping	≥A	P
4.7.2	4.10.3	Starting to generate electrical power	≥A	P
4.7.3	4.10.4	Synchronization	≥A	P
4.8	4.11	Ceasing and reduction of active power on set point	≥A	P
4.8.1	4.11.1	Ceasing active power	≥A	P
4.8.2	4.11.2	Reduction of active power on set point	B	P
4.9	4.13 & 4.3	Requirements regarding single fault tolerance of interface protection system and interface switch	≥A	N/R (***)

Note: The declaration of conformity has been evaluated taking into account the IEC Guide 115.

The compliances with these requirements are stated in the following test reports:

(*) Test Report no. ES170725041E-1, issued by EMTEK (SHENZHEN) CO., LTD on March 10th, 2020.

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(**) Model HPS30

The compliance with these requirements is stated in page 30 to page 35 of the following test report:
IEC 62116:2014: Test Report n° ES200211002P which was issued by EMTEK (SHENZHEN) CO., LTD on Mar 01th, 2020.

Model HPS50

The compliance with these requirements is stated in page 25 to page 31 of the following test report:
IEC 62116:2014: Test Report n° ES190602003P which was issued by EMTEK (SHENZHEN) CO., LTD on Dec 18th, 2019.

(***) **IEC/EN 62109-1: 2010:** Test Report n° ES170725041P-1 which was issued by EMTEK (SHENZHEN) CO., LTD on March 31th, 2020.

4. TEST RESULTS

4.1. NORMAL OPERATING RANGE

4.1.1. Operating frequency range

The test has been done according to the clause 4.4.2 of the standard, the requirement is as follows:

Table 1 — Minimum time periods for operation in underfrequency and overfrequency situations

Frequency Range	Time period for operation Minimum requirement	Time period for operation stringent requirement
47,0 Hz – 47,5 Hz	not required	20 s
47,5 Hz – 48,5 Hz	30 min ^a	90 min
48,5 Hz – 49,0 Hz	30 min ^a	90 min ^a
49,0 Hz – 51,0 Hz	Unlimited	Unlimited
51,0 Hz – 51,5 Hz	30 min ^a	90 min
51,5 Hz – 52,0 Hz	not required	15 min

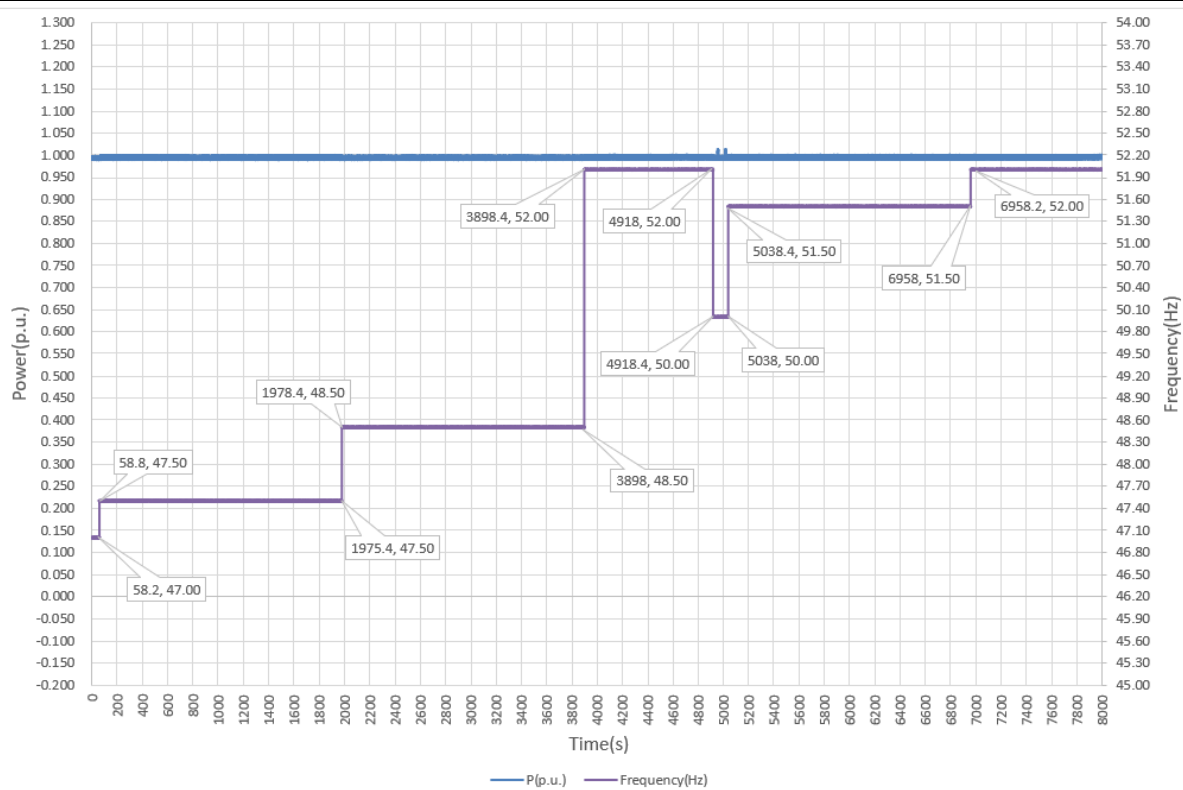
^a Respecting the legal framework, it is possible that longer time periods are required by the responsible party in some synchronous areas.

“Time period for operation, Minimum requirement” (first column of the table) has been considered for this test.

In order to verify this function, parameter settings as in the following table have been considered to perform the test. Time requirements considered are the “minimum requirement” according to Table 1 of the standard:

Steps	f setting (Hz)	Time requirement	f measured (Hz)	Time measured (min)	Power measured (p.u.)
1	47.0	Not required	47.00	0.97	0.994
2	47.5	>30 min	47.50	31.94	0.994
3	48.5	>30 min	48.50	31.99	0.994
4	52.0	Not required	52.00	16.99	0.995
5	50.0	Unlimited	50.00	1.99	0.996
6	51.5	>30 min	51.50	31.99	0.995
7	52.0	Not required	52.00	19.03	0.995

Operating frequency range



4.1.2. Minimal requirement for active power delivery at underfrequency

The test has been done according to the clause 4.4.3 of the standard, the requirement is as follows:

A generating plant shall be resilient to the reduction of frequency at the point of connection while reducing the maximum active power as little as possible.

The admissible active power reduction due to underfrequency is limited by the full line in Figure 5 of the standard and is characterized by a maximum allowed reduction rate of 10 % of P_{max} per 1 Hz for frequencies below 49.5 Hz.

It is possible that a more stringent power reduction characteristic is required by the responsible party. Nevertheless this requirement is expected to be limited to an admissible active power reduction represented by the dotted line in Figure 5 which is characterised by a reduction rate of 2 % of the maximum power P_{max} per 1 Hz for frequencies below 49 Hz.

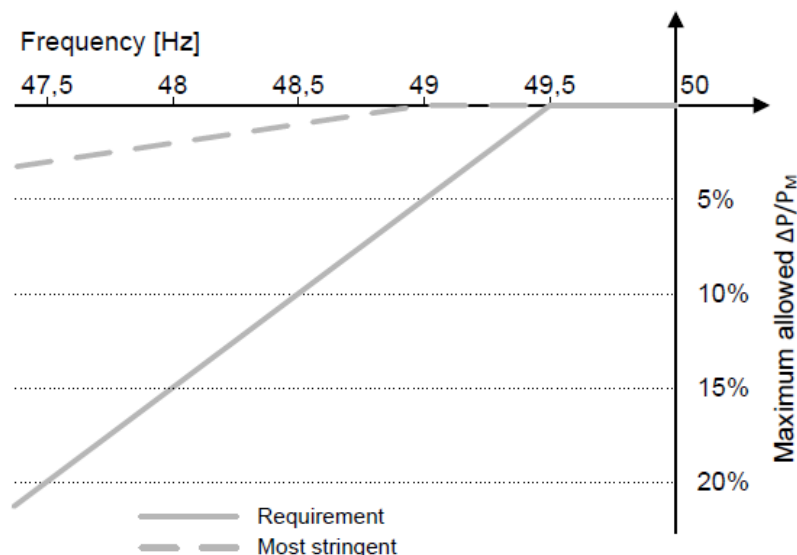
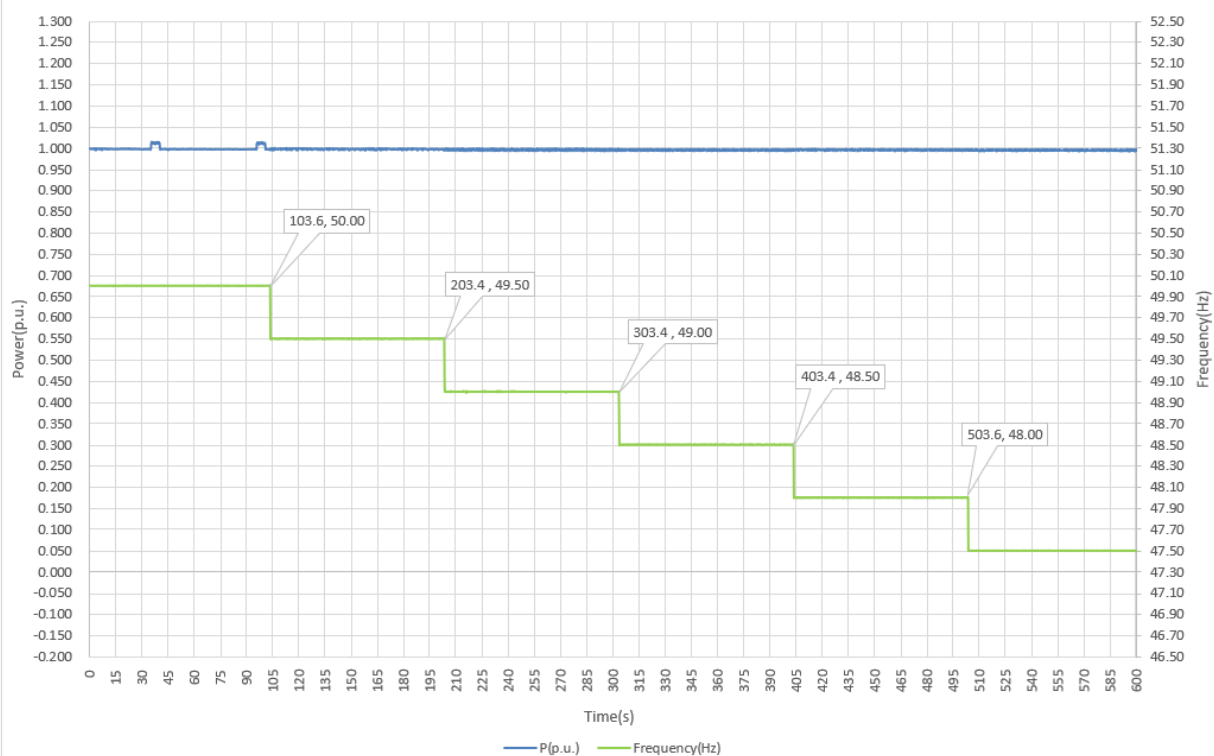


Figure 5 — Maximum allowable power reduction in case of underfrequency

As defined by manufacturer, the power will not reduce when the frequency is below 49.5 Hz.

Step	f (Hz)	f meas. (Hz)	T (s)	T meas. (s)	P desired (p.u.)	P meas. (p.u.)	P deviation (p.u.)
1	50.00 ± 0.05	50.00	>60	103.6	1.000	0.999	-0.001
2	49.50 ± 0.05	49.50	>60	99.6	1.000	0.998	-0.002
3	49.00 ± 0.05	49.00	>60	99.6	1.000	0.997	-0.003
4	48.50 ± 0.05	48.50	>60	99.6	1.000	0.996	-0.004
5	48.00 ± 0.05	48.00	>60	99.8	1.000	0.996	-0.004
6	47.50 ± 0.05	47.50	>60	96.2	1.000	0.996	-0.004

Active power delivery at underfrequency



4.1.3. Continuous operating voltage range

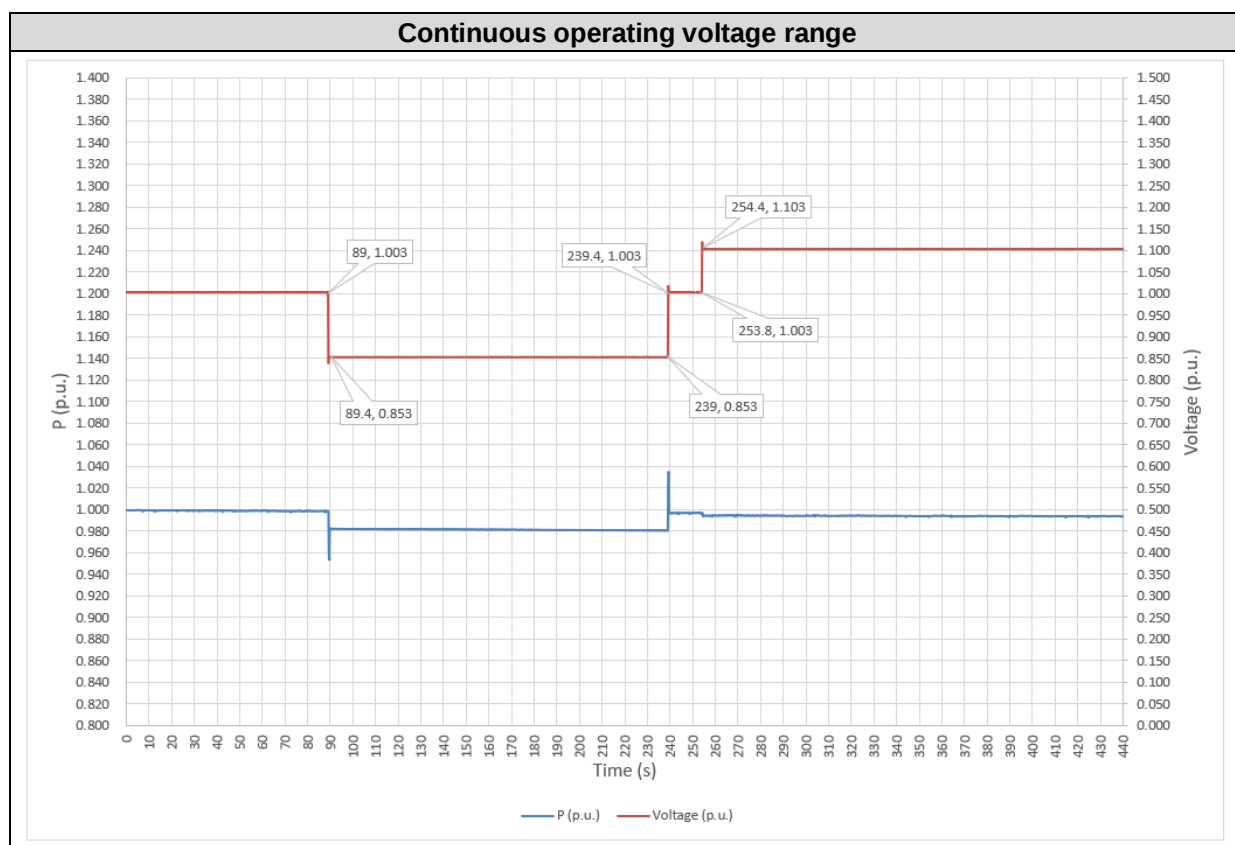
The test has been done according to the clause 4.4.4 of the standard, the requirement is as follows:

The generating plant shall be capable of operating continuously when the voltage at the point of connection stays within the range of 85 %Un to 110 %Un.

In order to verify this function, the parameter setting is as follows to perform the test:

Step	V desired (p.u.)	P desired (p.u.)	V meas. (p.u.)	P meas. (p.u.)	Time meas.(s)
1	1.000	1.000	1.003	1.000	89
2	0.850	1.000	0.853	0.981(*)	149.6
3	1.000	1.000	1.003	0.997	14.4
4	1.100	1.000	1.103	0.996	185.6

(*) Active power reduction is allowed due to current limitation.



4.2. IMMUNITY TO DISTURBANCES

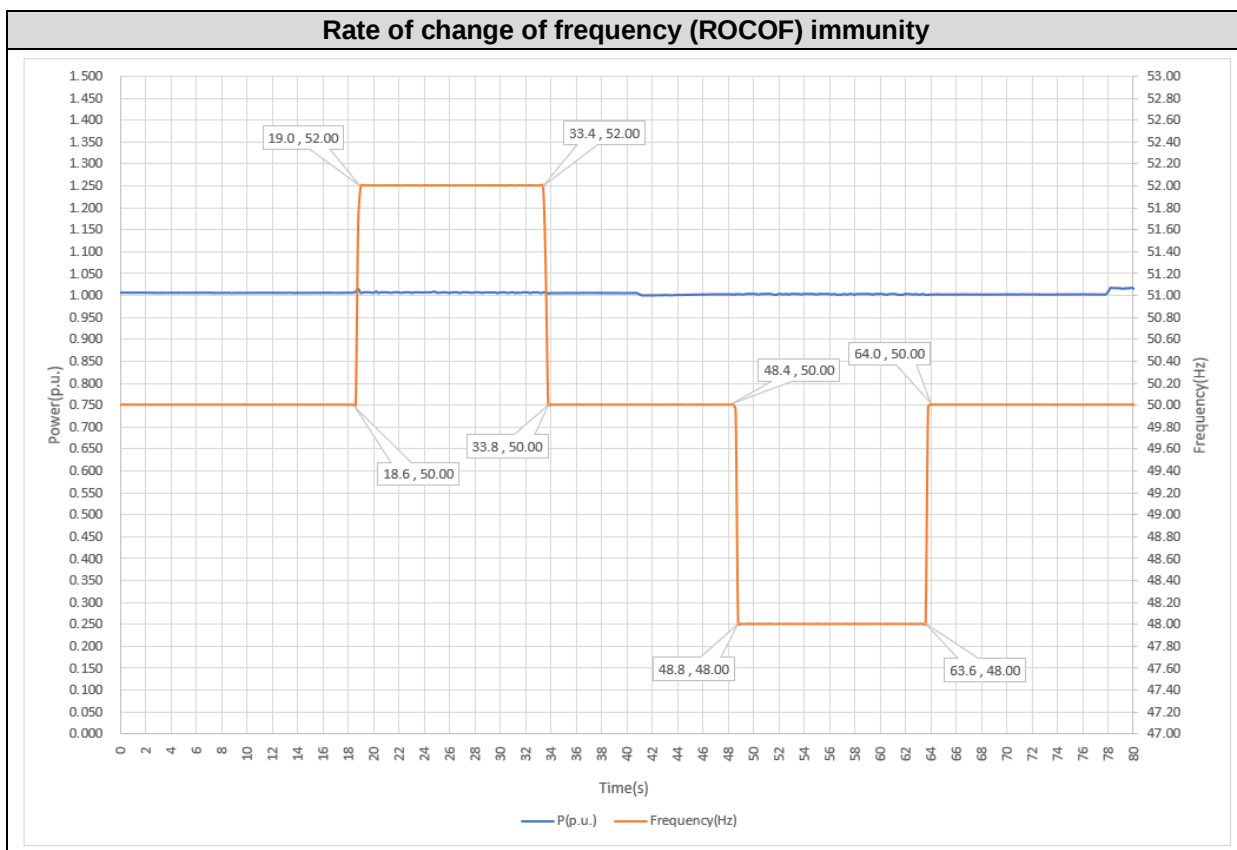
4.2.1. Rate of change of frequency (ROCOF) immunity

The test has been done according to the clause 4.5.2 of the standard, the requirement is as follows:

- **Non-synchronous generating technology: at least 2 Hz/s**

The ROCOF immunity is defined with a sliding measurement window of 200 ms as follows:

Step s	f (Hz)	ROCOF requirement (Hz/s)	Step time	Measured frequency (Hz)	Measured step change time (s)	ROCOF meas. (Hz/s)	Disconnection
1	50.00 ± 0.05	N/A	>10 s	50.00	--	--	No
2	52.00 ± 0.05	>2	>10 s	52.00	0.4	5.00	No
3	50.00 ± 0.05	>2	>10 s	50.00	0.4	5.00	No
4	48.00 ± 0.05	>2	>10 s	48.00	0.4	5.00	No
5	50.00 ± 0.05	>2	>10 s	50.00	0.4	5.00	No



4.2.2. Under-voltage ride through (UVRT)

The requirements are defined in the clause 4.5.3 of the standard.

4.2.1.1. No load Test

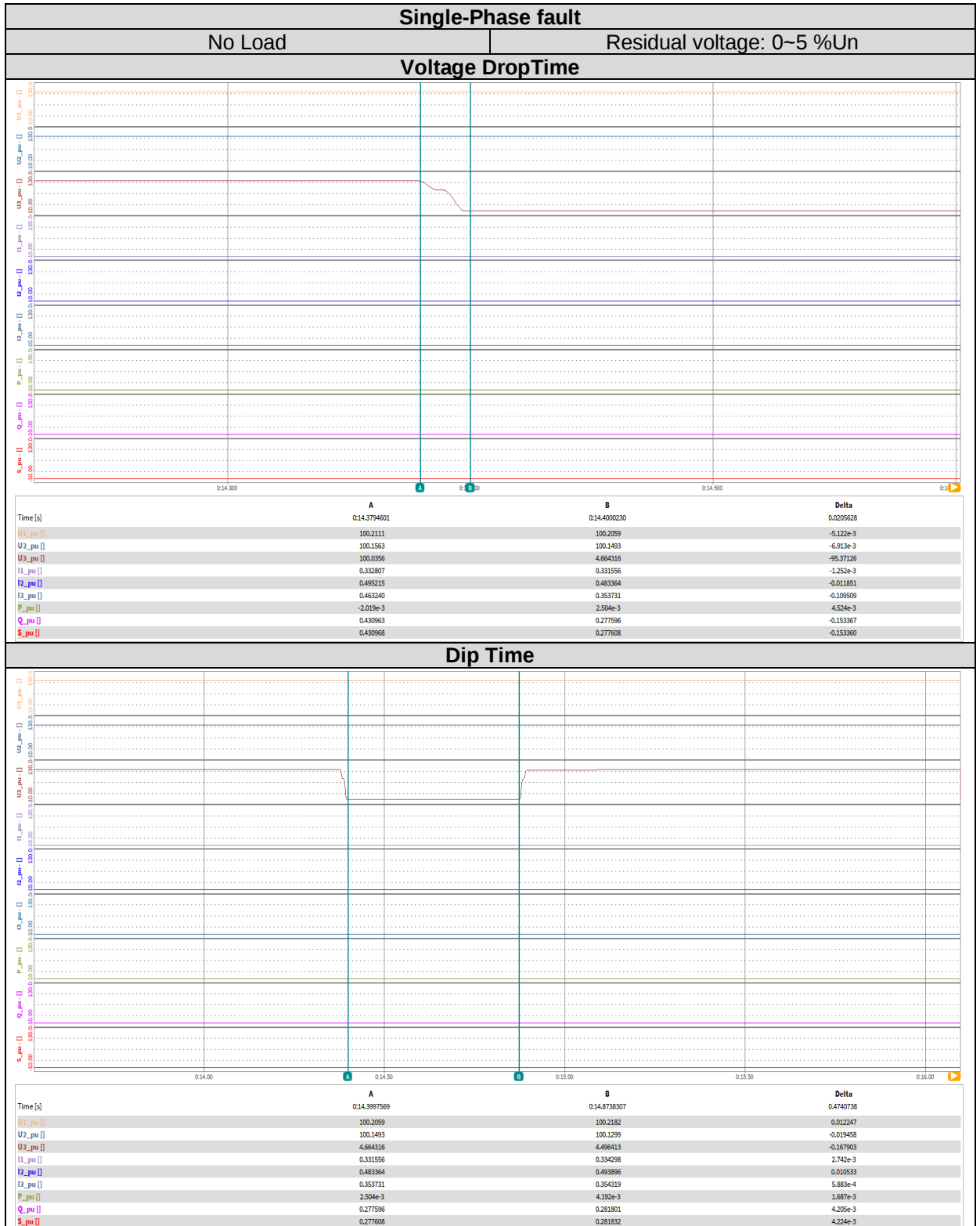
Test results of different no-load cases performed are offered below:

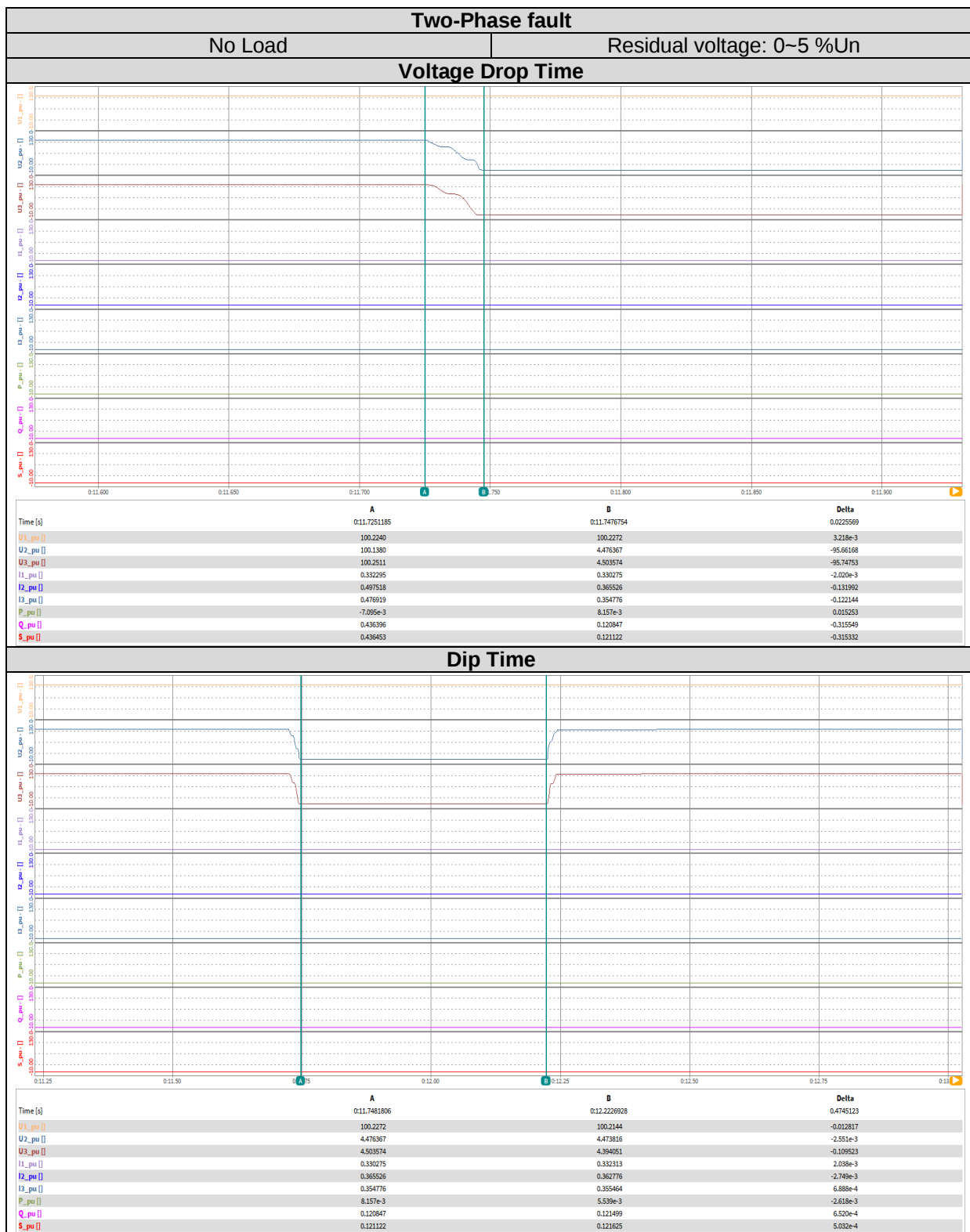
No Load							
Phase type	Residual voltage desired (%Un)	Voltage measured (%Un)	Voltage drop time (ms)	Dip time desired (ms)	Dip time measured (ms)	Power recovery time (ms)	Voltage after recovery (%Un)
1 ph	0-5	4.7	21	≥ 400	474	--	100.2
2 ph		4.5	22		475	--	100.2
3 ph		4.2	21		474	--	100.0
1 ph	25	25.1	23	≥ 1000	1176	--	99.9
2 ph		25.2	21		1173	--	100.0
3 ph		25.3	21		1173	--	100.0
1 ph	50	49.7	21	≥ 1850	1975	--	99.9
2 ph		49.8	21		1972	--	100.0
3 ph		49.8	23		1955	--	100.0
1 ph	75	75.4	21	≥ 3000	3169	--	99.9
2 ph		75.0	21		3150	--	100.0
3 ph		75.0	21		3153	--	100.1

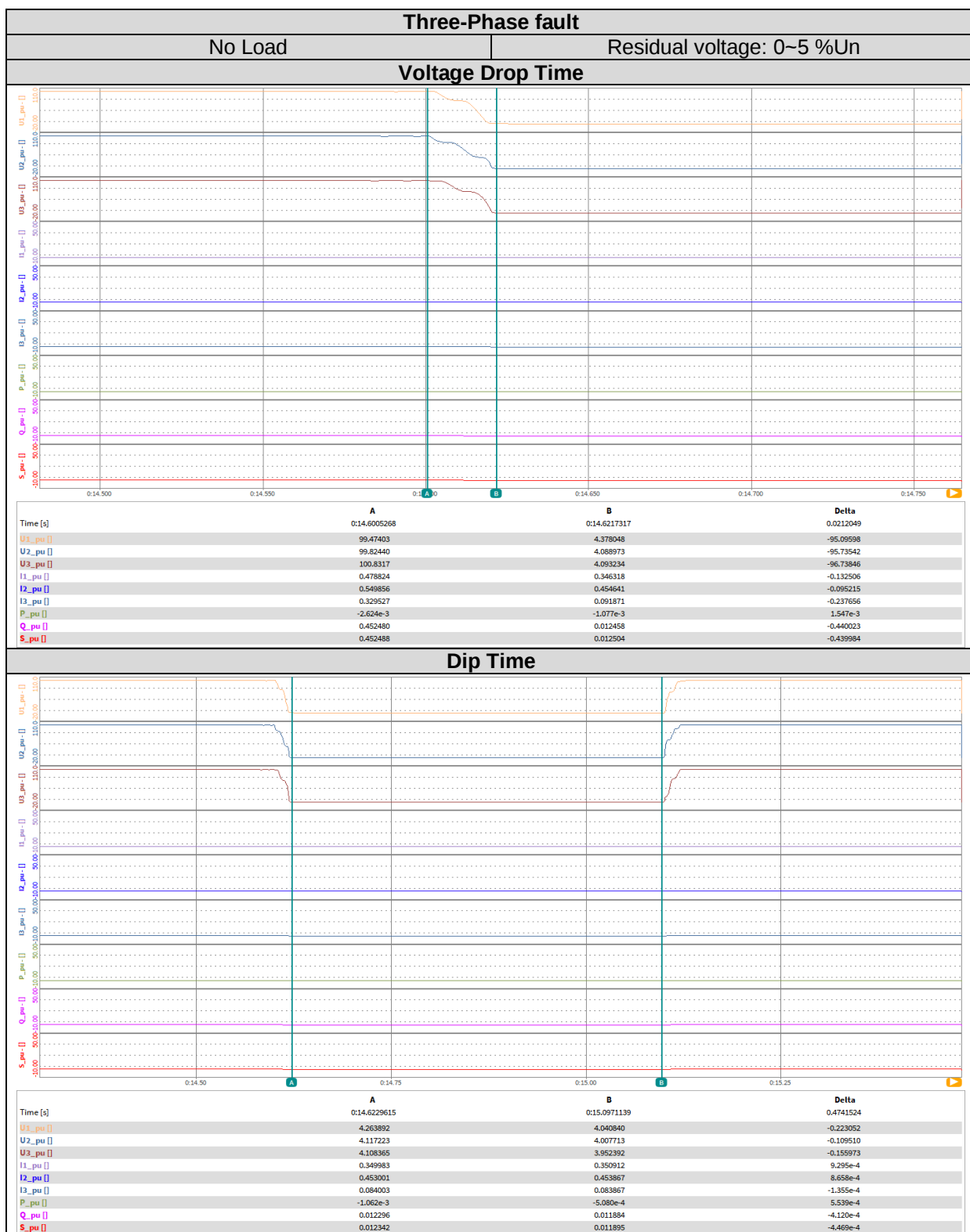
Remark: The virtual neutral line was used in the test.

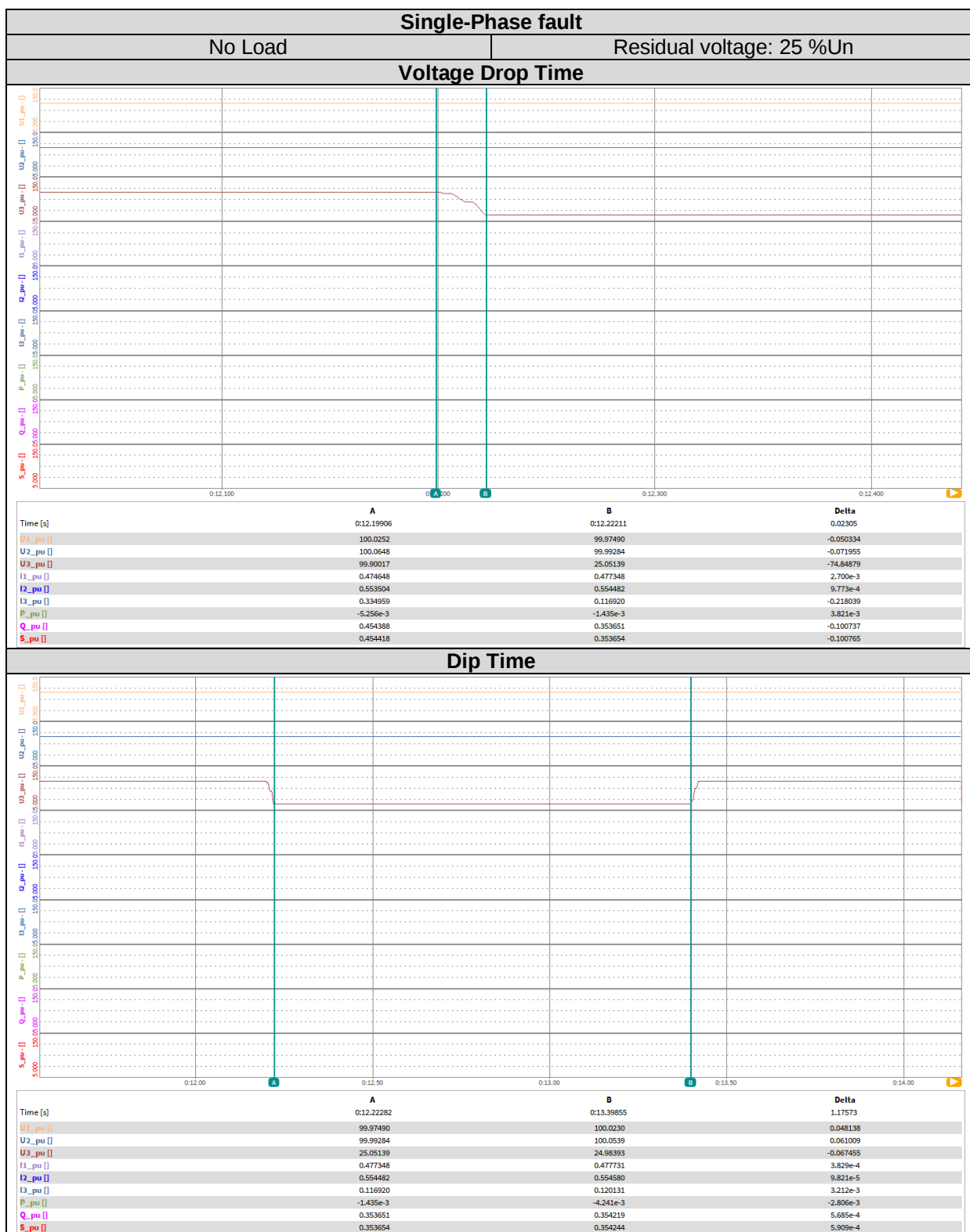
Test results are graphically represented in the following pages.

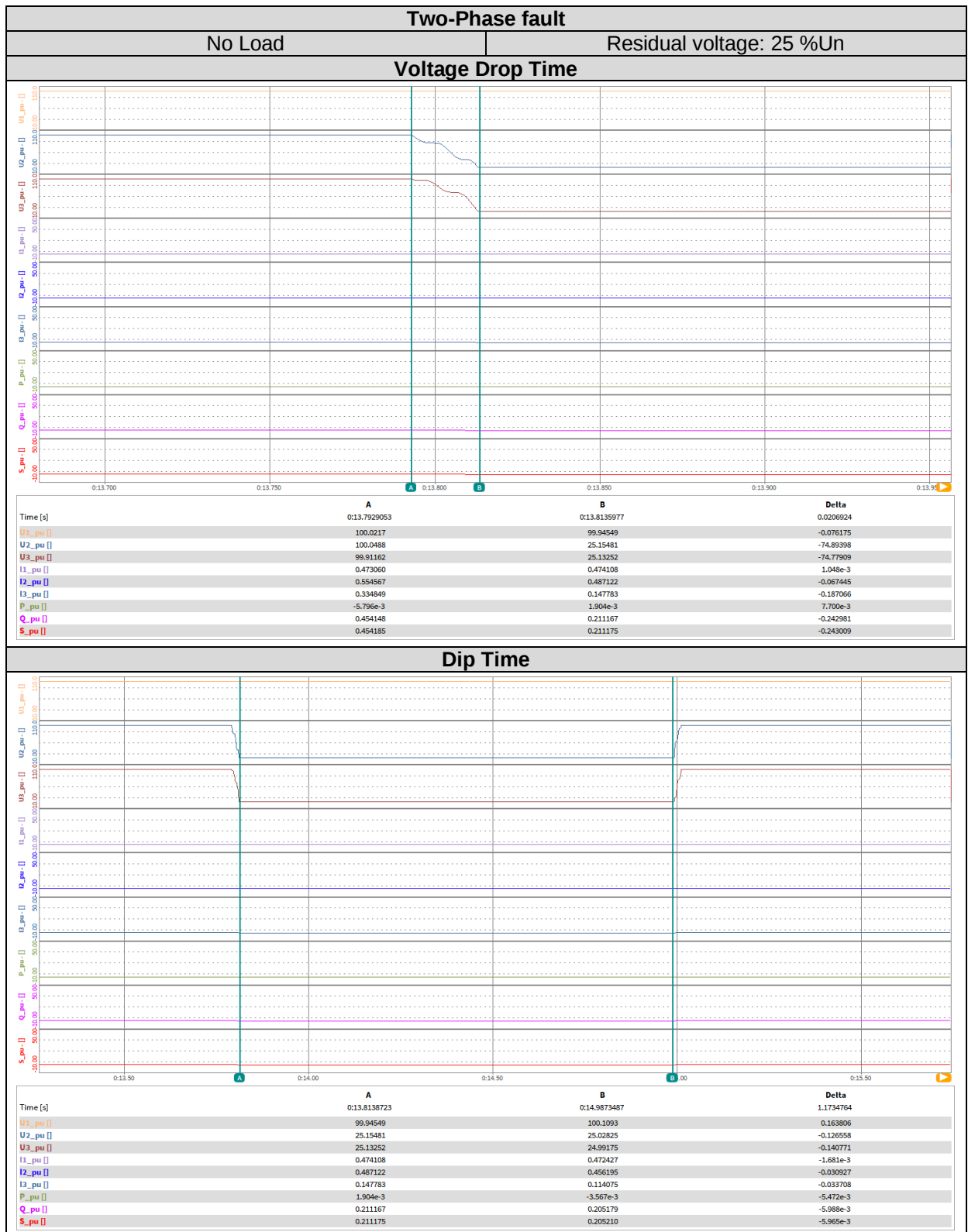
Note: The following test figure the voltage unit of U1_pu, U2_pu and U3_pu are %Un , the current unit of I1_pu, I2_pu and I3_pu are %In and the power unit is %Pn

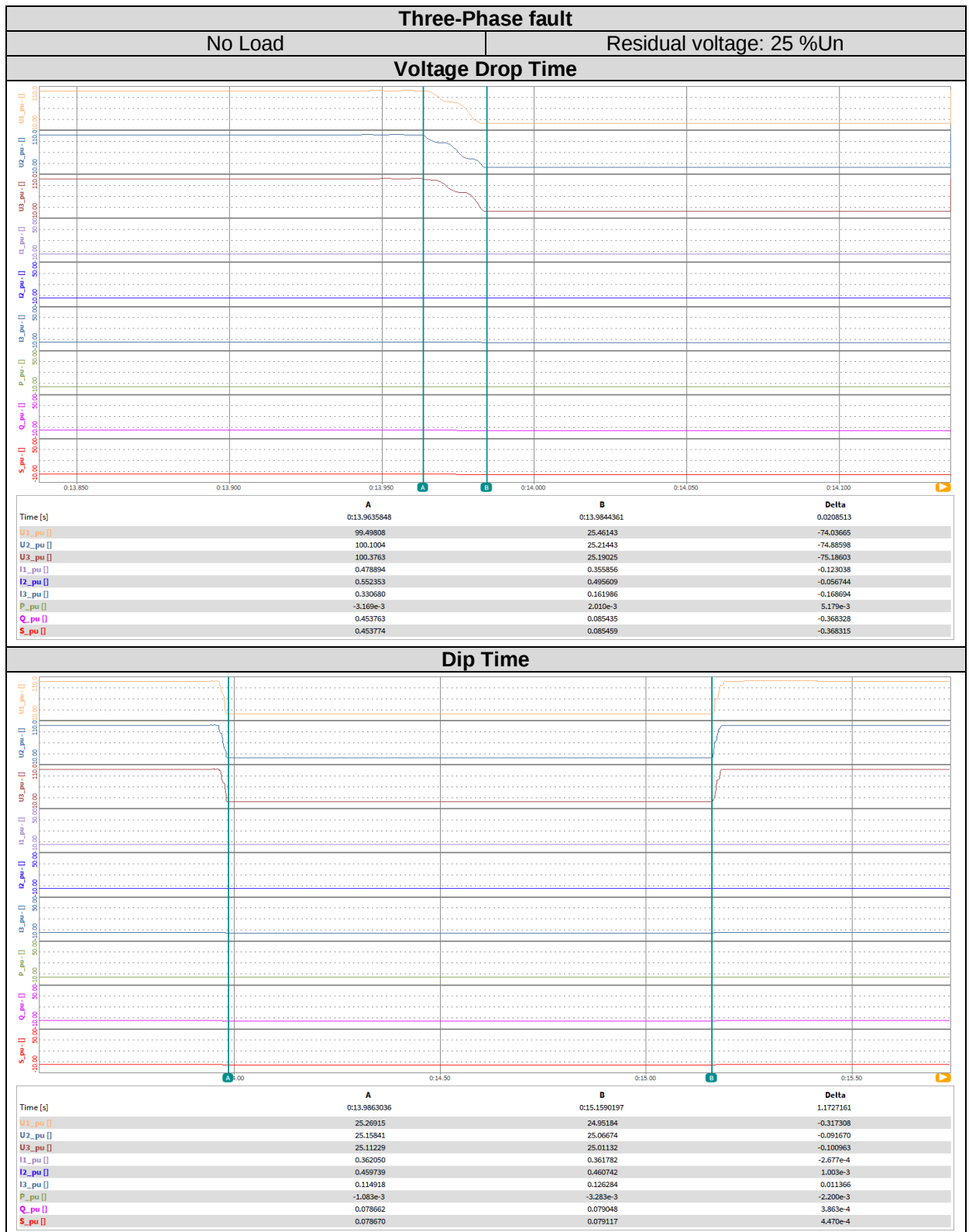


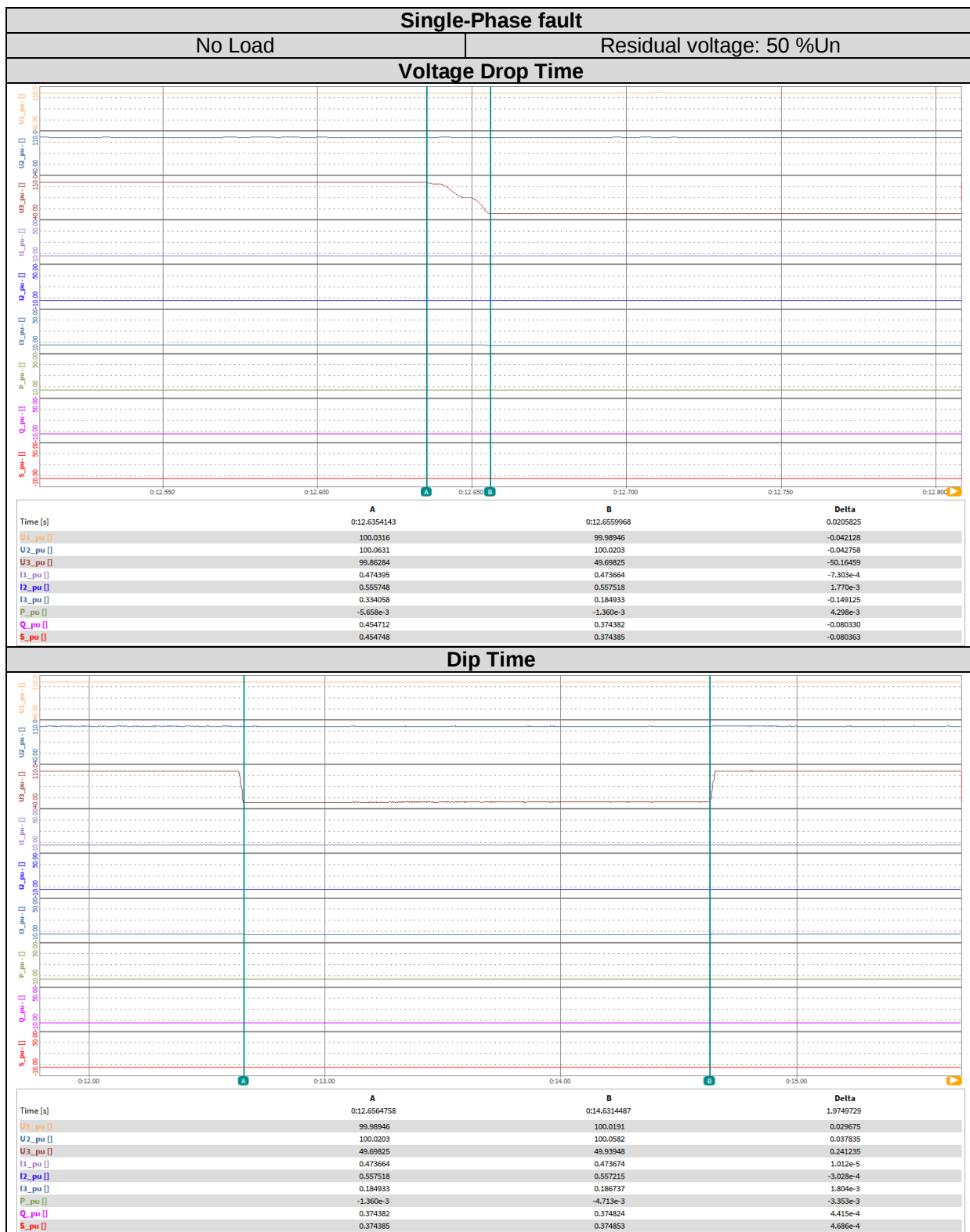


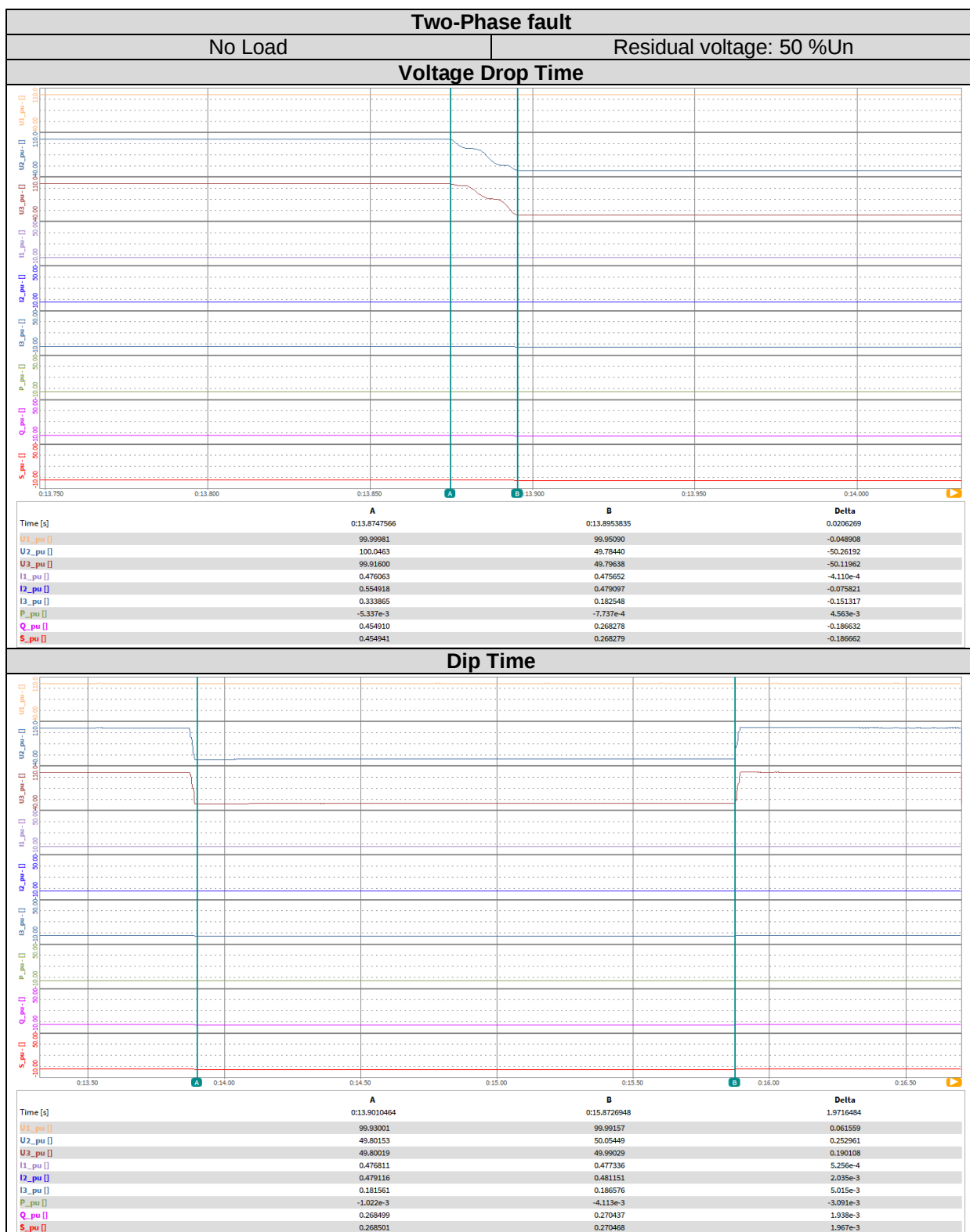


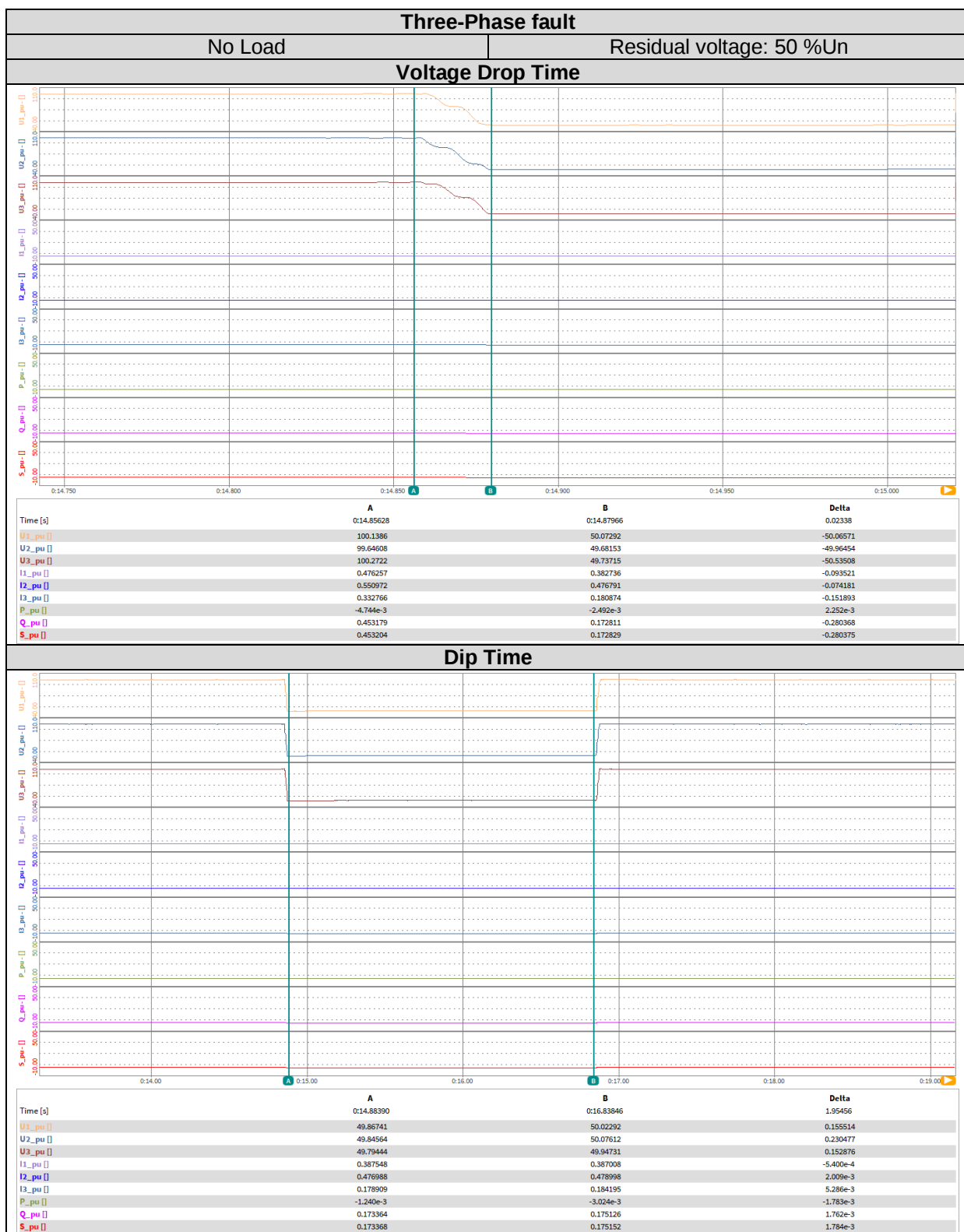


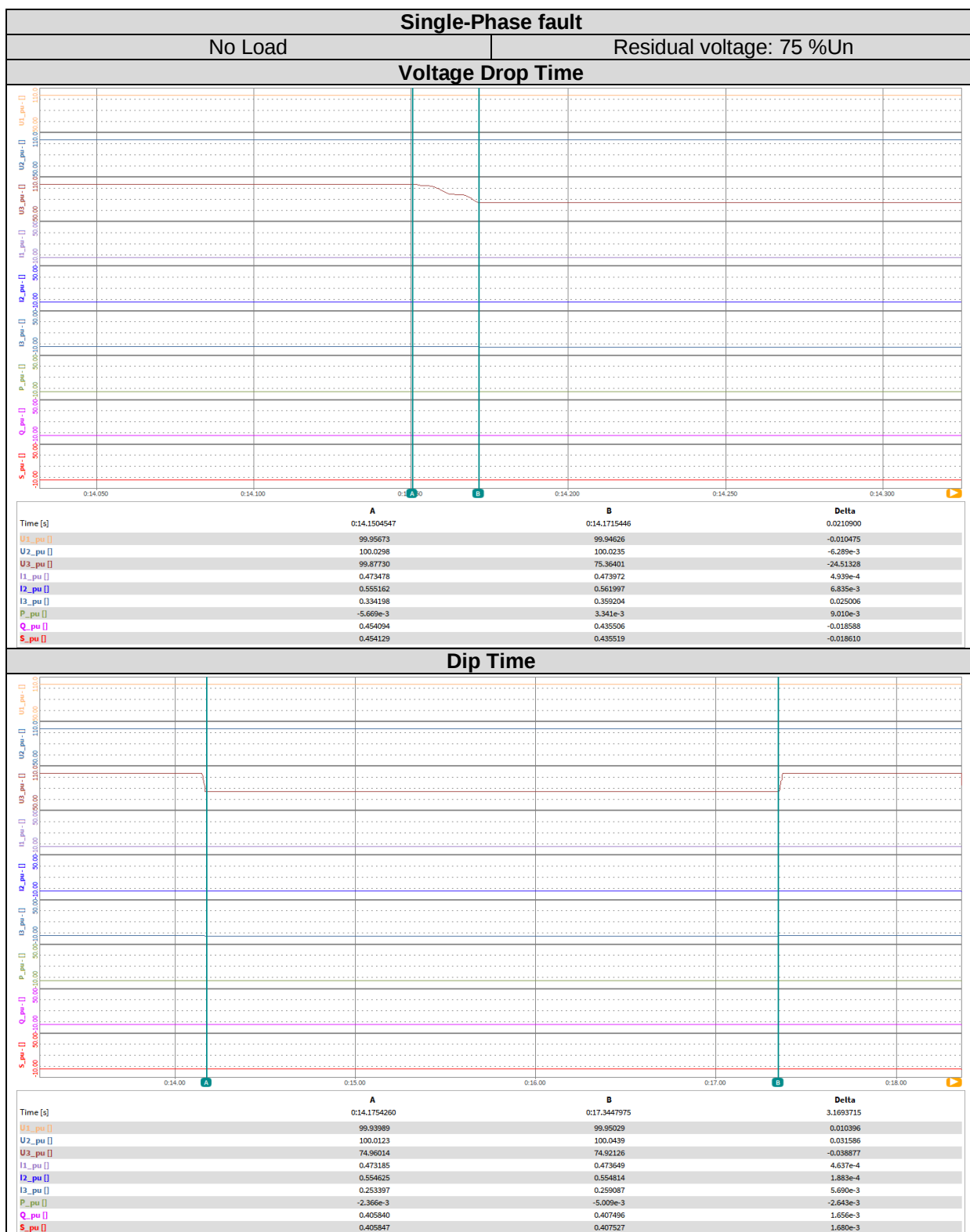


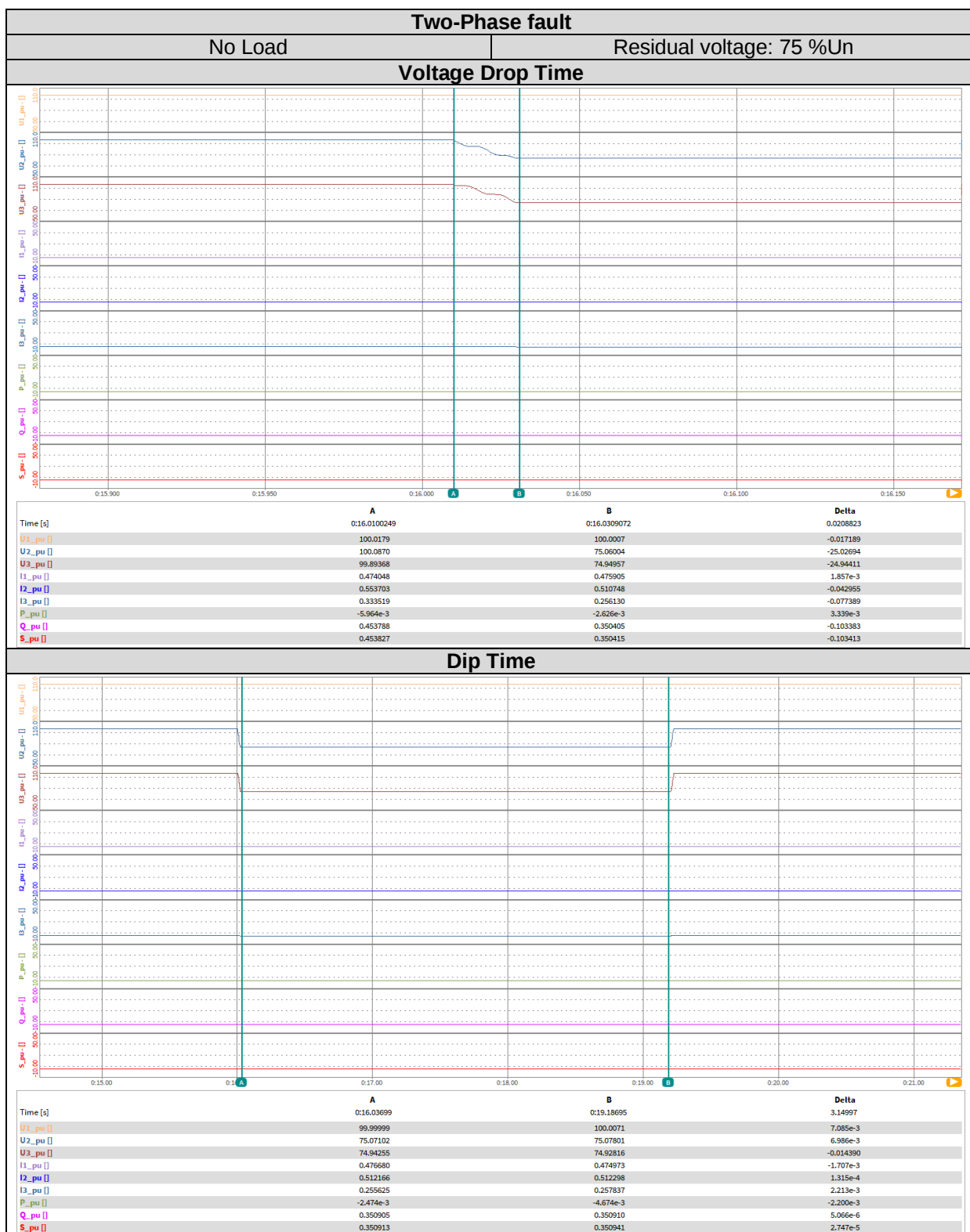


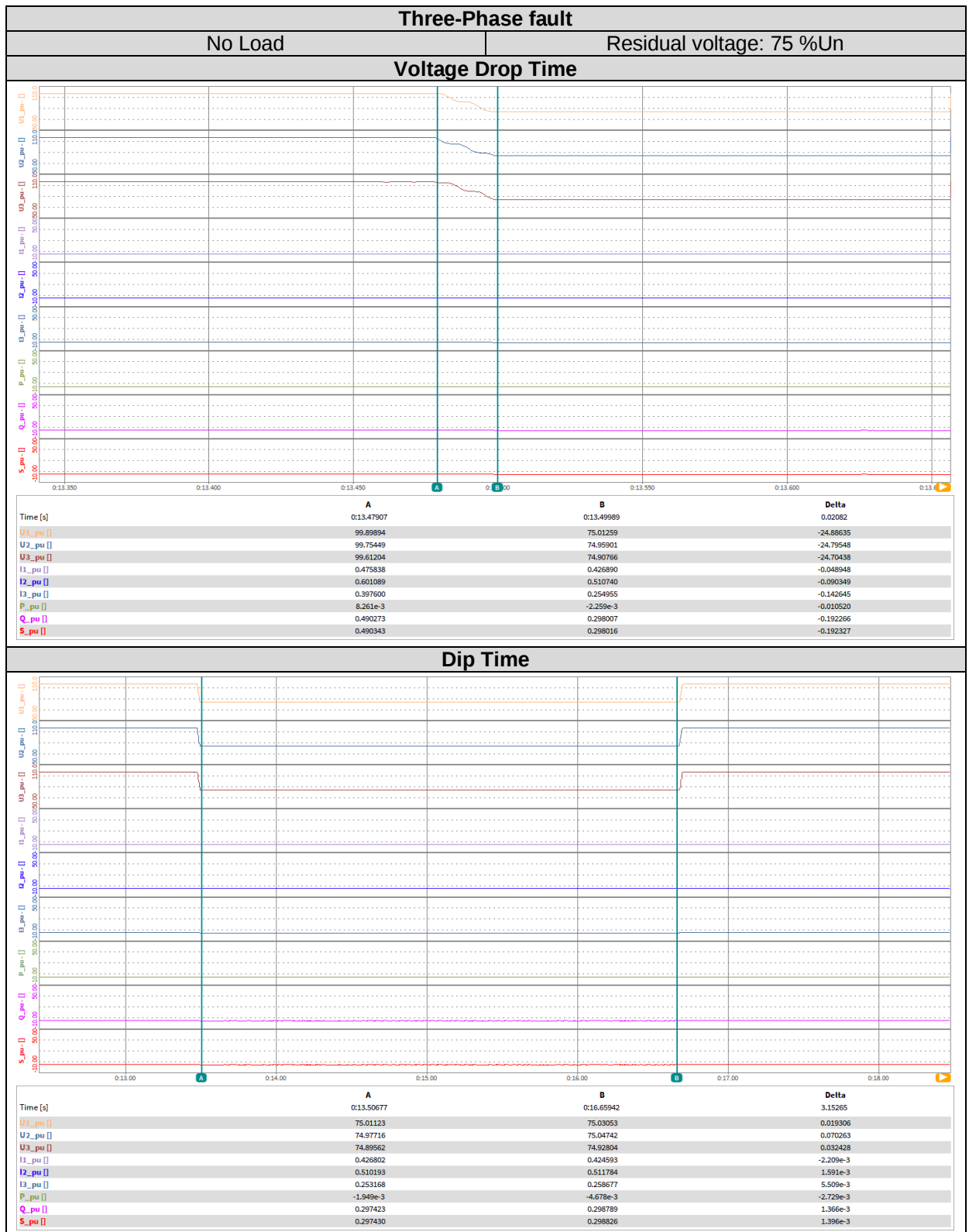












4.2.1.2. Load Tests: Partial load (20 %P_n)

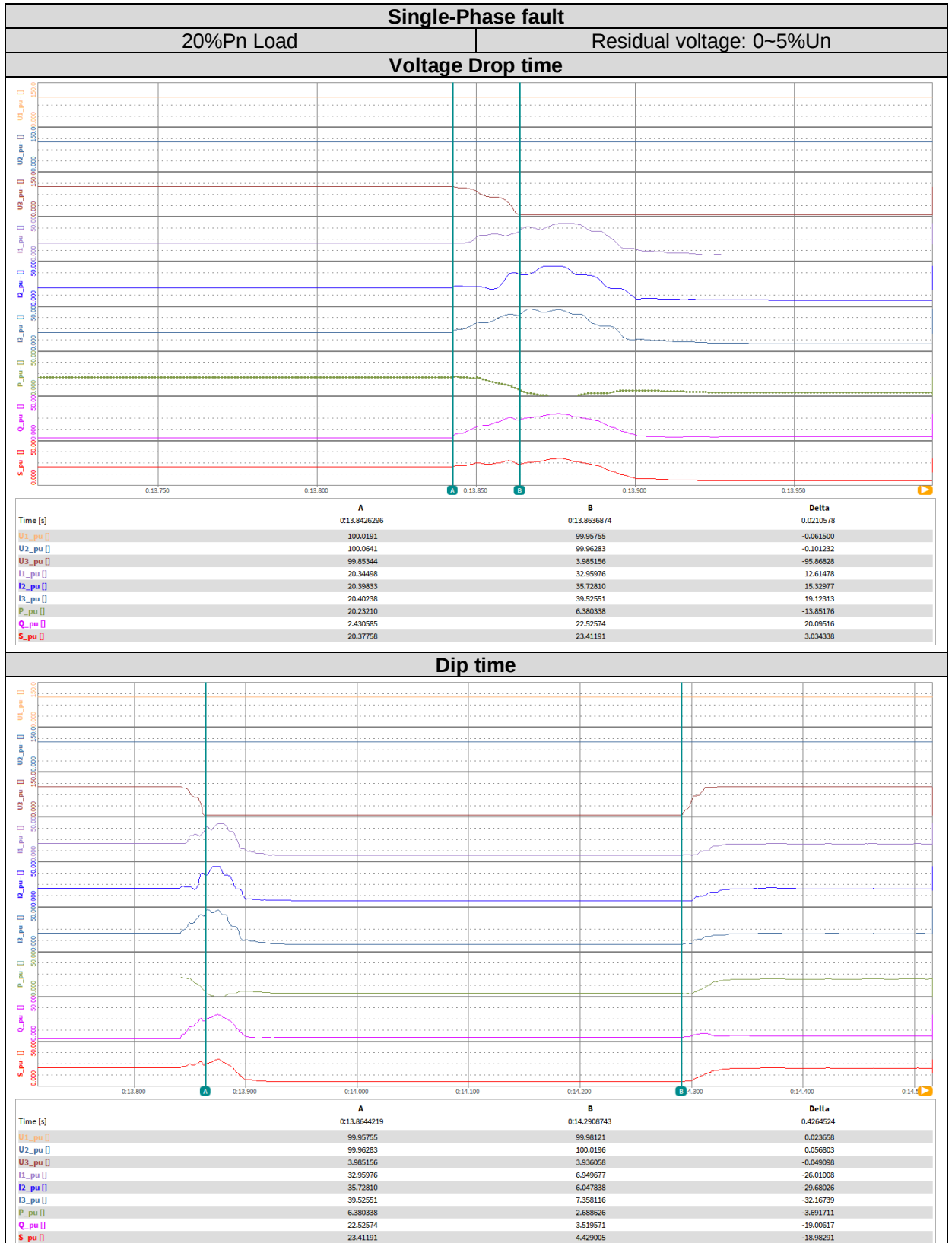
Test results of different no-load cases performed are offered below:

20 %P _n Load							
Phase type	Residual voltage desired (%Un)	Voltage measured (%Un)	Voltage drop time (ms)	Dip time desired (ms)	Dip time measured (ms)	Power recovery time (ms)	Voltage after recovery (%Un)
1 ph	0-5	4.0	21	≥ 400	426	21	100.0
2 ph		4.0	23		424	22	99.9
3 ph		4.2	21		425	13	99.9
1 ph	25	25.0	20	≥ 1000	1071	20	99.9
2 ph		25.0	22		1074	20	100.0
3 ph		25.4	21		1072	27	100.0
1 ph	50	49.8	21	≥ 1850	1967	5	99.9
2 ph		49.8	22		1966	20	100.0
3 ph		49.8	23		1970	15	100.0
1 ph	75	74.9	20	≥ 3000	3062	16	99.9
2 ph		75.0	21		3073	11	100.0
3 ph		75.4	20		3074	20	100.0

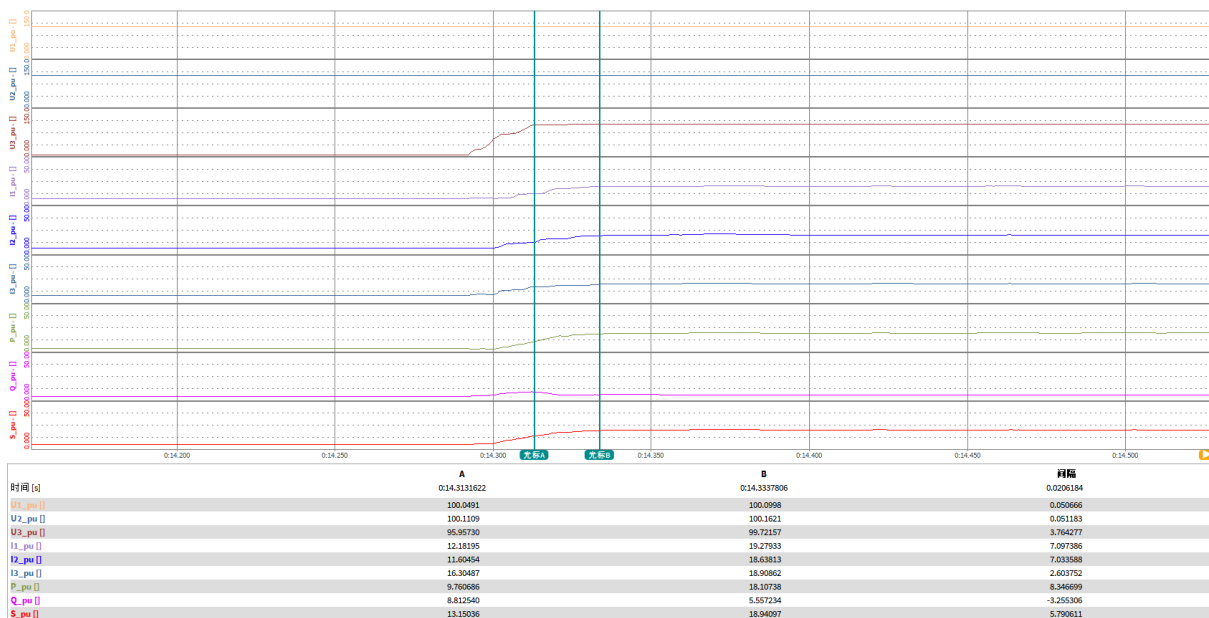
Remark: The virtual neutral line was used in the test.

Test results are graphically represented at following pages.

Note: The following test figure the voltage unit of U1_pu, U2_pu and U3_pu are %Un , the current unit of I1_pu, I2_pu and I3_pu are %In and the power unit is %Pn



Power Recovery time

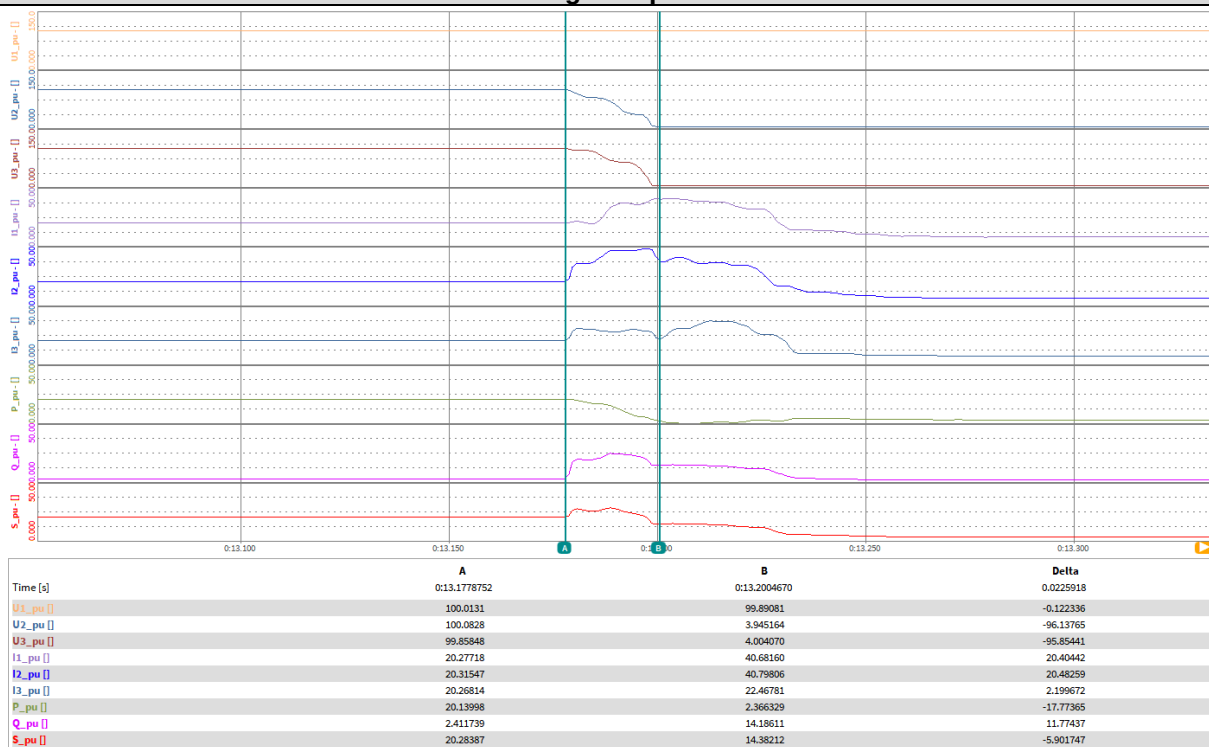


Two-Phase fault

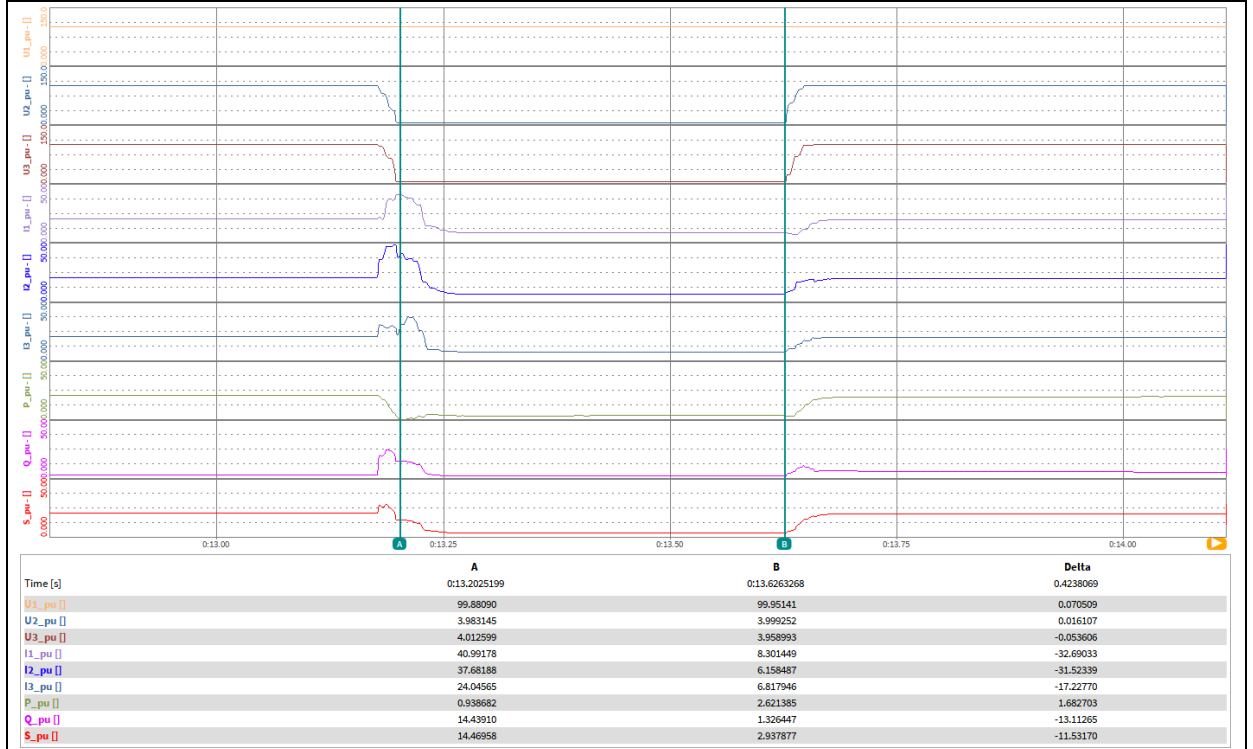
20%Pn Load

Residual voltage: 0~5%Un

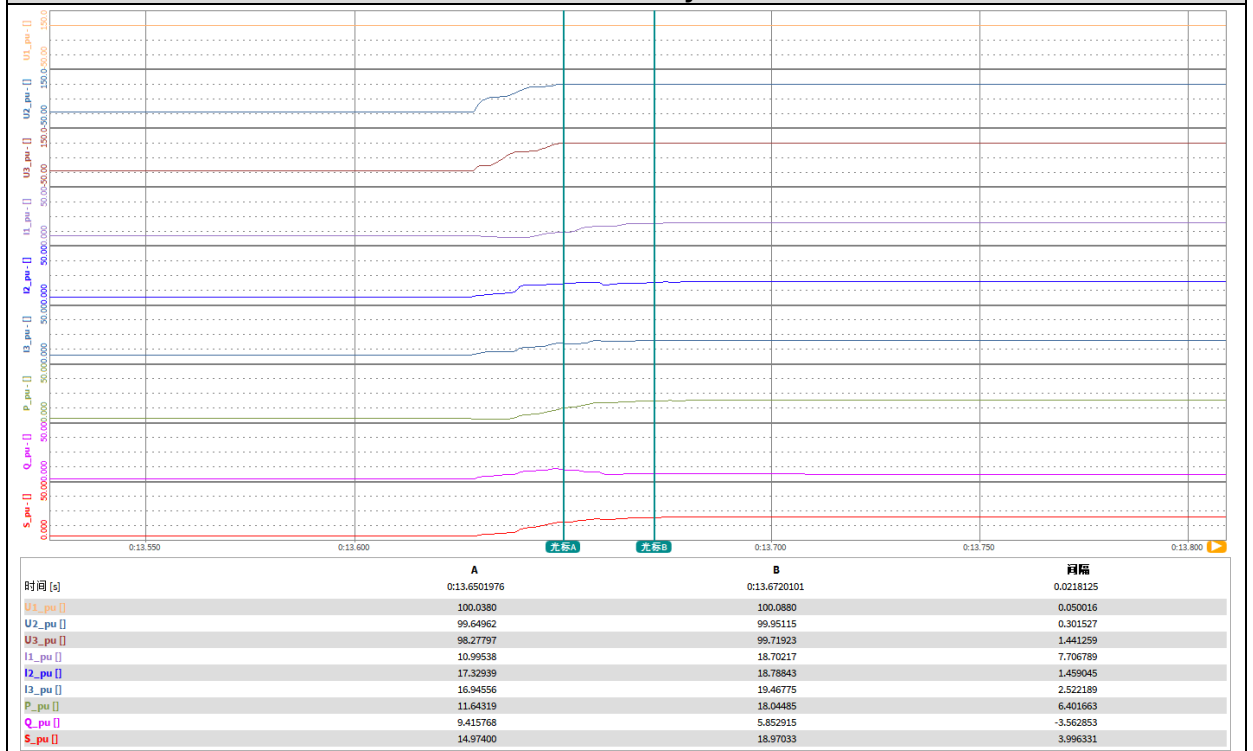
Voltage Drop time

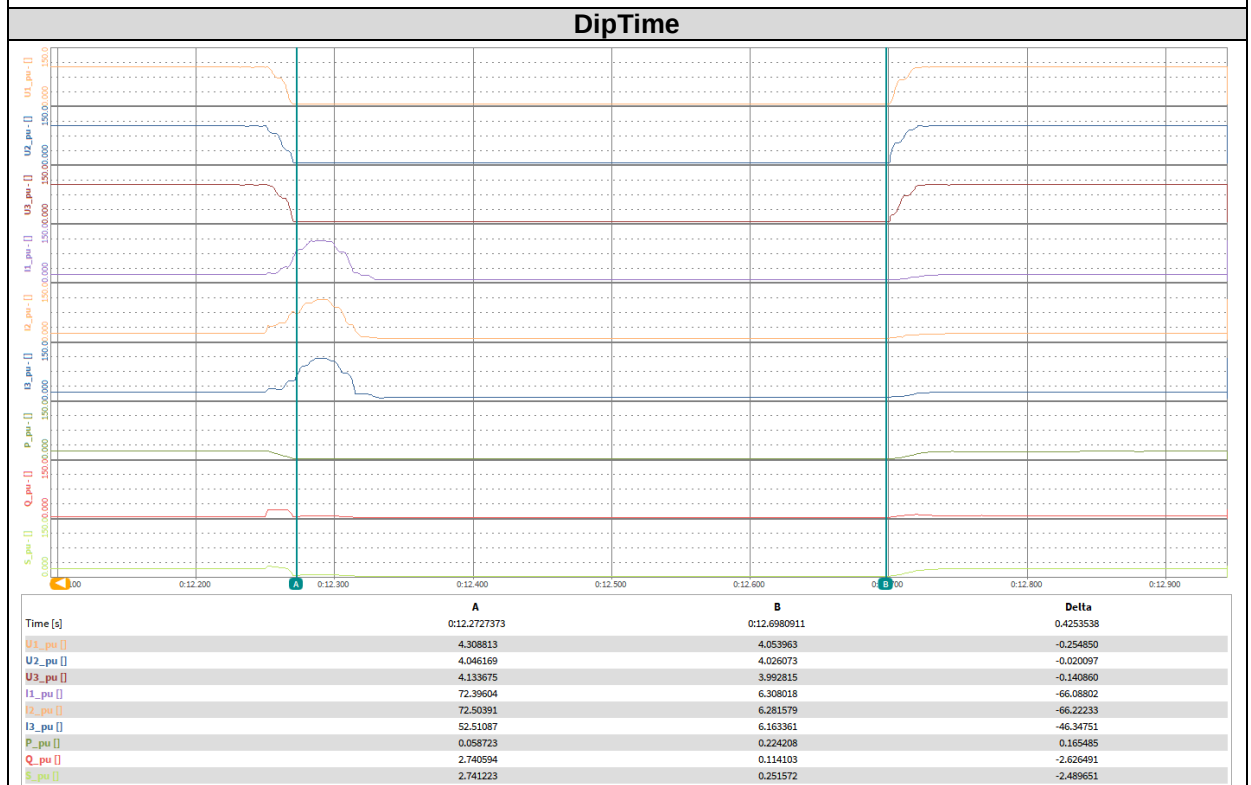
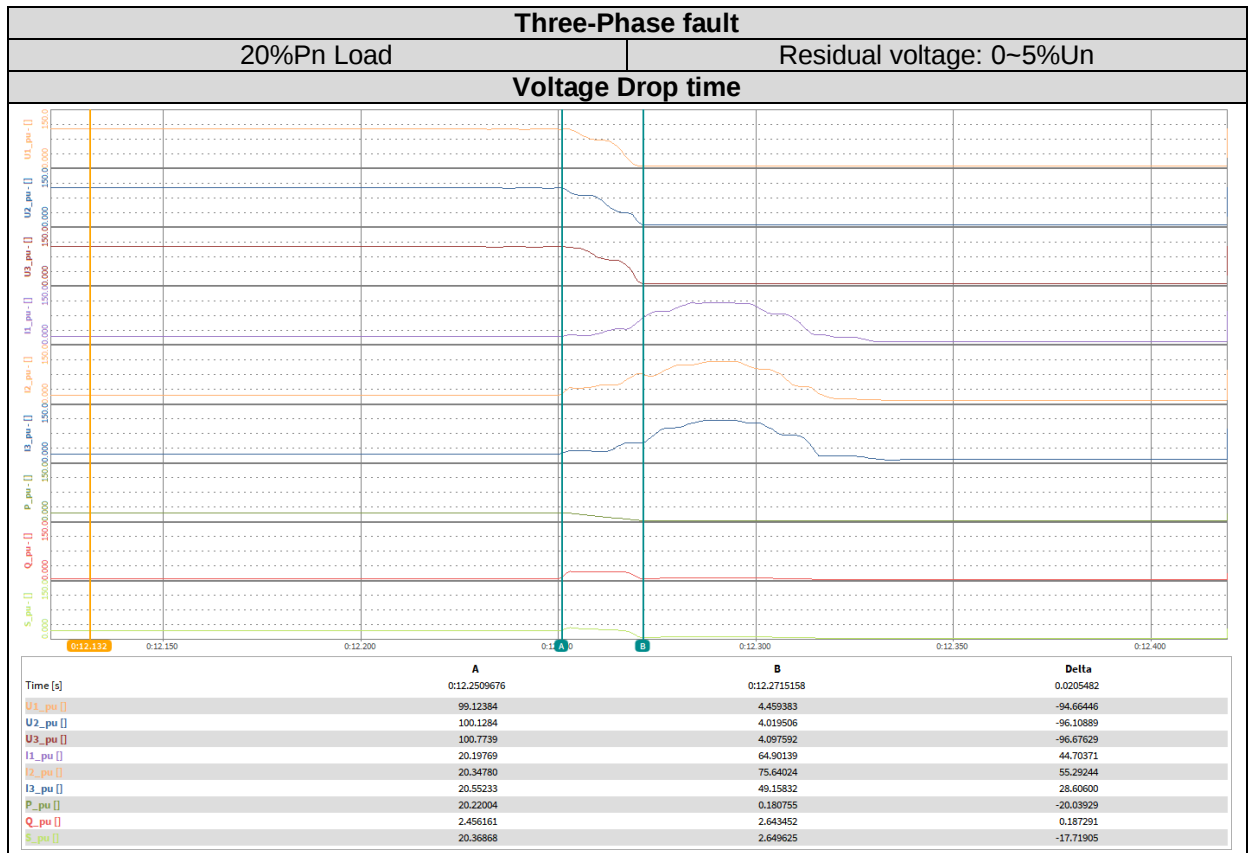


DipTime

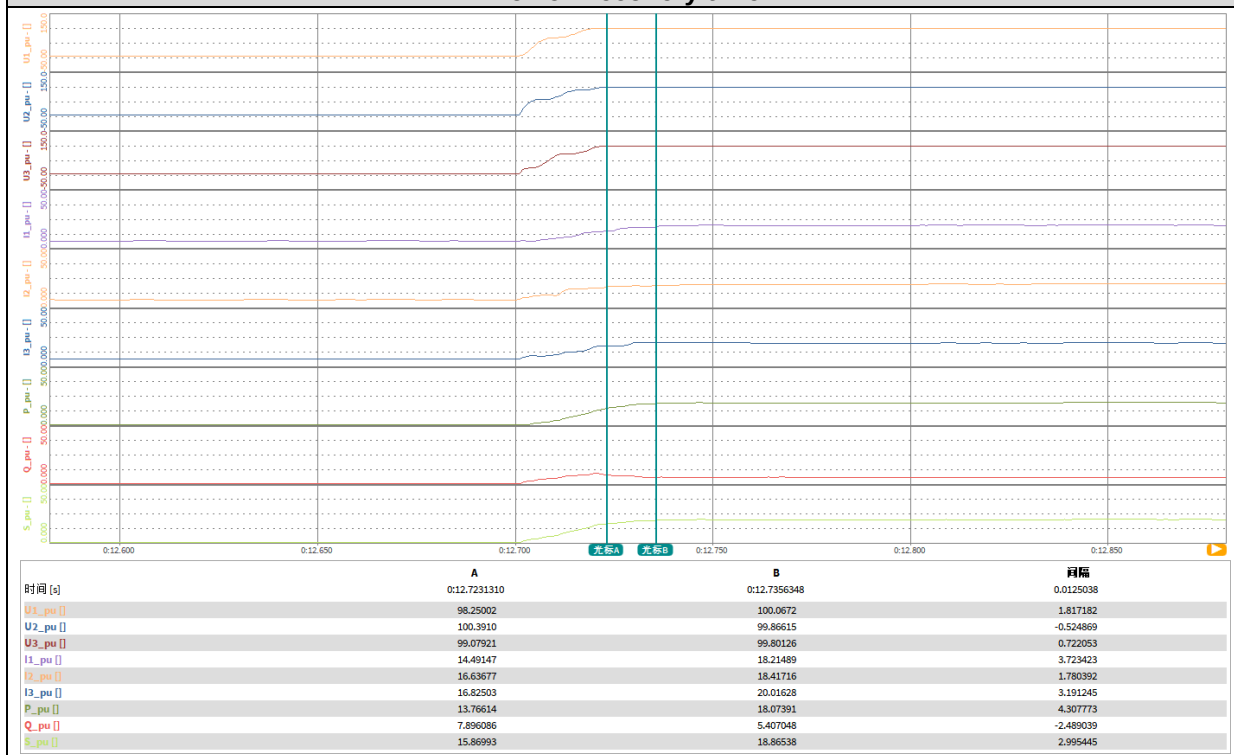


Power Recovery time





Power Recovery time

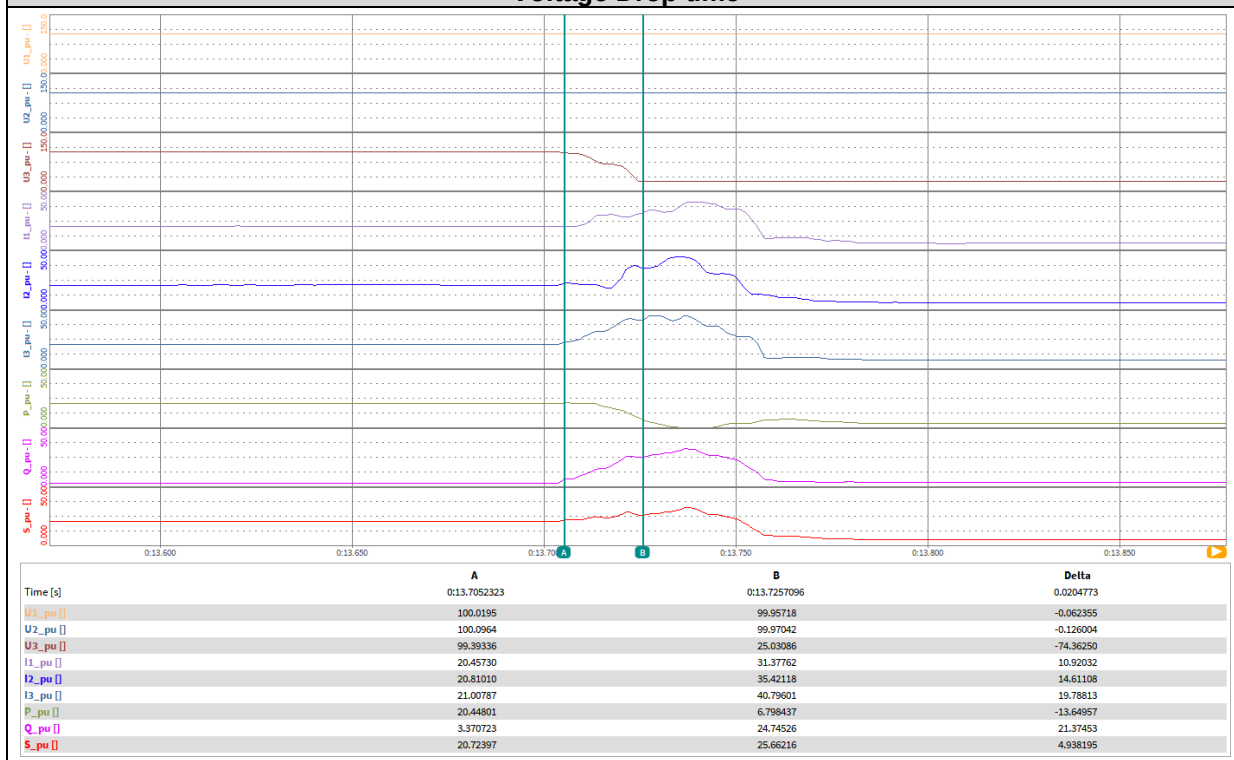


Single-Phase fault

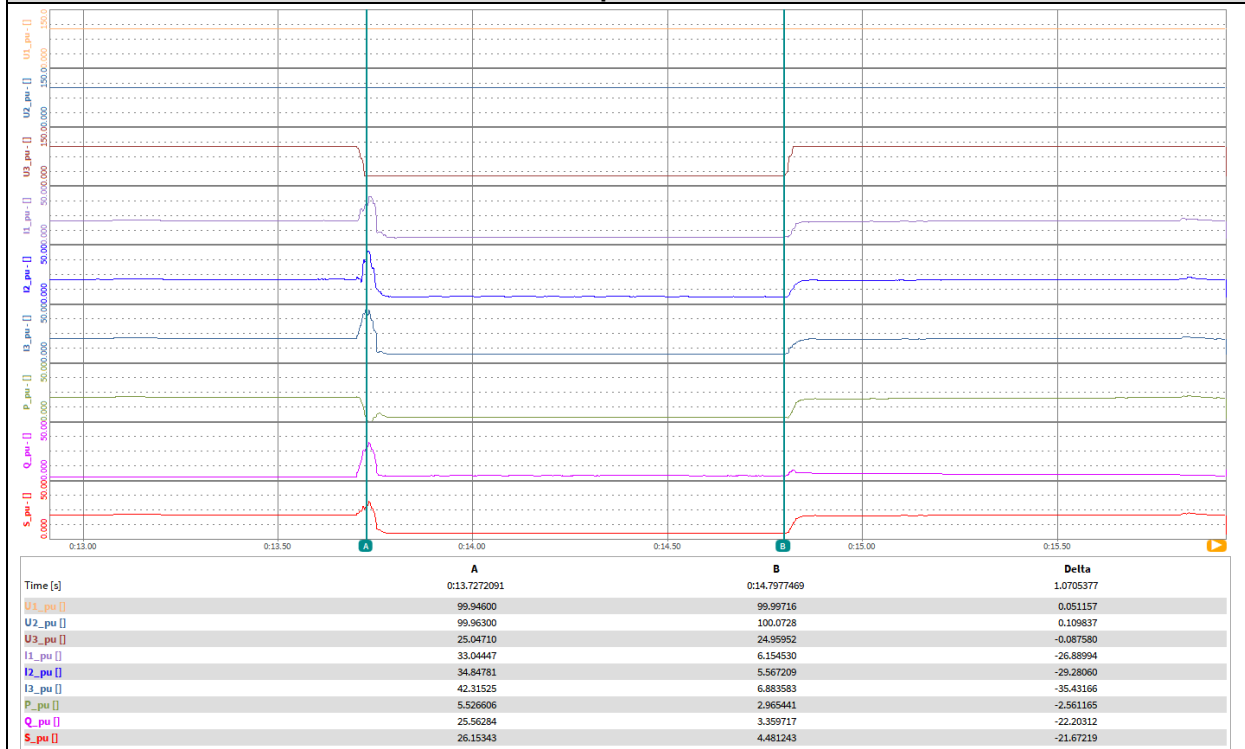
20%Pn Load

Residual voltage: 25%Un

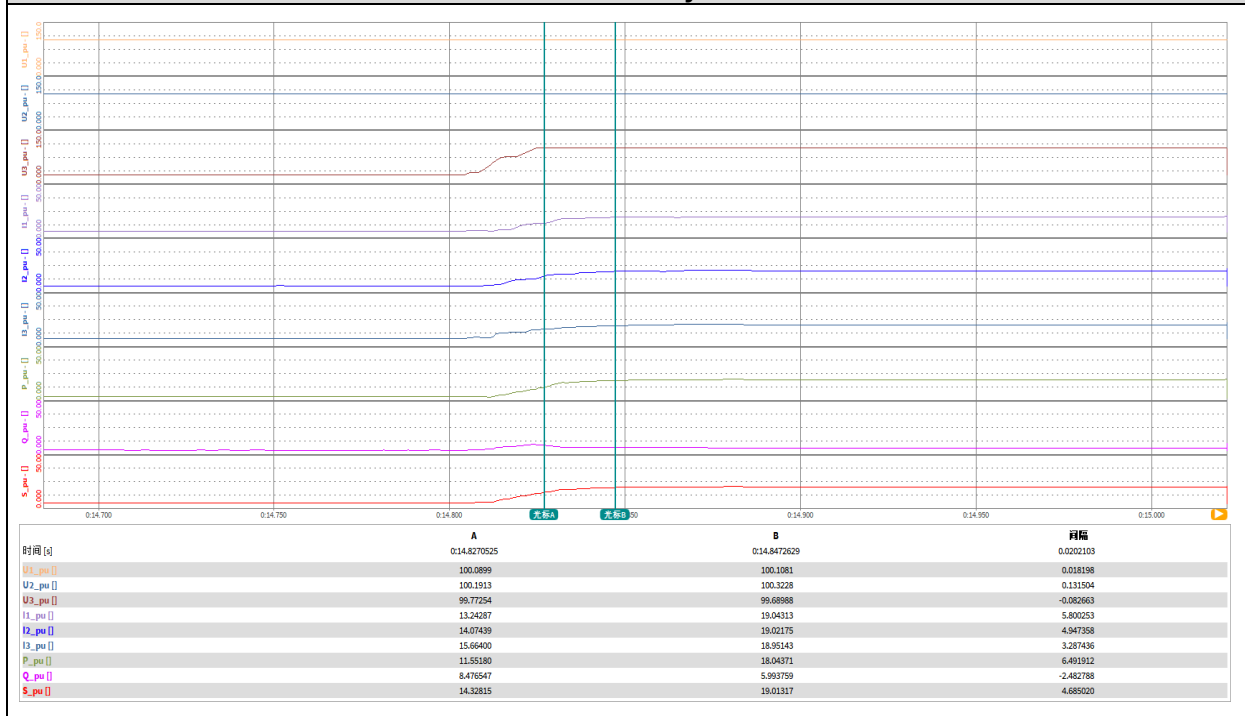
Voltage Drop time

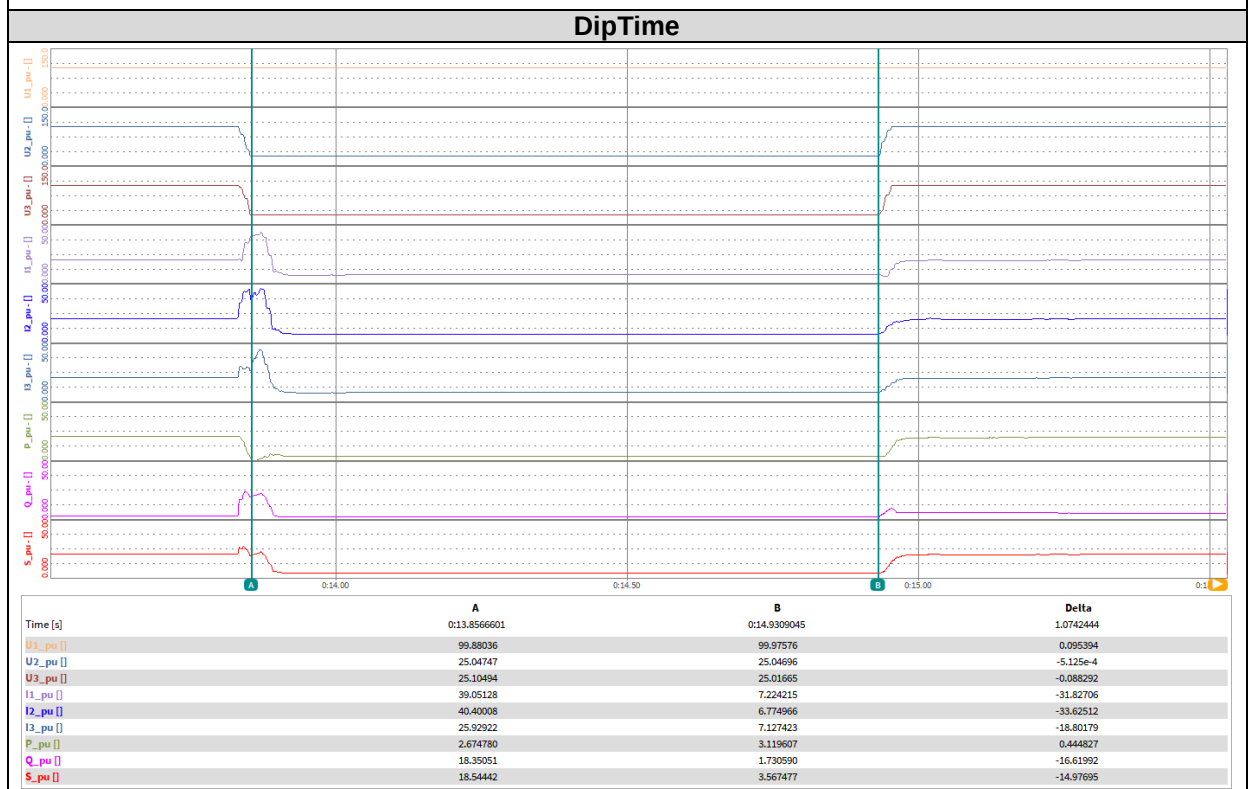
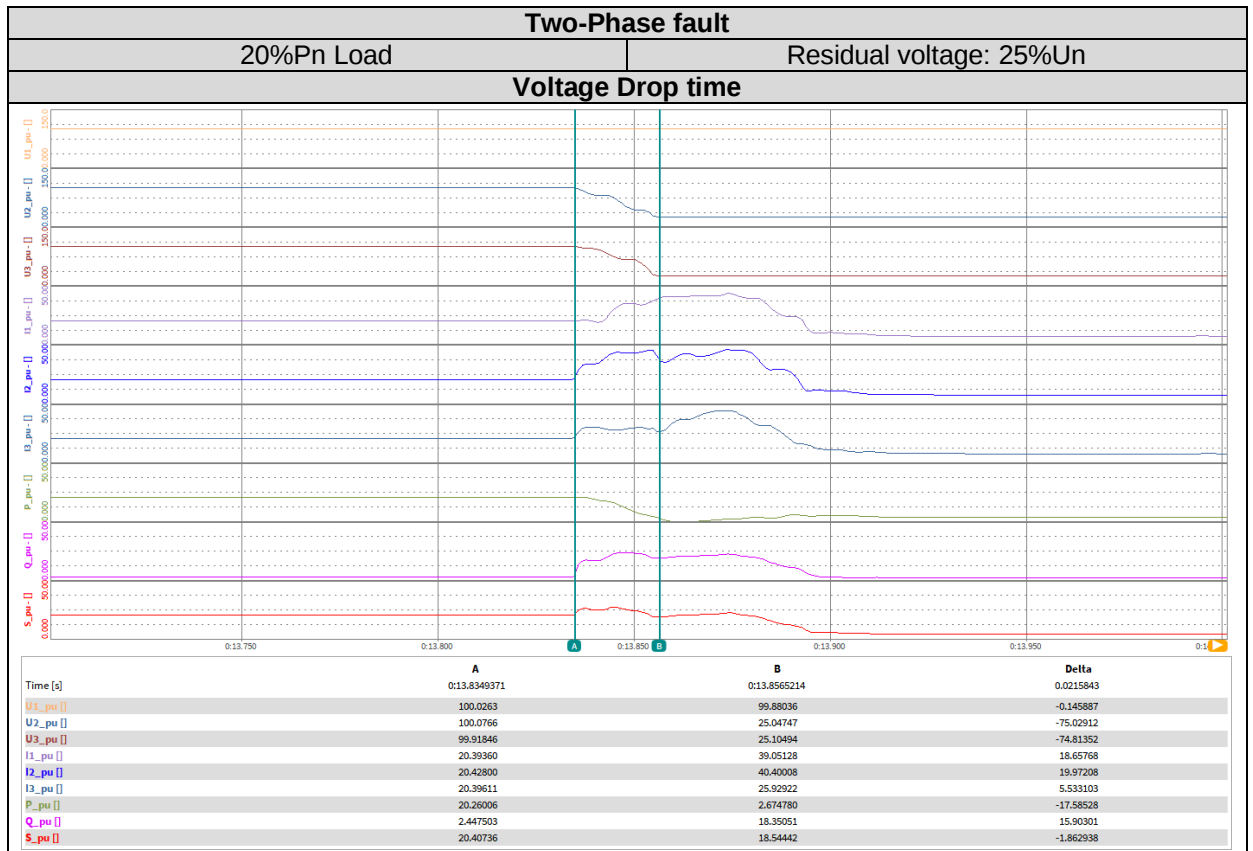


DipTime

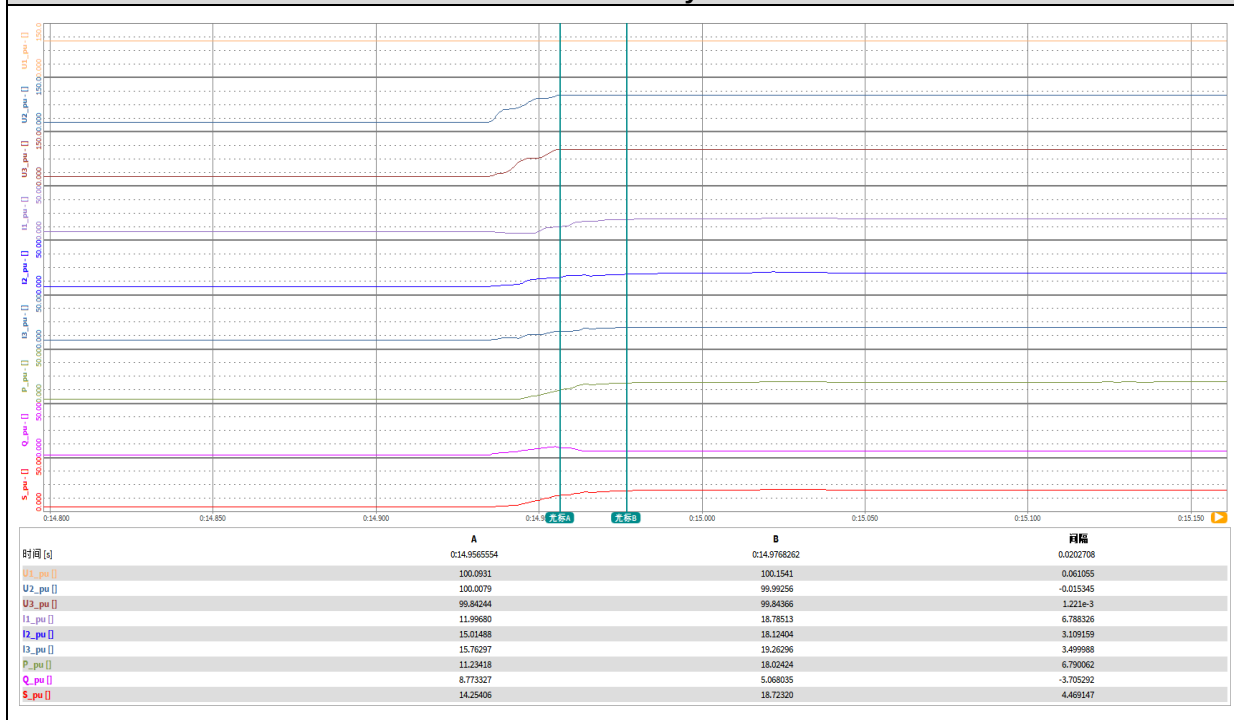


Power Recovery time





Power Recovery time

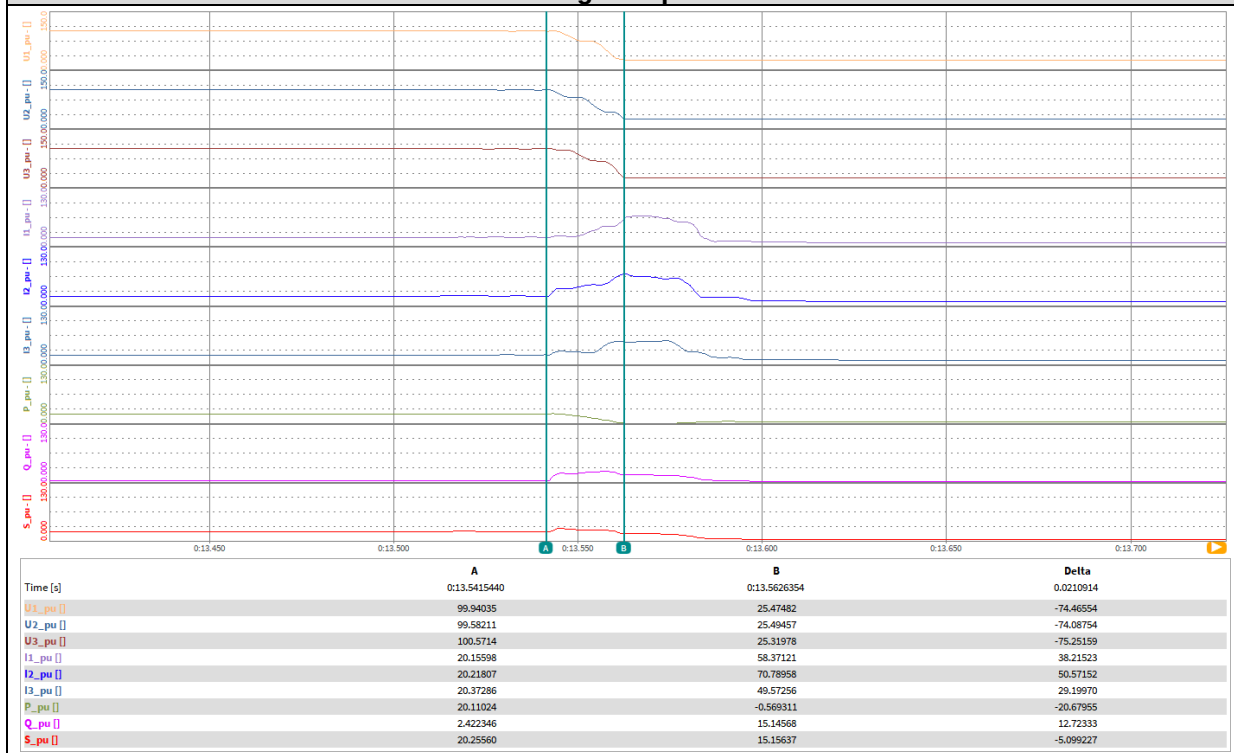


Three-Phase fault

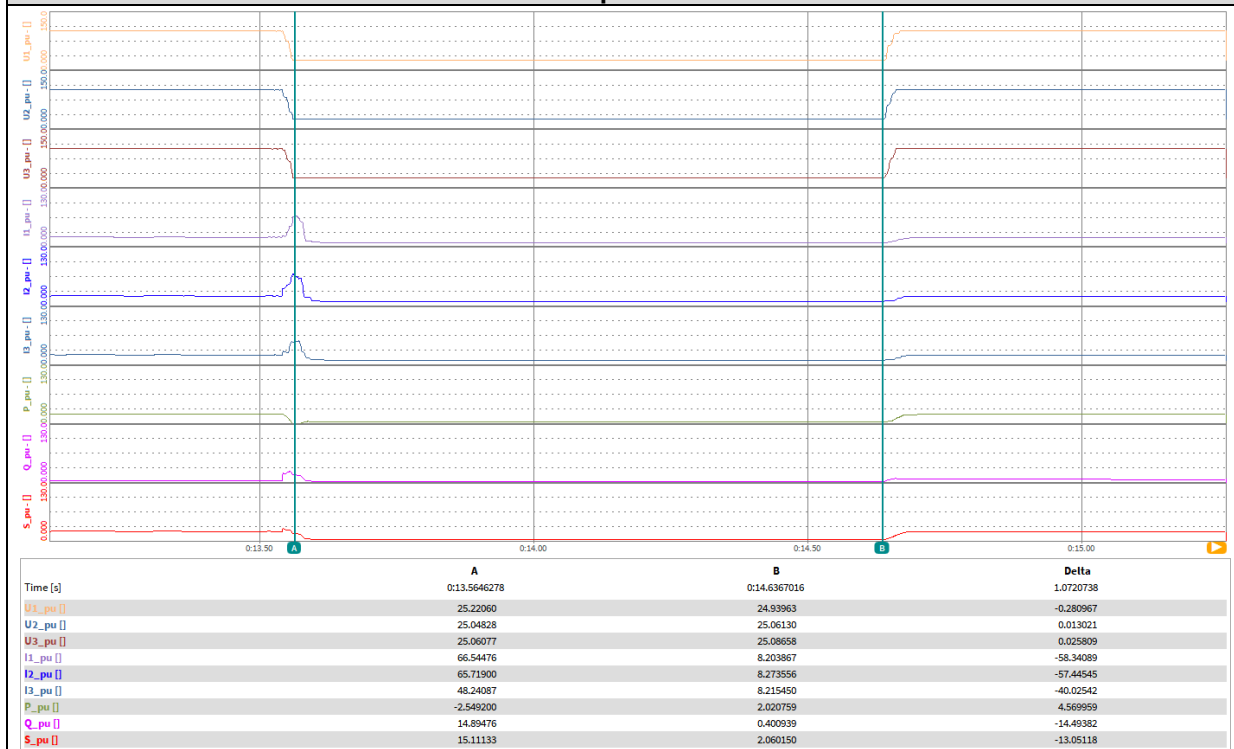
20%Pn Load

Residual voltage: 25%Un

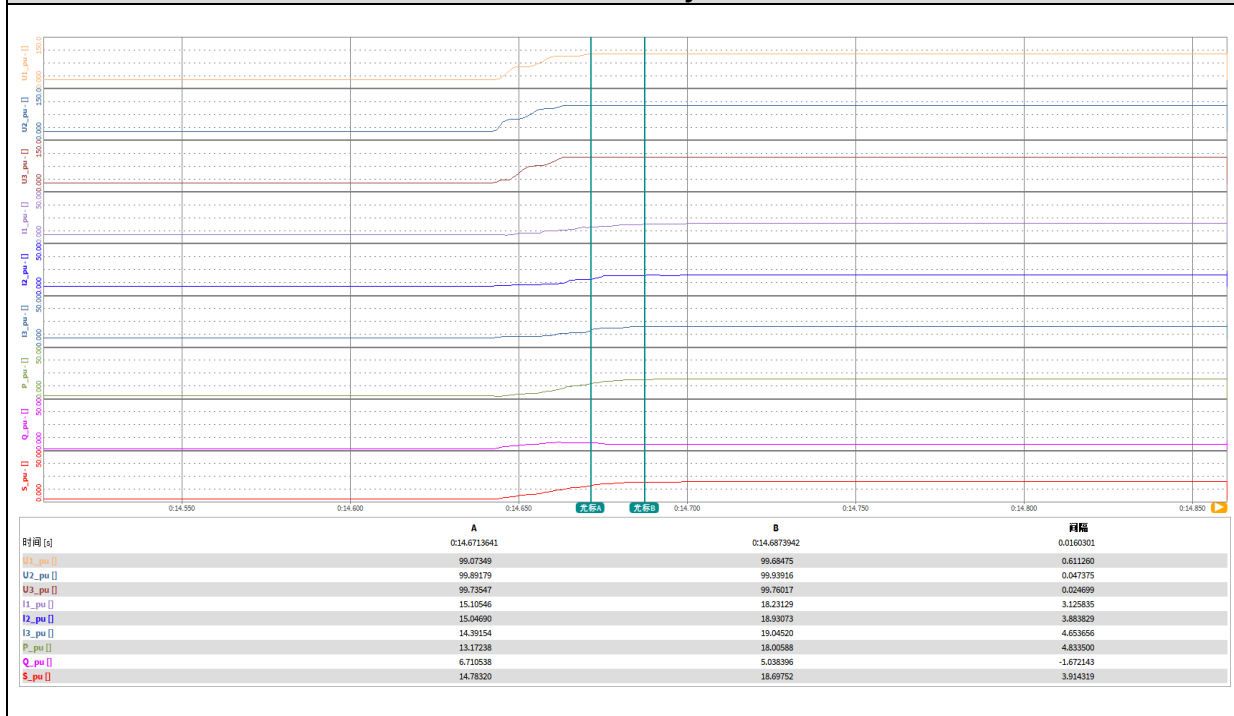
Voltage Drop time

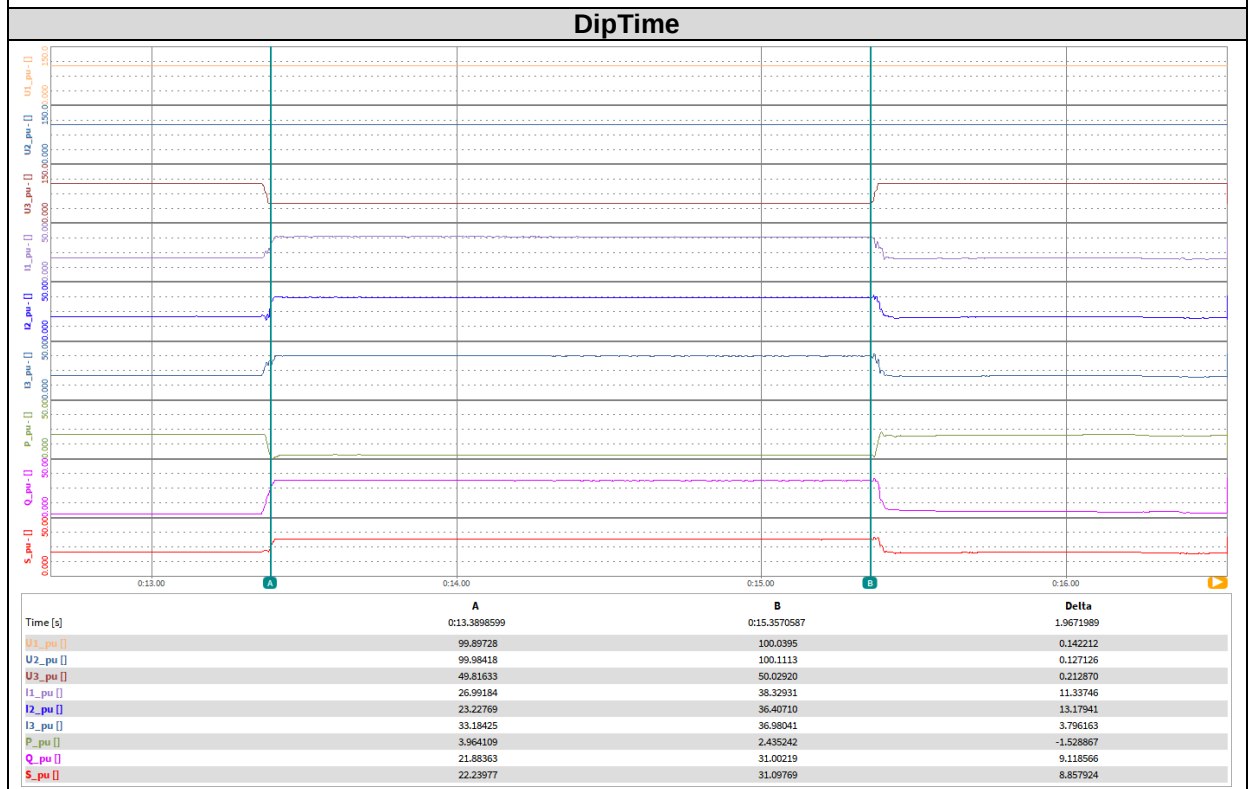
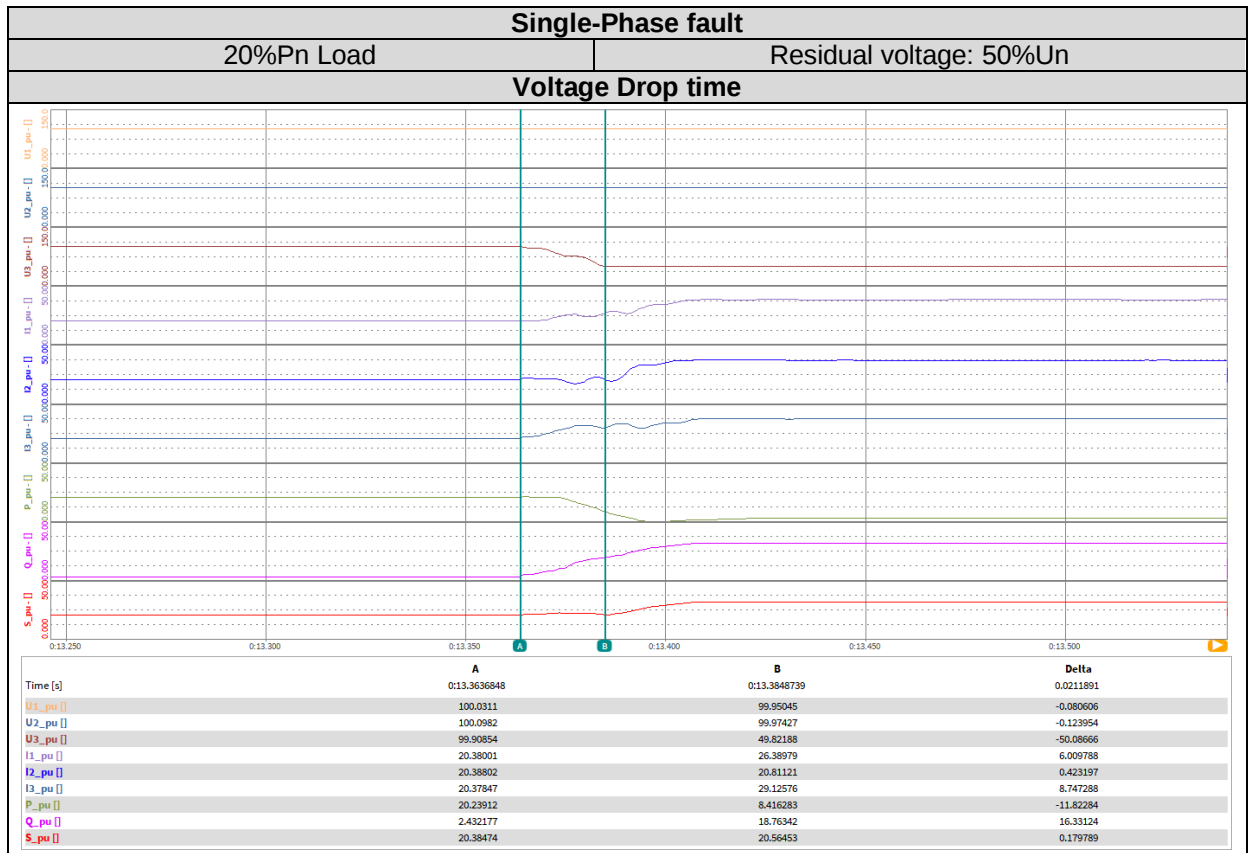


DipTime

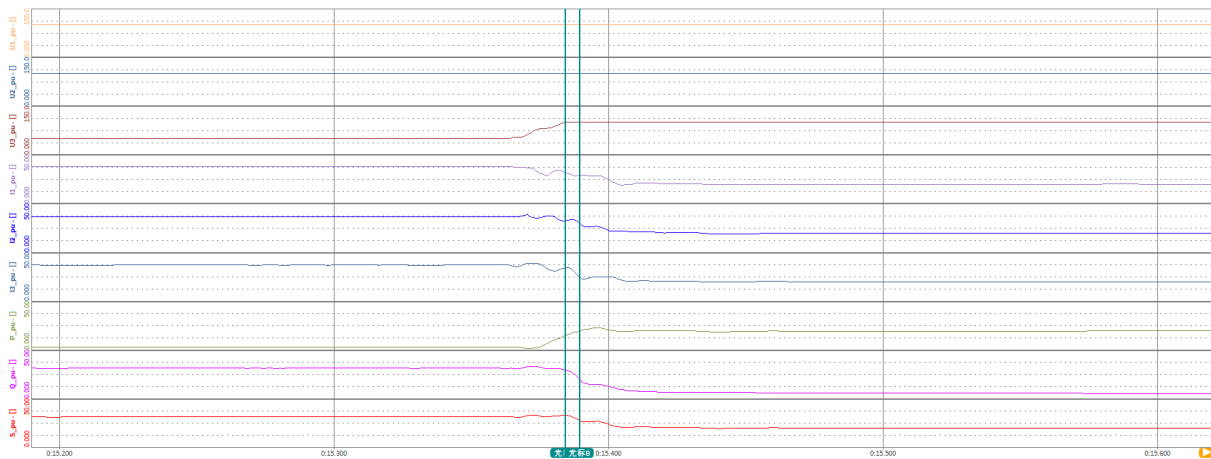


Power Recovery time





Power Recovery time



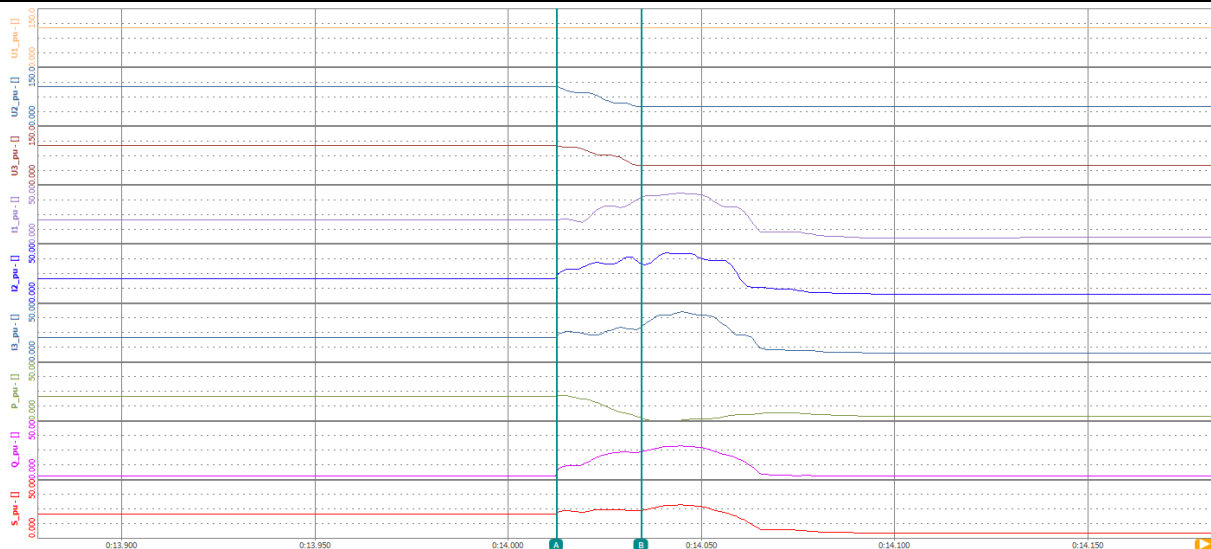
时间 [s]	A	B	间隔
U1_pu []	0.15.3841352	0.15.3894065	0.0052713
U2_pu []	100.1242	100.1270	2.773e-3
U3_pu []	100.2191	100.2251	6.017e-3
I1_pu []	97.70183	100.1400	2.438175
I2_pu []	32.58876	28.42650	-4.162265
I3_pu []	32.41364	32.39645	-0.017191
P_pu []	33.54591	27.84300	-5.702910
Q_pu []	13.53410	18.22367	4.689566
S_pu []	29.69041	23.33095	-6.359455
S_pu []	32.62962	29.60465	-3.024971

Two-Phase fault

20%Pn Load

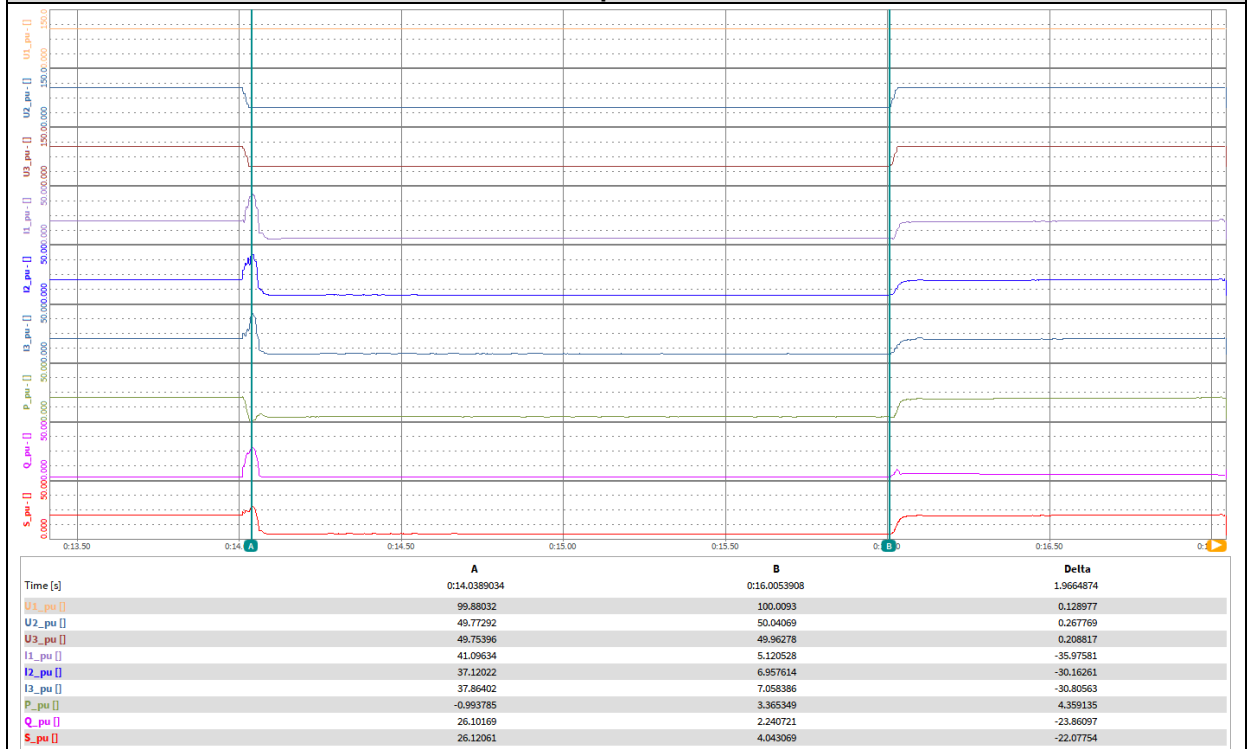
Residual voltage: 50%Un

Voltage Drop time

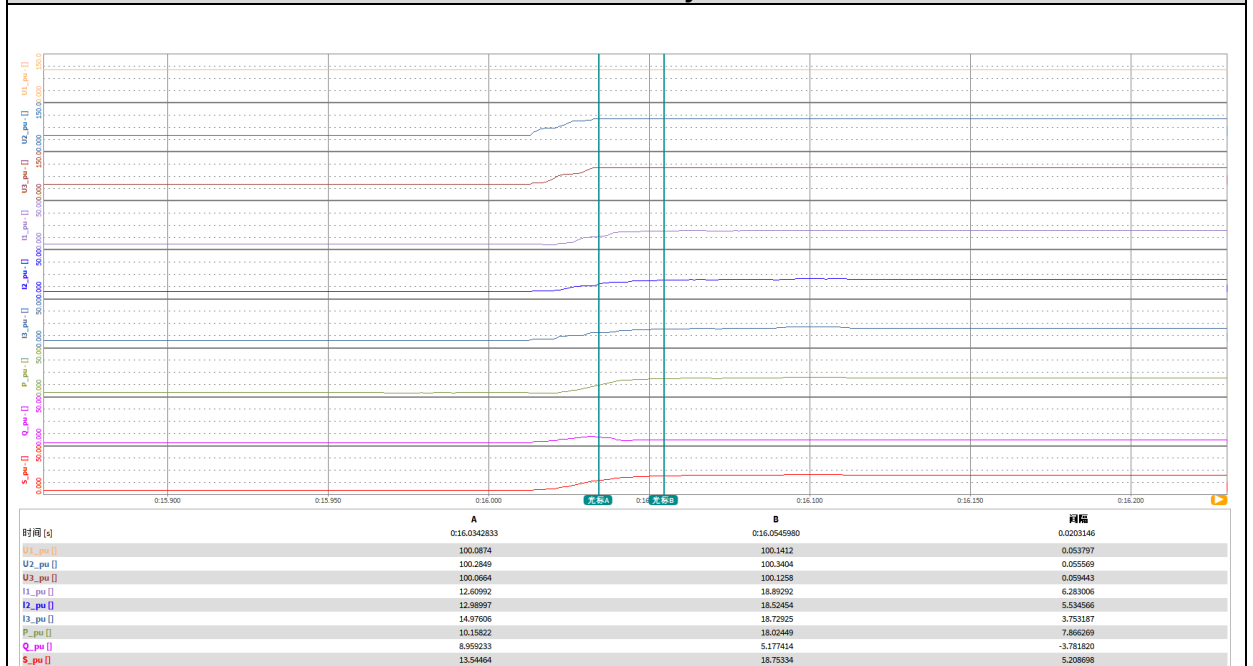


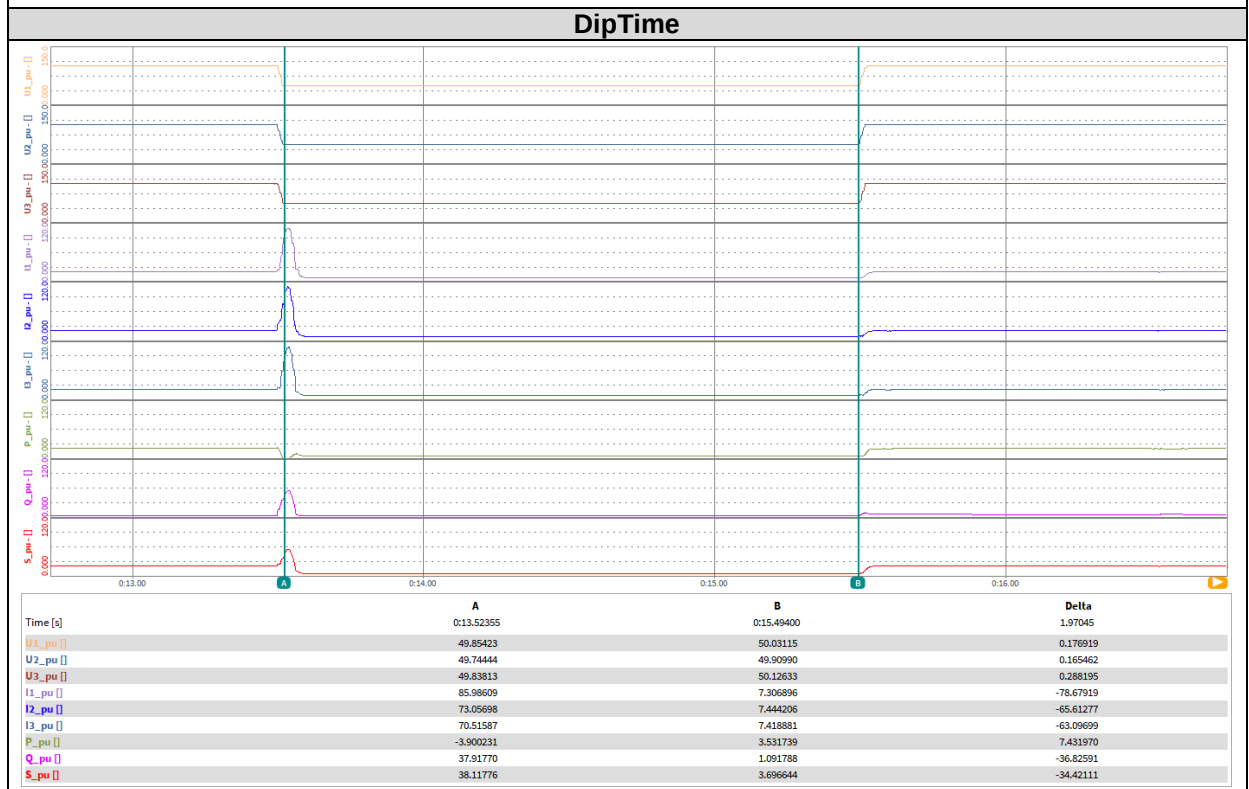
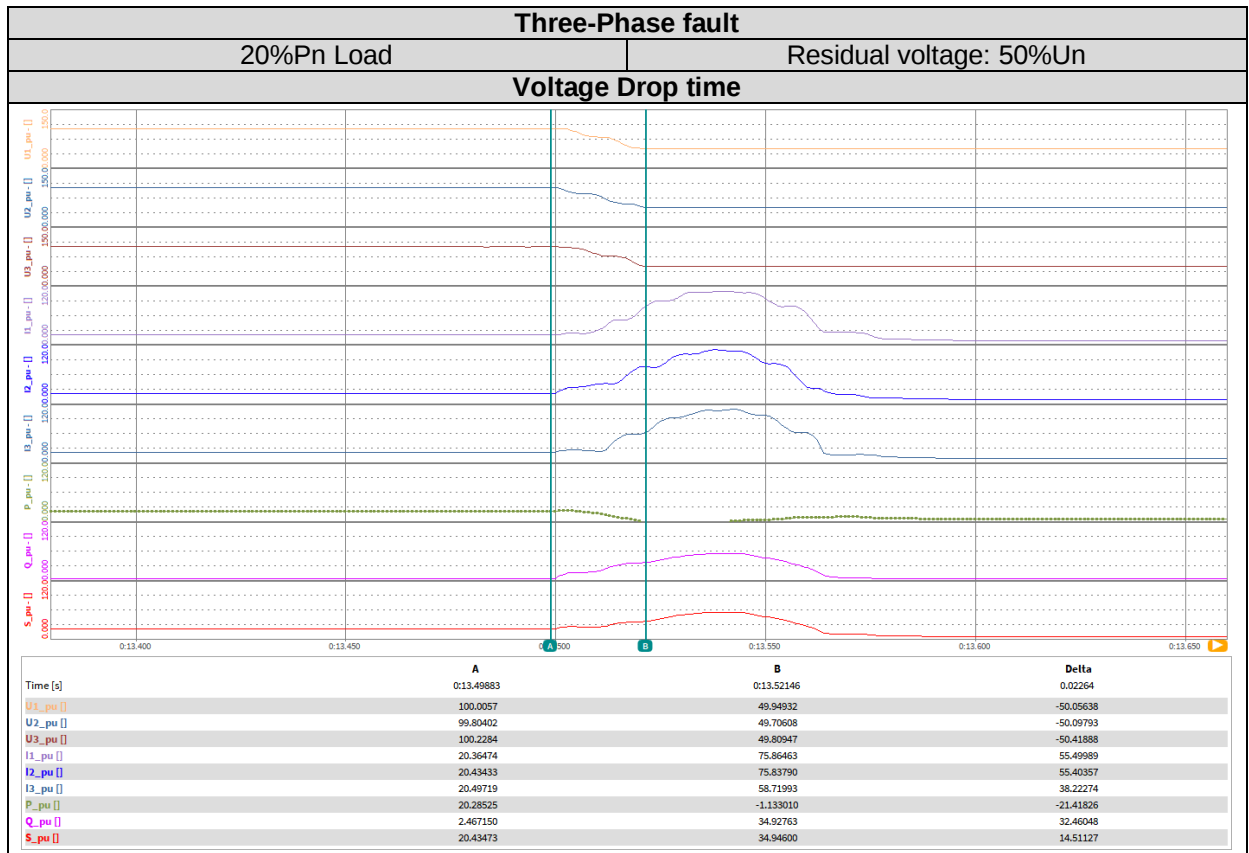
Time [s]	A	B	Delta
U1_pu []	0.14.0125601	0.14.0344705	0.0219104
U2_pu []	100.0456	99.90027	-0.145303
U3_pu []	100.0730	49.74231	-50.33068
I1_pu []	99.87610	49.76423	-50.11187
I2_pu []	20.26924	39.16184	18.89260
I3_pu []	20.33532	33.48363	13.14831
P_pu []	20.23925	28.34196	8.102706
Q_pu []	20.13449	1.910171	-18.22432
S_pu []	2.432817	23.21571	20.78289
S_pu []	20.28094	23.29416	3.013221

DipTime

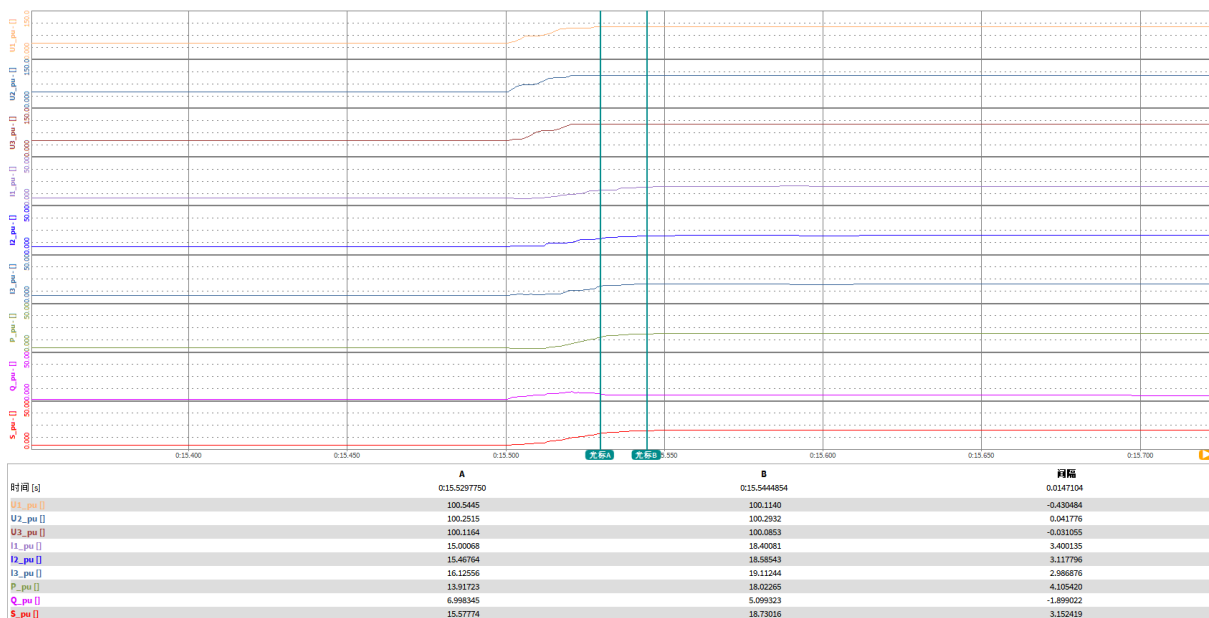


Power Recovery time





Power Recovery time

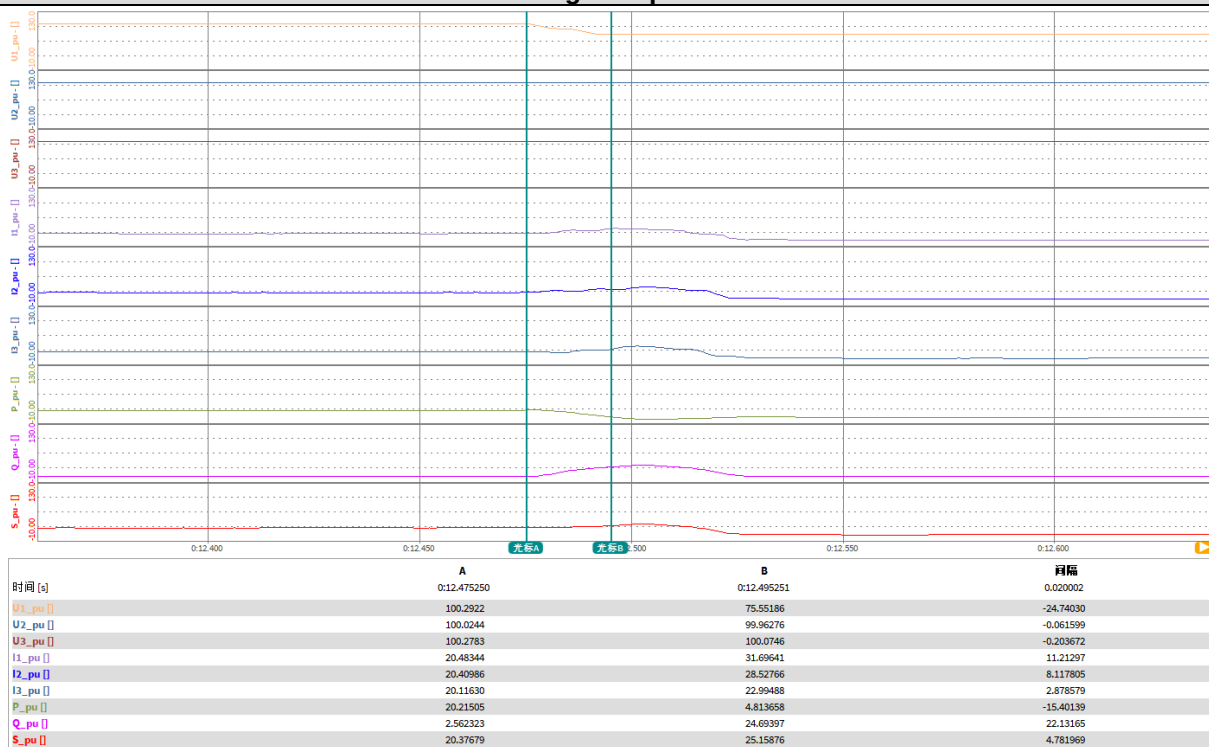


Single-Phase fault

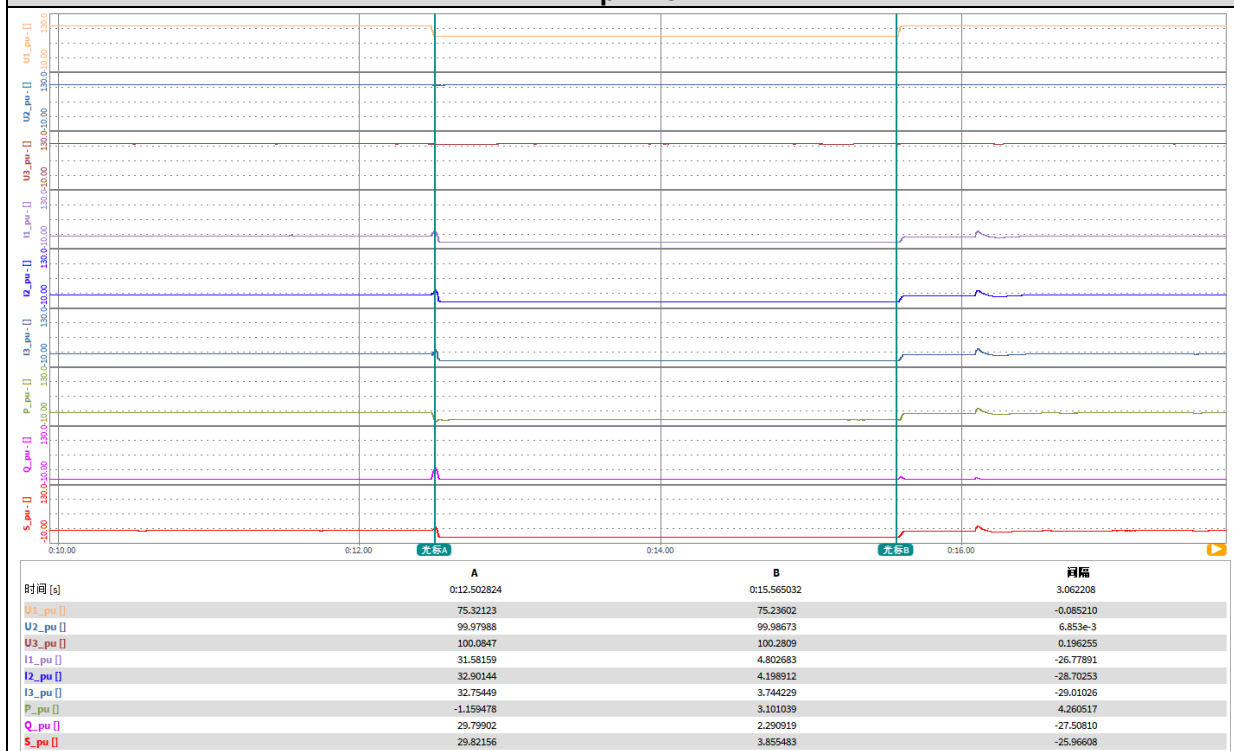
20%Pn Load

Residual voltage: 75%Un

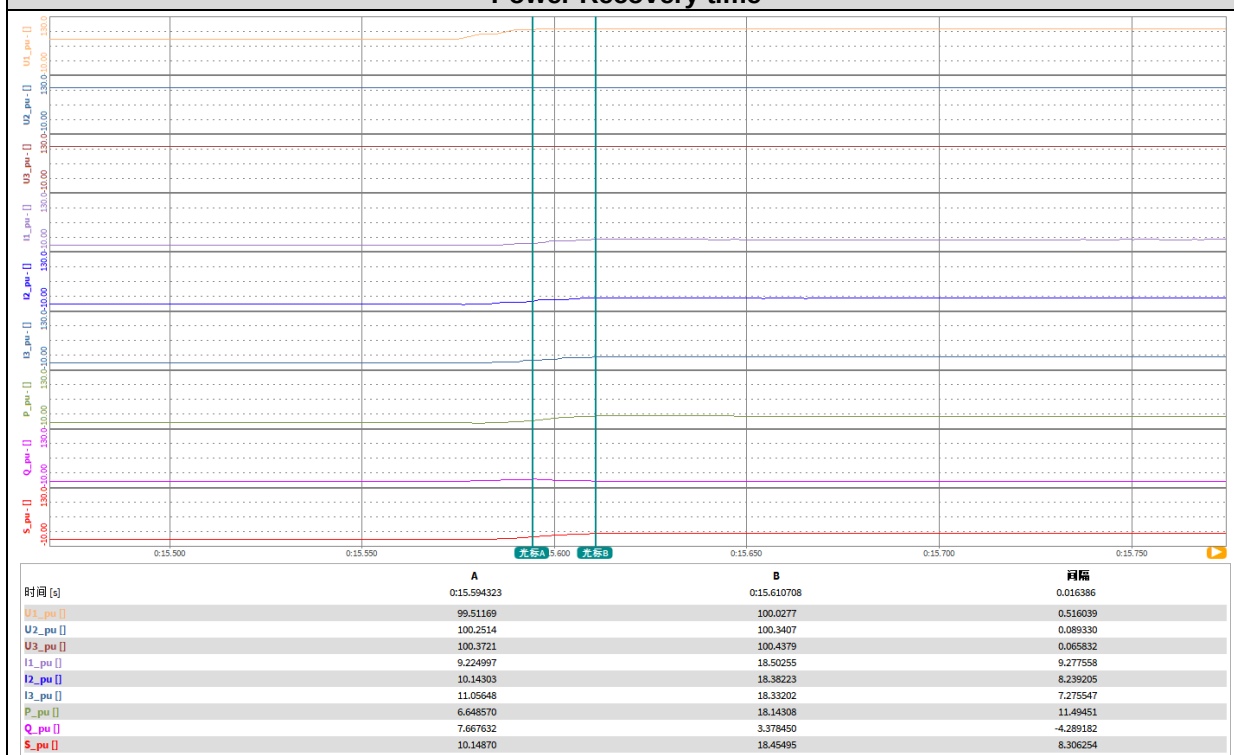
Voltage Drop time

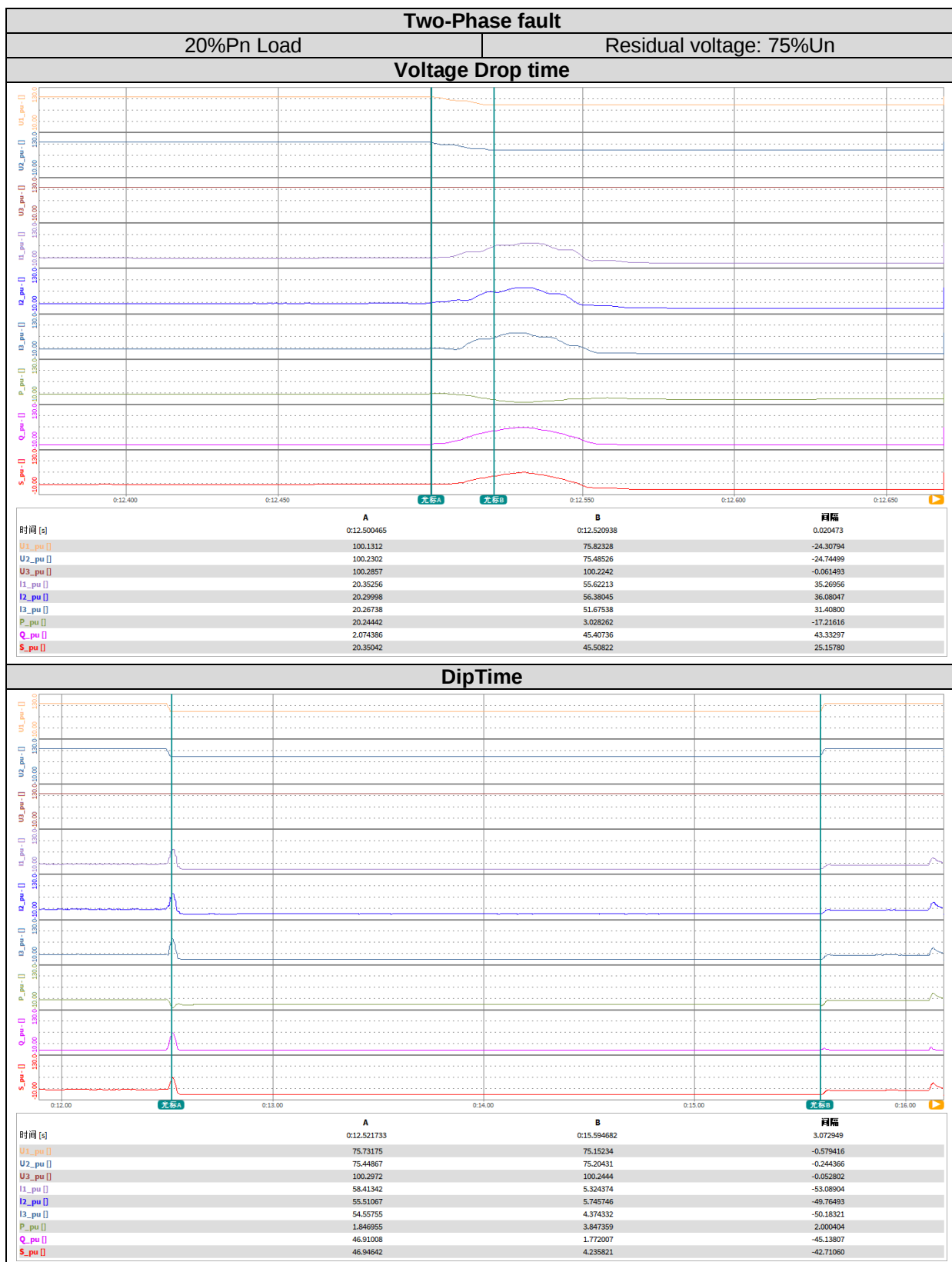


DipTime

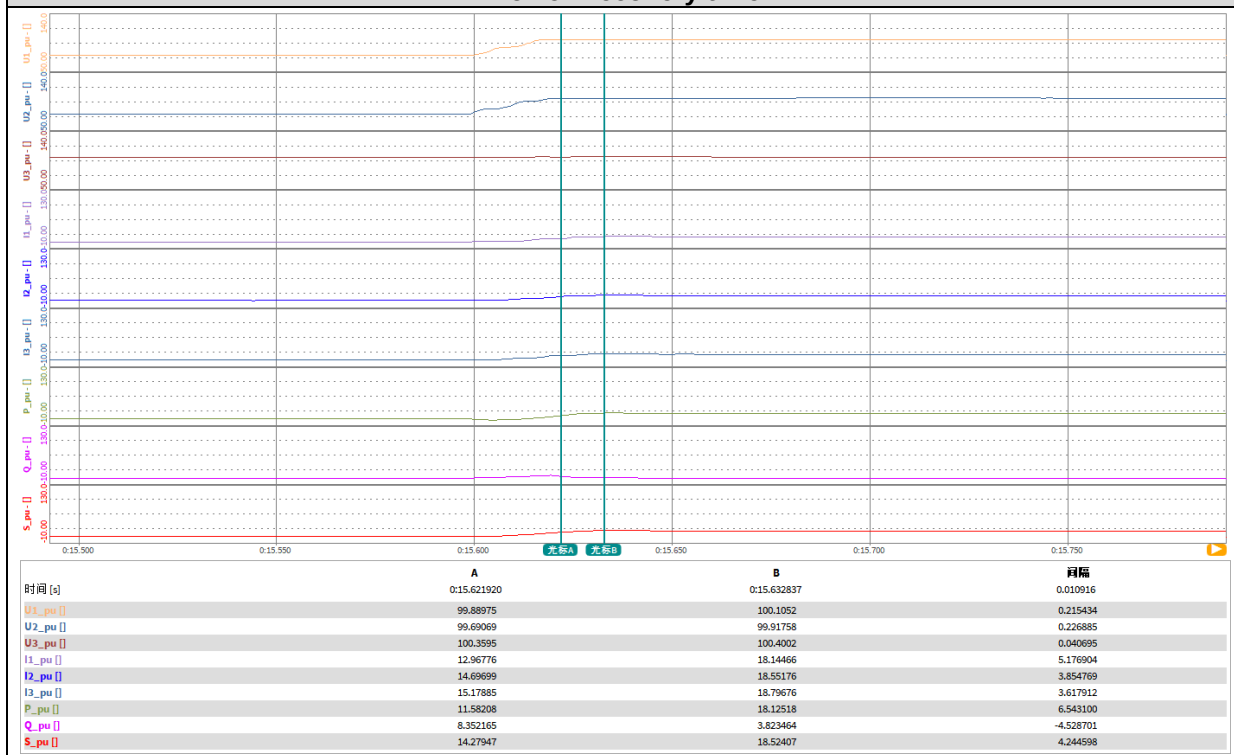


Power Recovery time





Power Recovery time

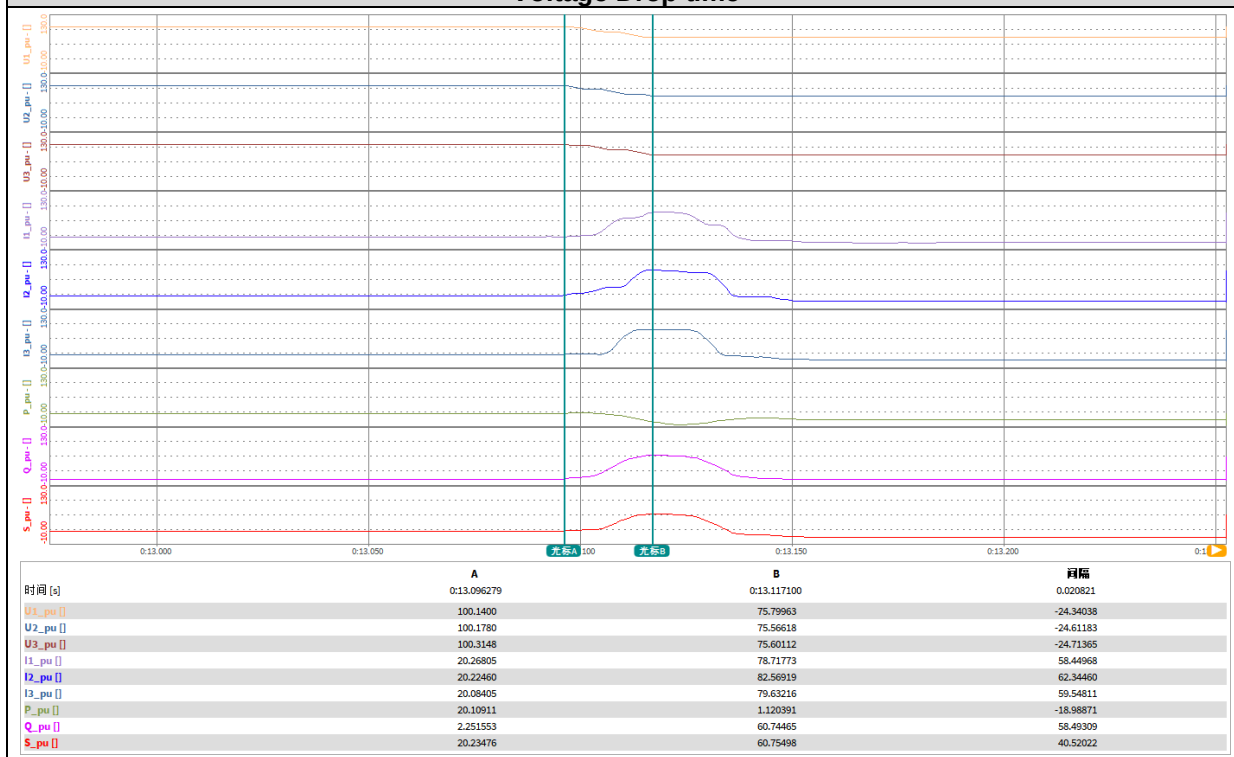


Three-Phase fault

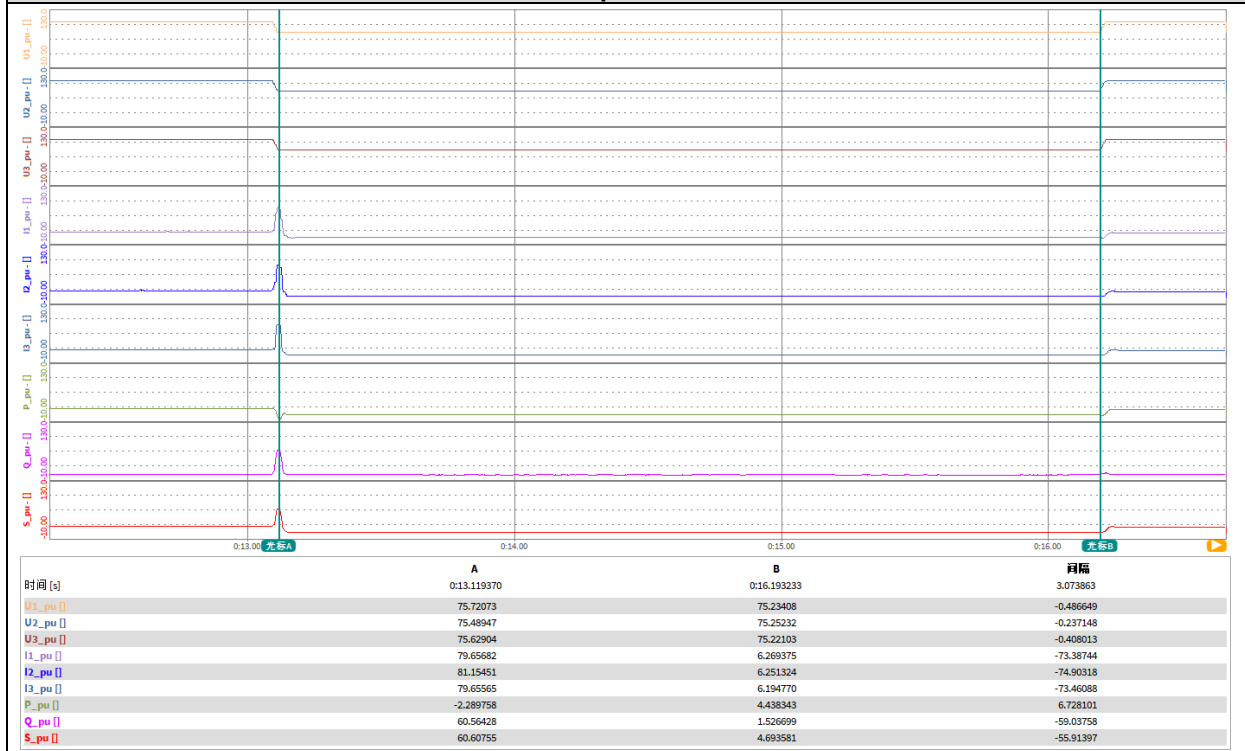
20%Pn Load

Residual voltage: 75%Un

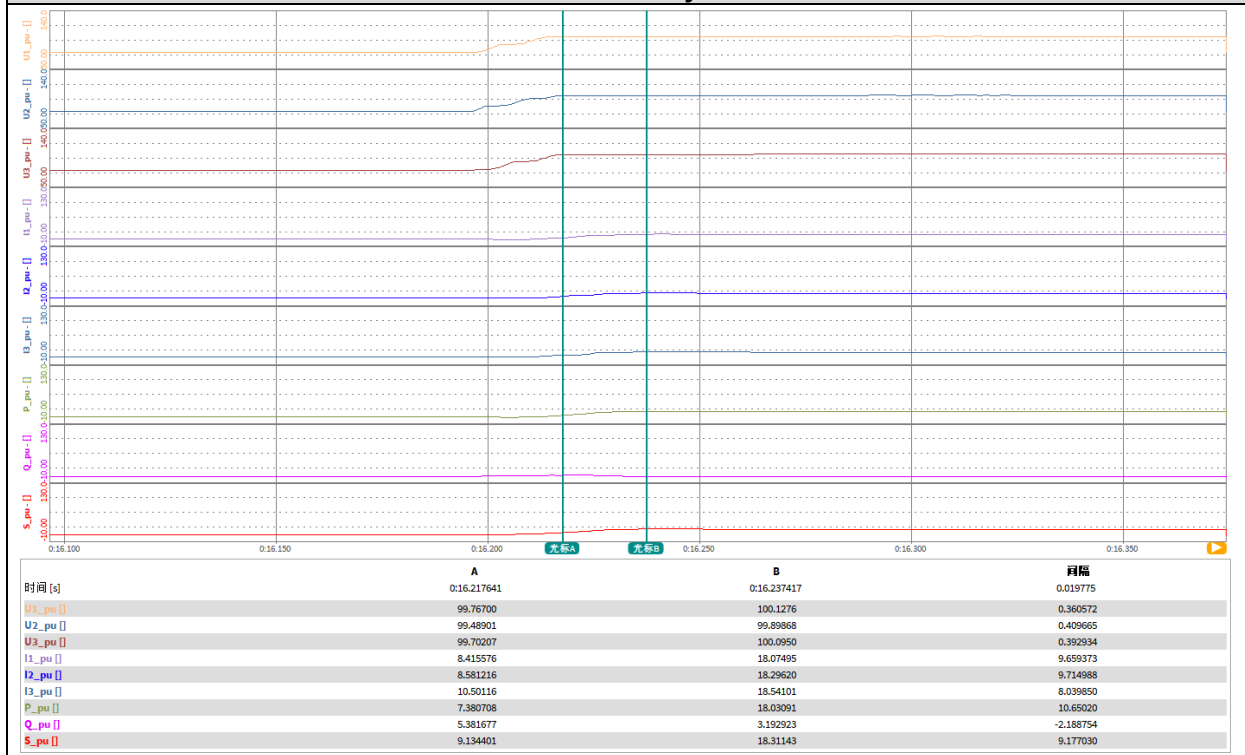
Voltage Drop time



DipTime



Power Recovery time



4.2.1.3. Load Tests: Partial load (> 90 %Pn)

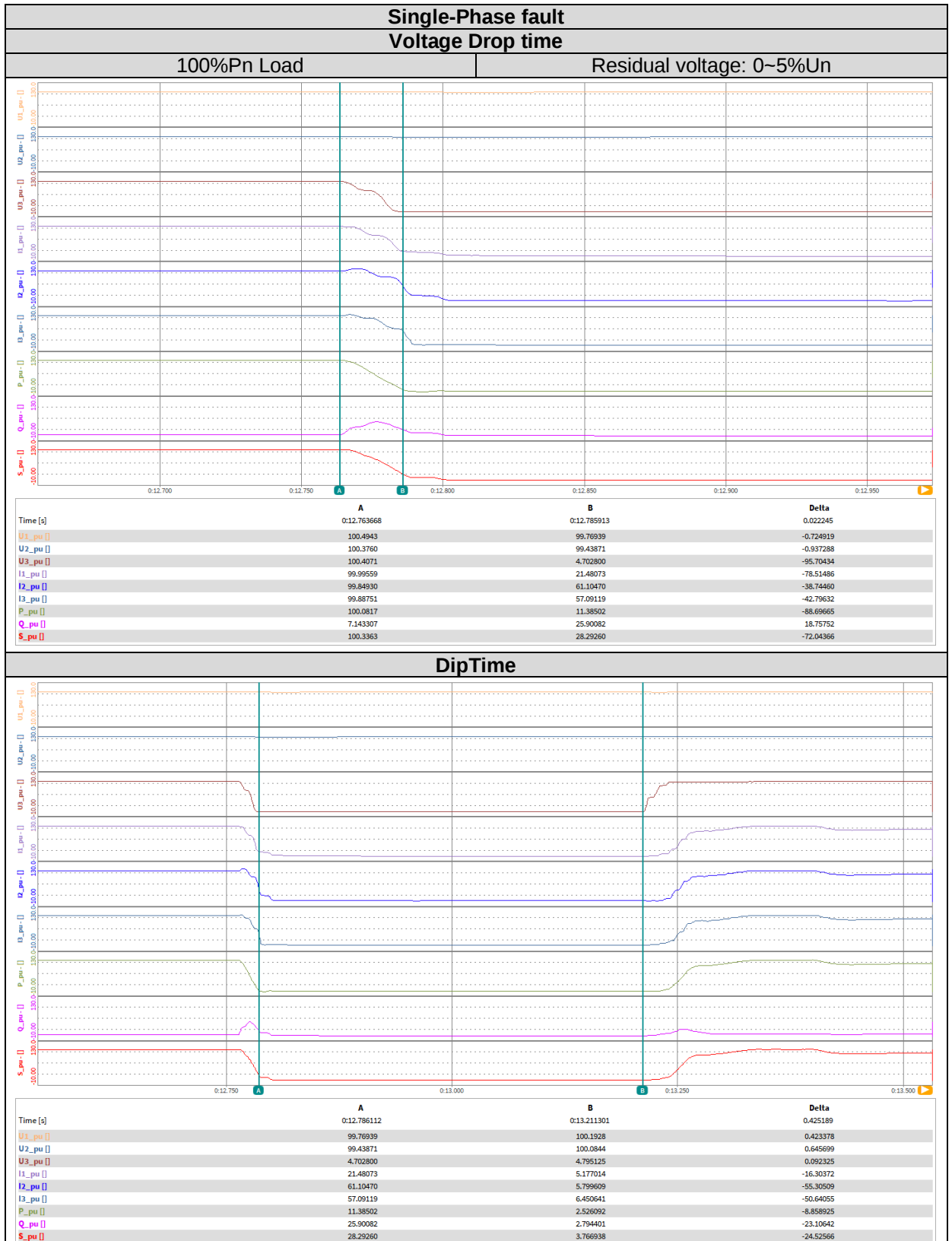
Test results of different no-load cases performed are offered below:

100%Pn Load							
Phase Type	Residual Voltage Desired (%Un)	Voltage Measured (%Un)	Voltage Drop Time (ms)	Dip Time Desired (ms)	Time Measured (ms)	Power Recovery Time (ms)	Voltage After Recovery (%Un)
1 ph	0-5%	4.7	22	≥400	425	61	100.4
2 ph		4.7	22		425	62	100.4
3 ph		4.5	20		428	120	100.4
1 ph	25%	25.8	20	≥1000	1023	62	100.3
2 ph		25.9	22		1026	62	100.3
3 ph		25.6	21		1025	60	100.3
1 ph	50%	50.9	21	≥1850	1871	58	100.2
2 ph		50.7	21		1863	58	100.3
3 ph		50.8	21		1868	59	100.3
1 ph	75%	75.4	20	≥3000	3081	61	100.3
2 ph		75.7	20		3074	49	100.4
3 ph		75.4	20		3073	57	100.3

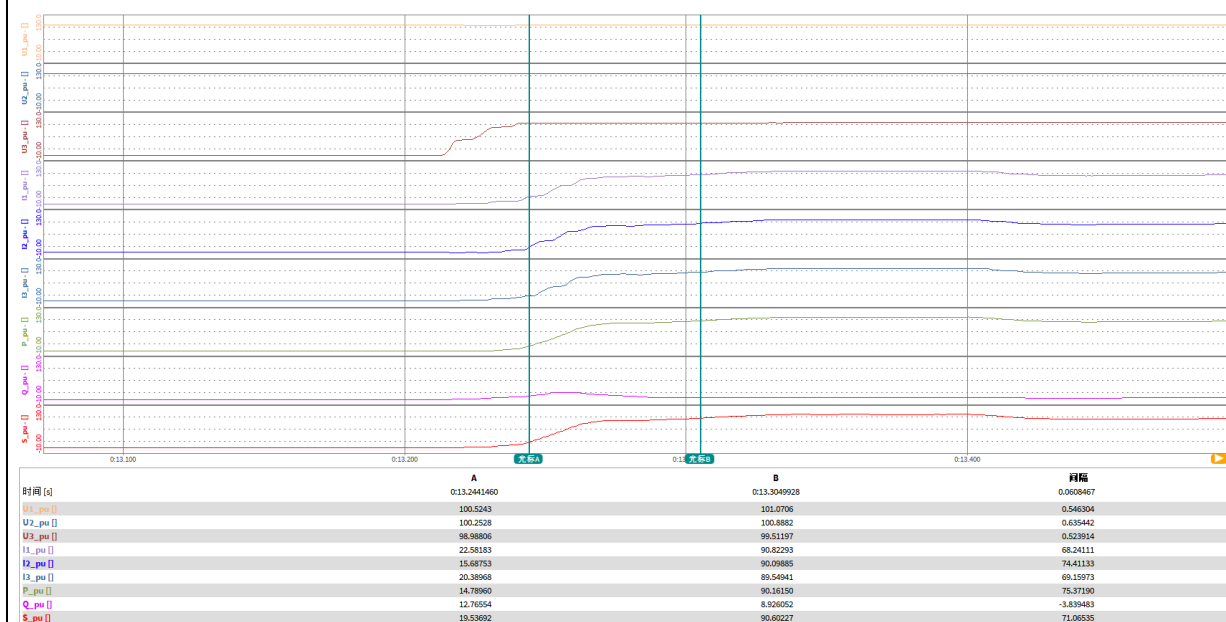
Remark: The virtual neutral line was used in the test.

Test results are graphically represented at following pages.

Note: The following test figure the voltage unit of U1_pu, U2_pu and U3_pu are %Un , the current unit of I1_pu, I2_pu and I3_pu are %In and the power unit is %Pn



Power Recovery Time

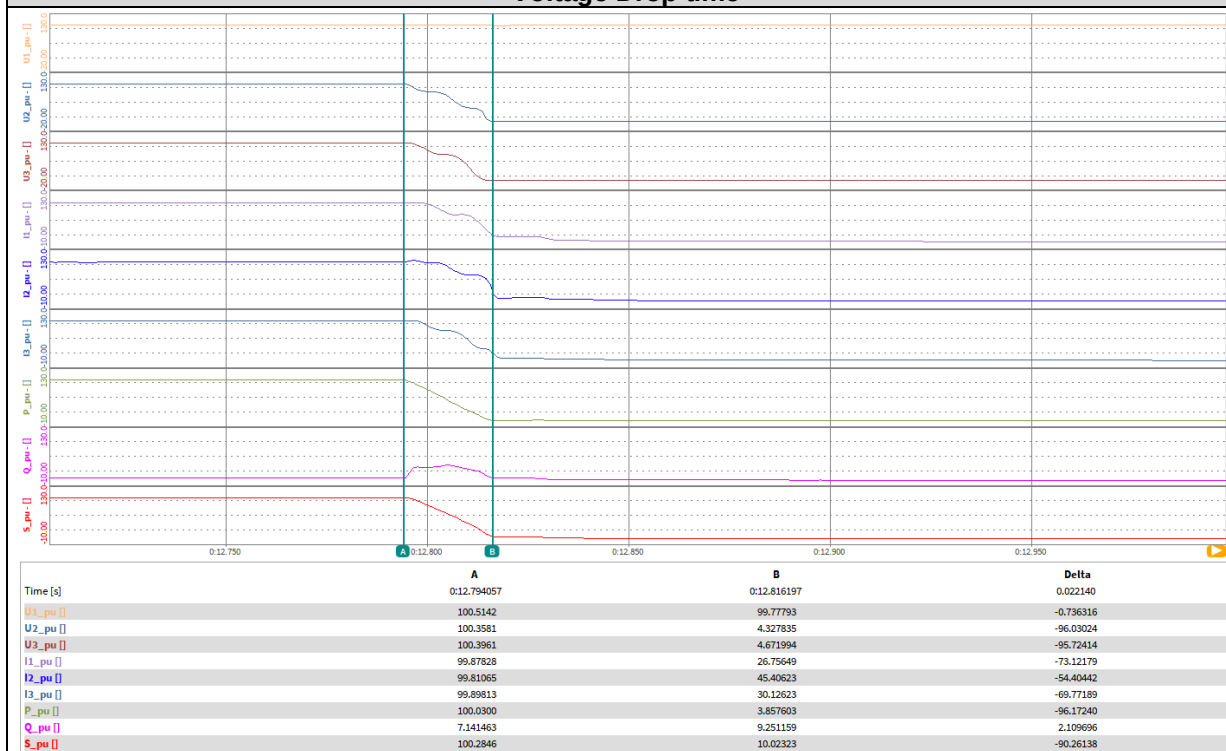


Two-Phase fault

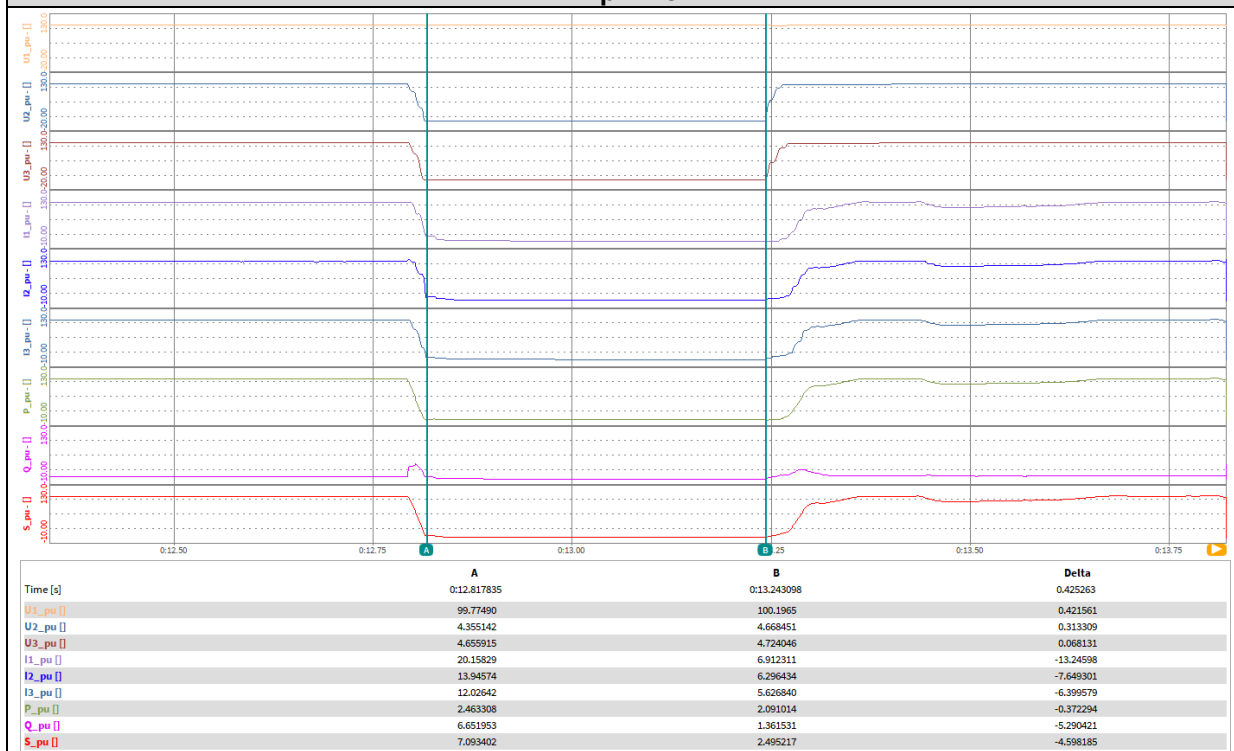
100%Pn Load

Residual voltage: 0~5%Un

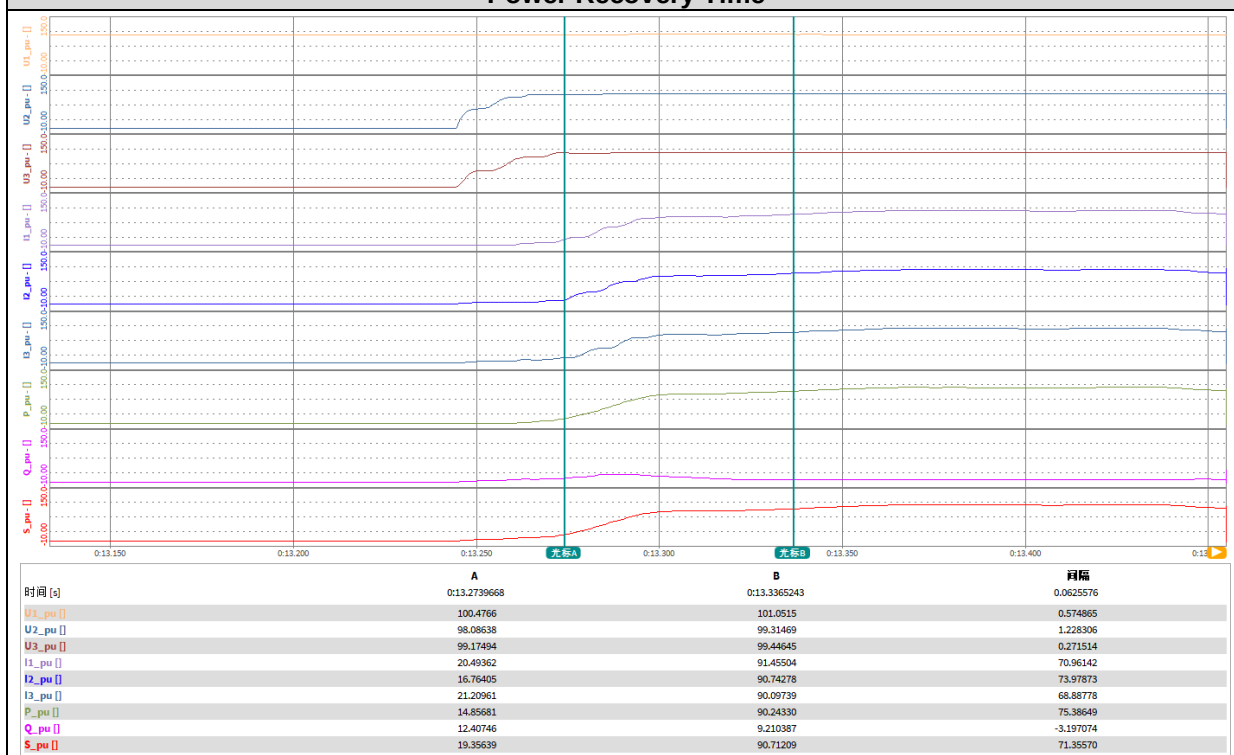
Voltage Drop time

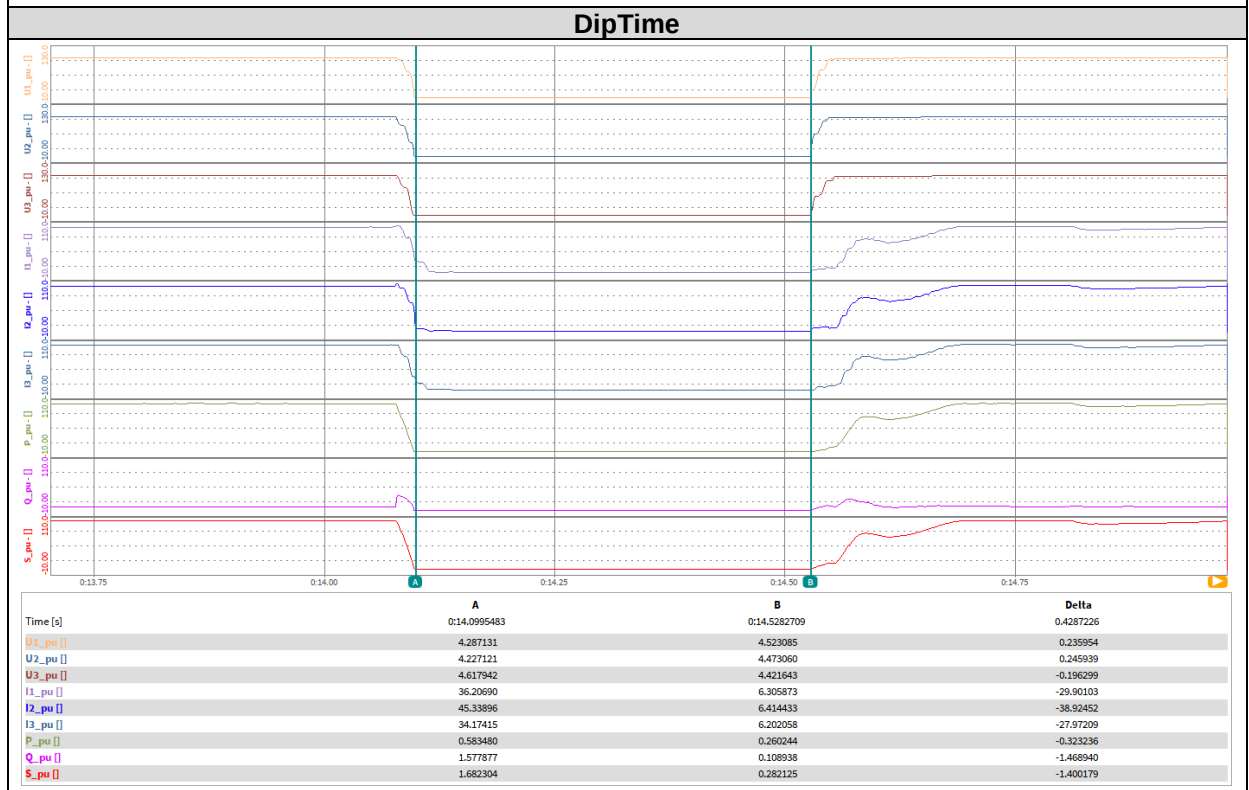
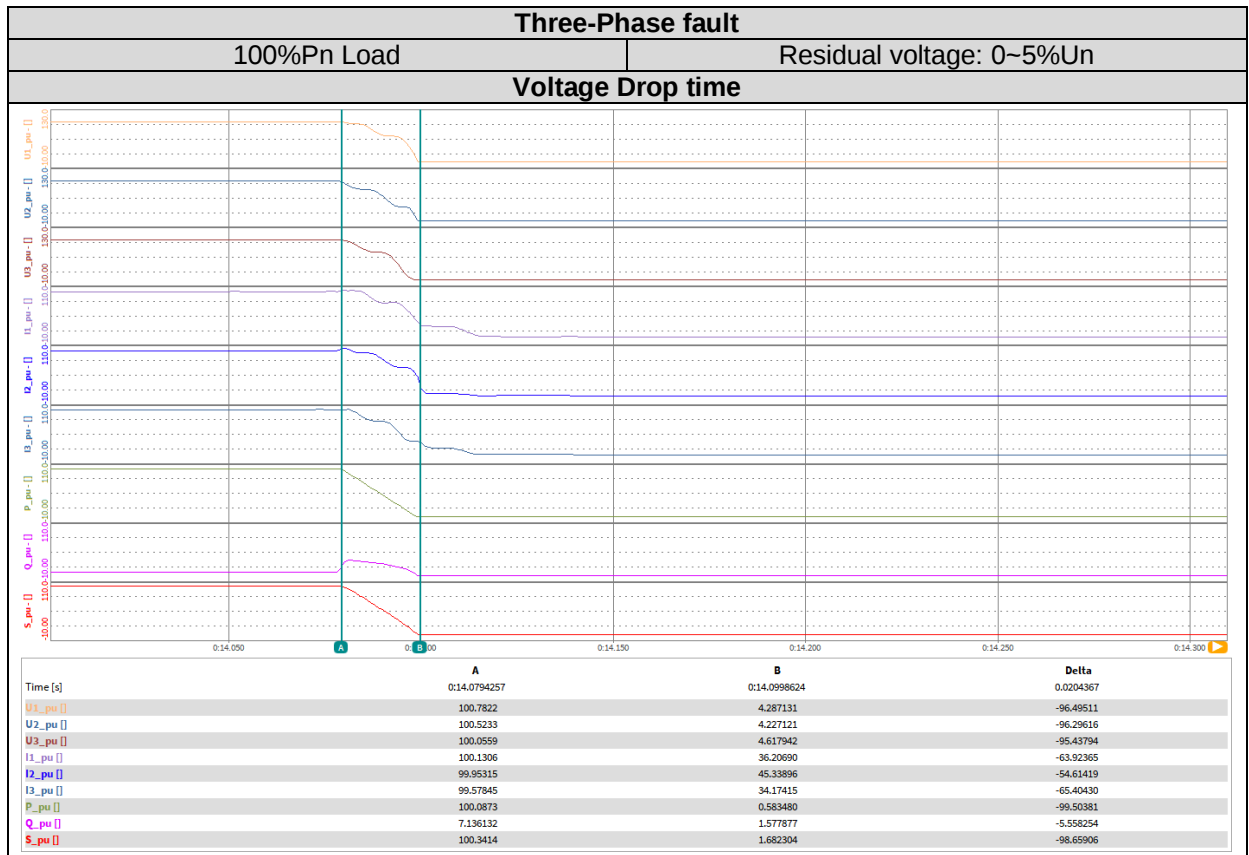


DipTime

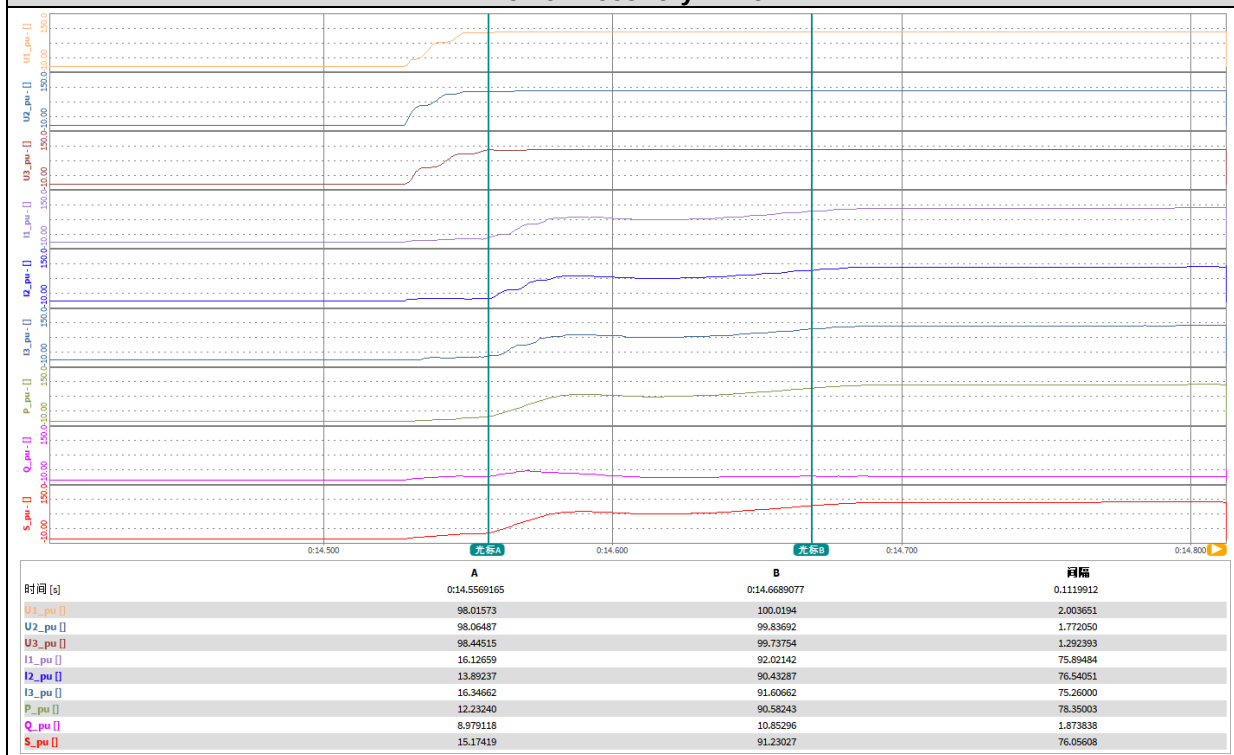


Power Recovery Time





Power Recovery Time

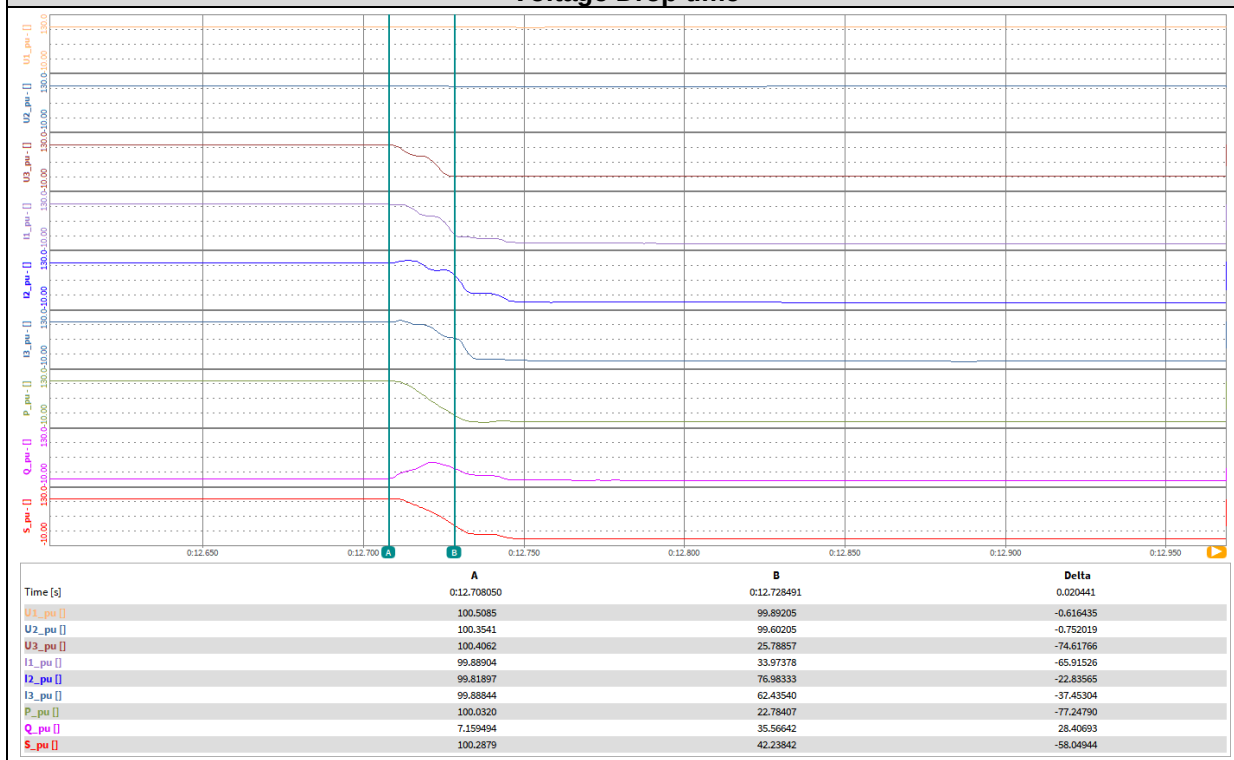


Single-Phase fault

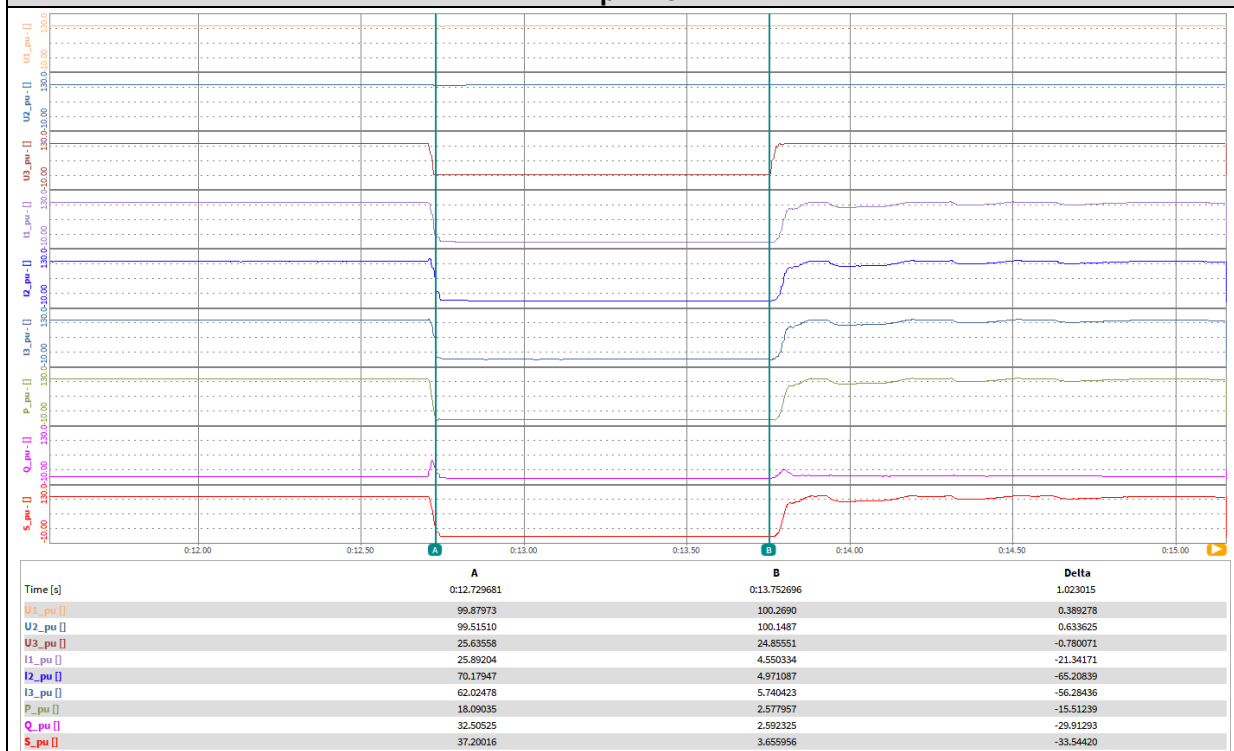
100%Pn Load

Residual voltage: 25%Un

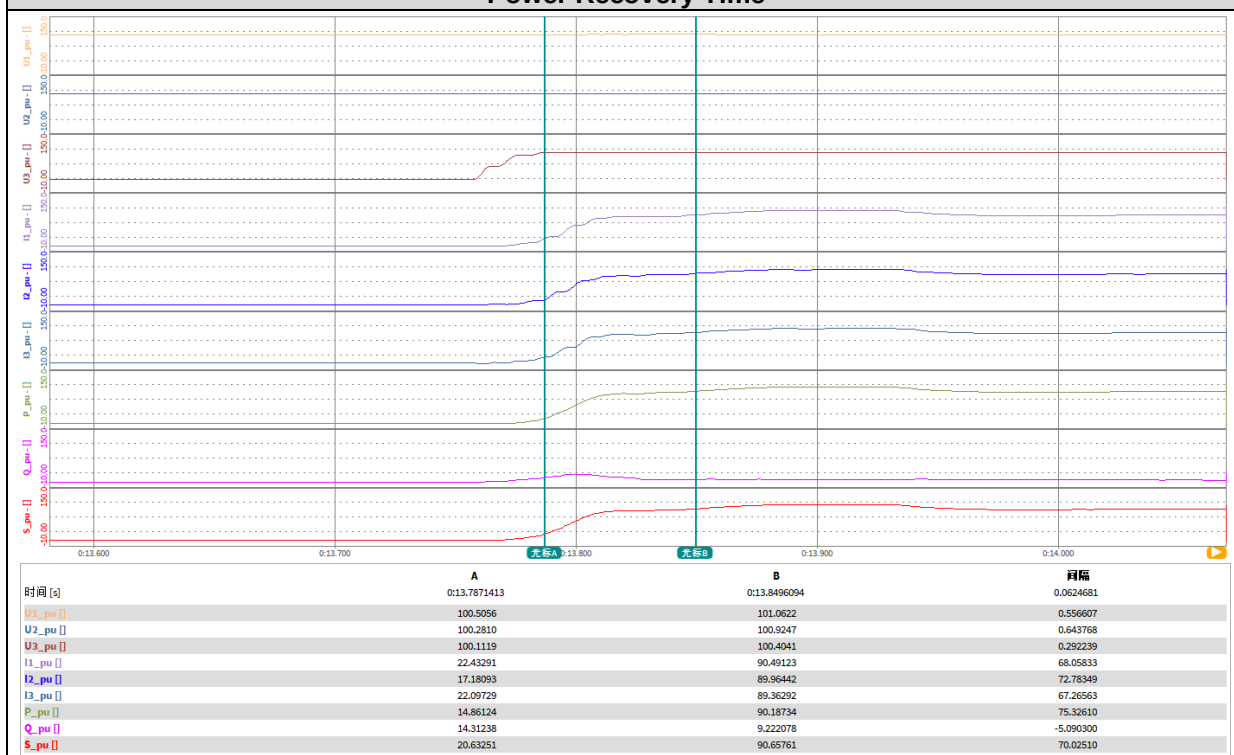
Voltage Drop time

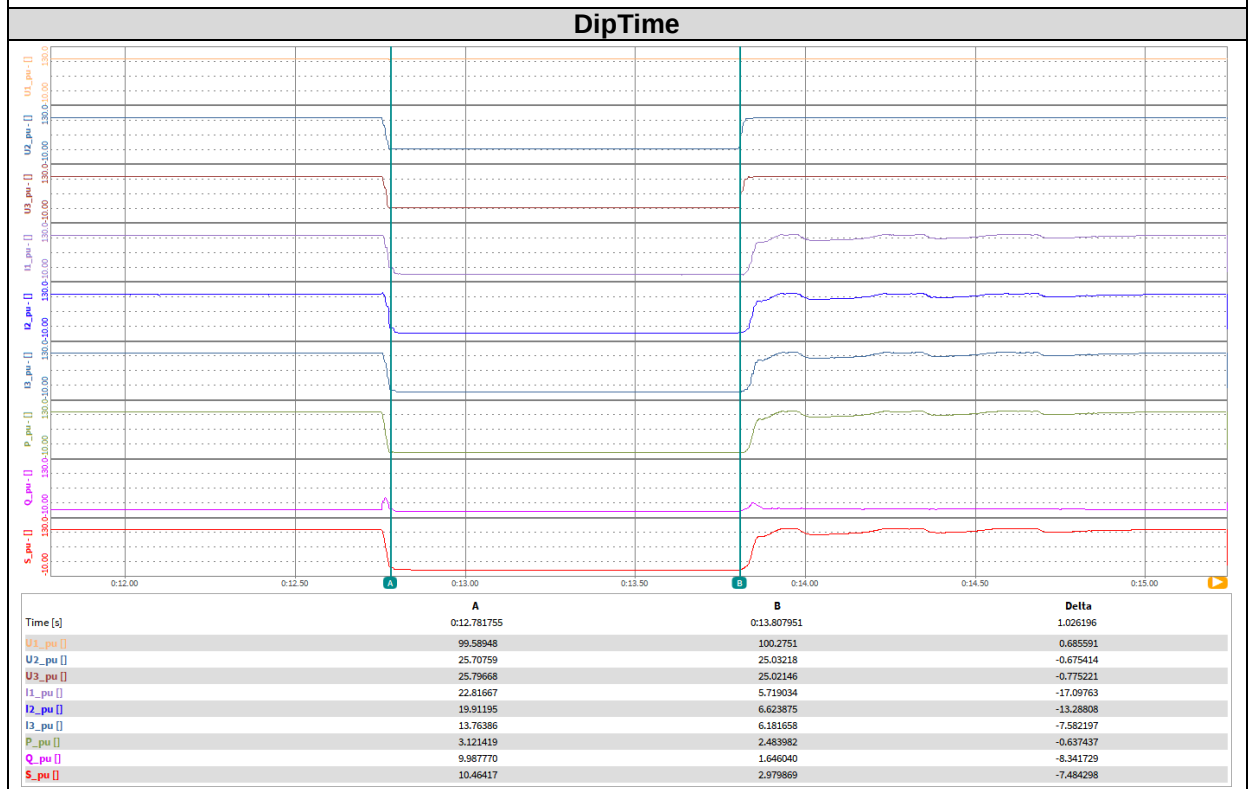
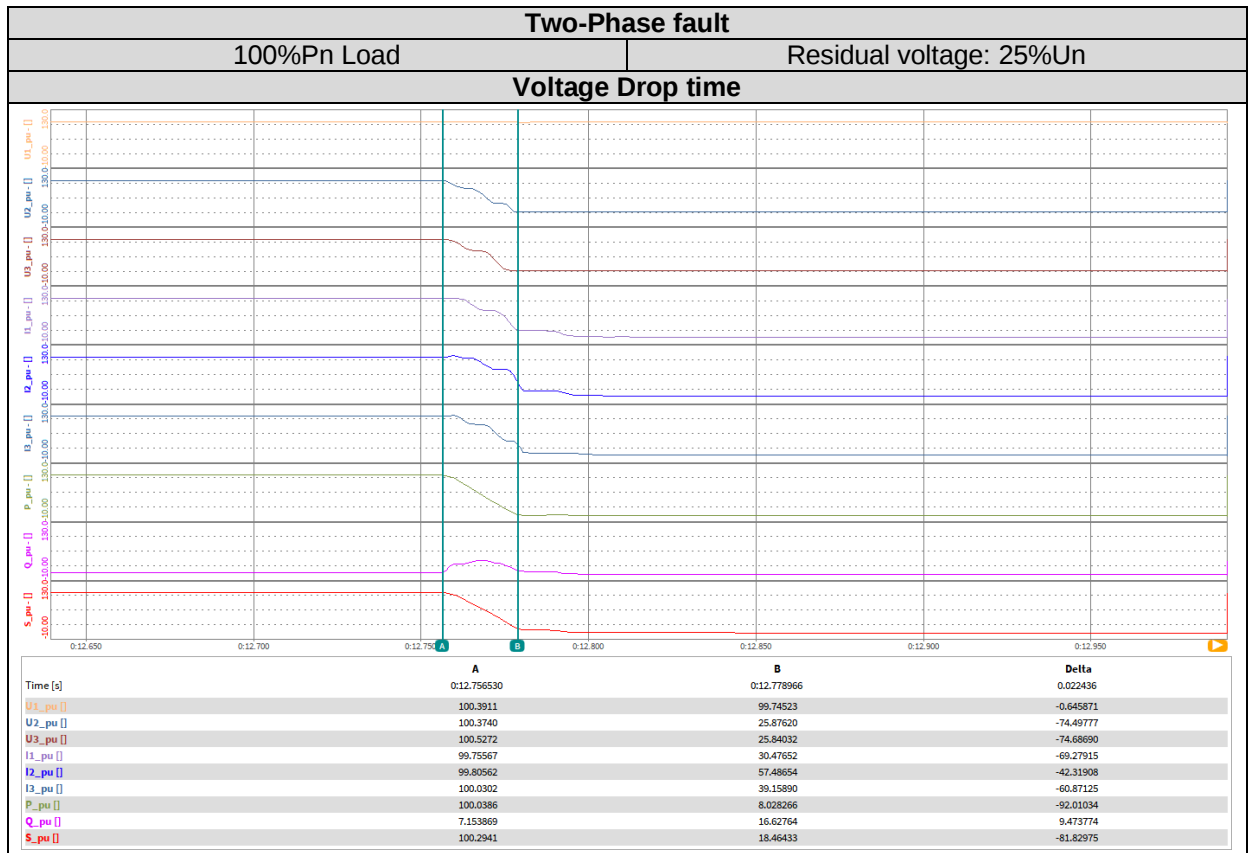


DipTime

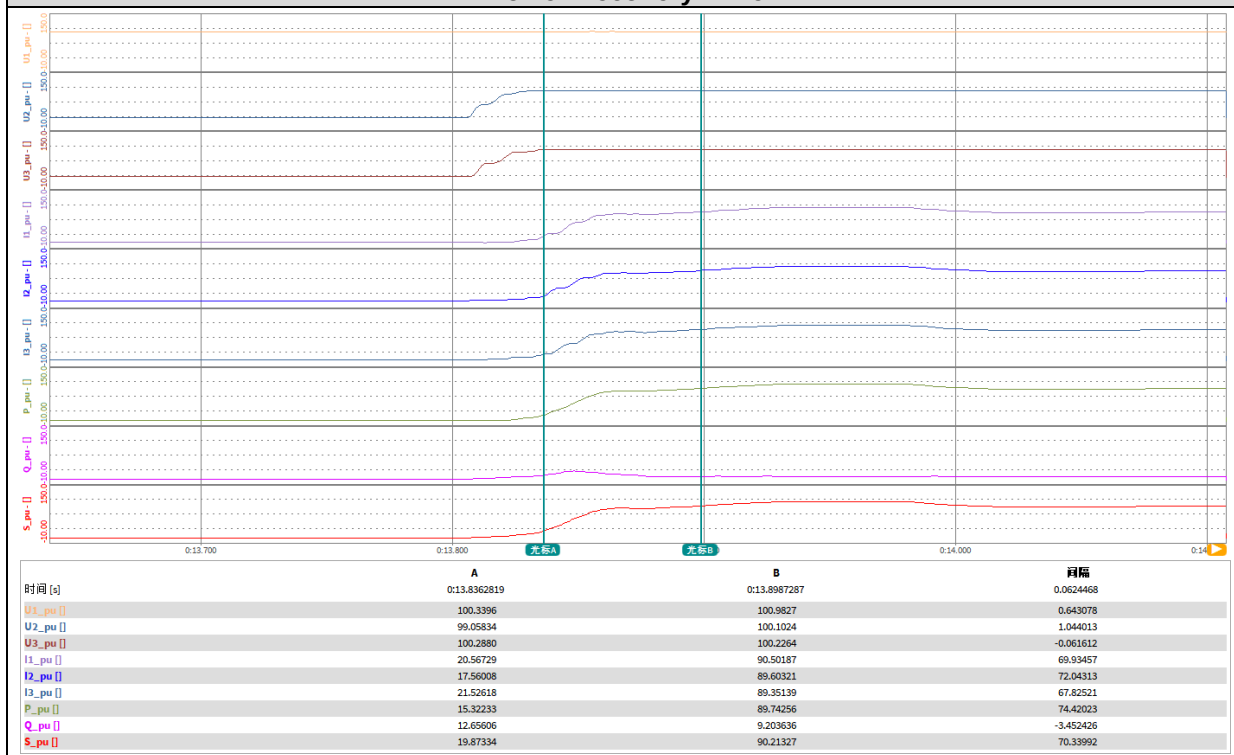


Power Recovery Time





Power Recovery Time

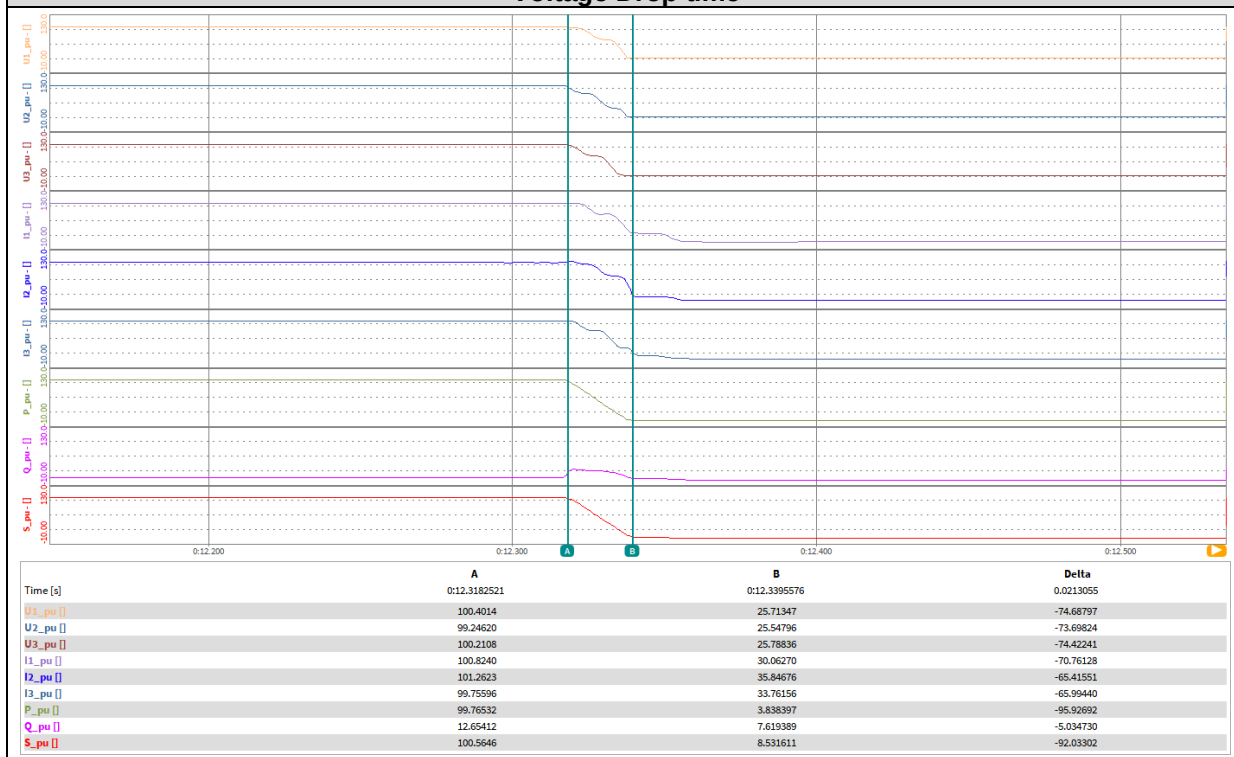


Three-Phase fault

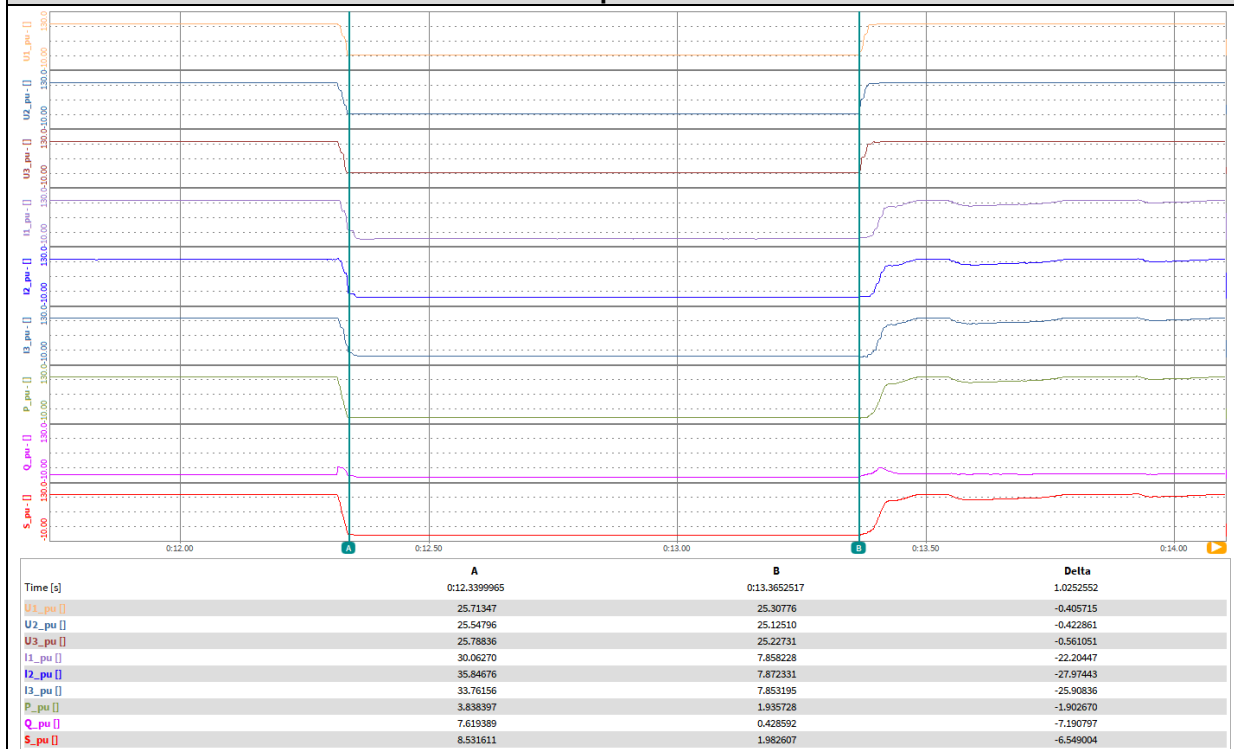
100%Pn Load

Residual voltage: 25%Un

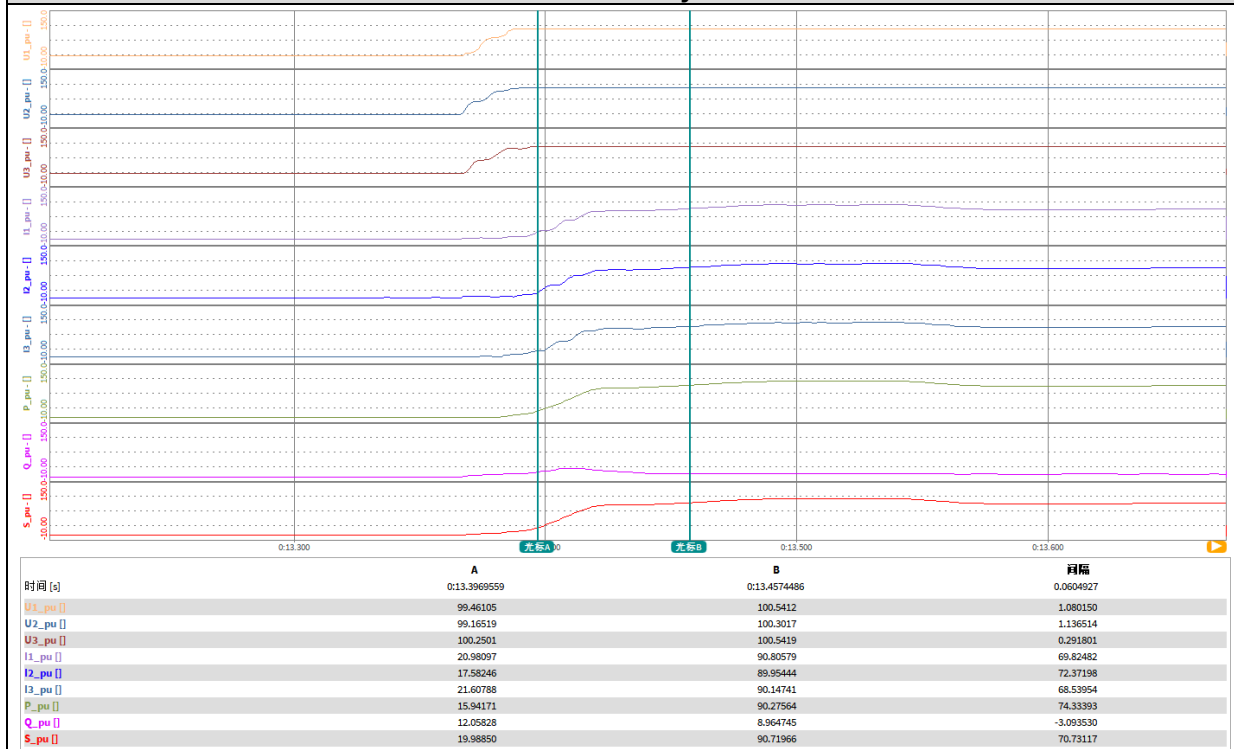
Voltage Drop time

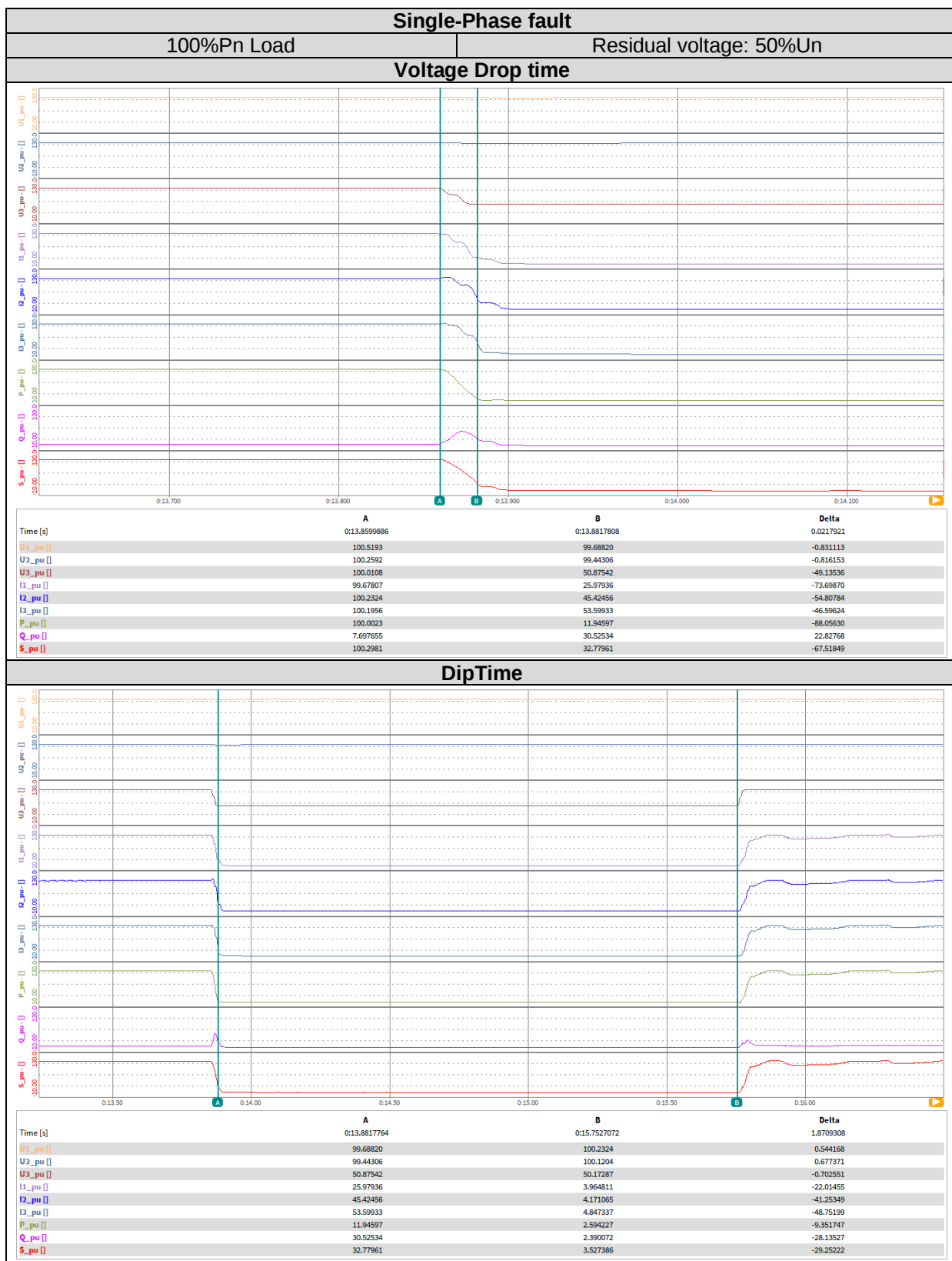


DipTime

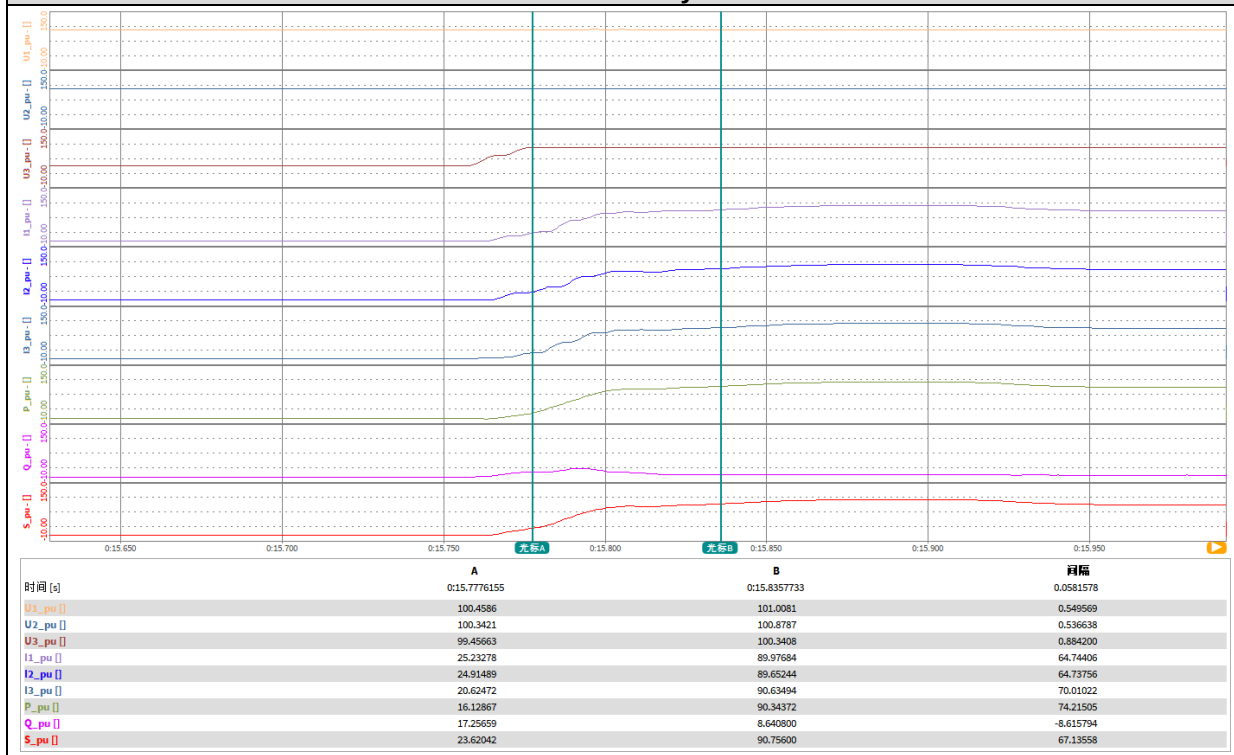


Power Recovery Time





Power Recovery Time

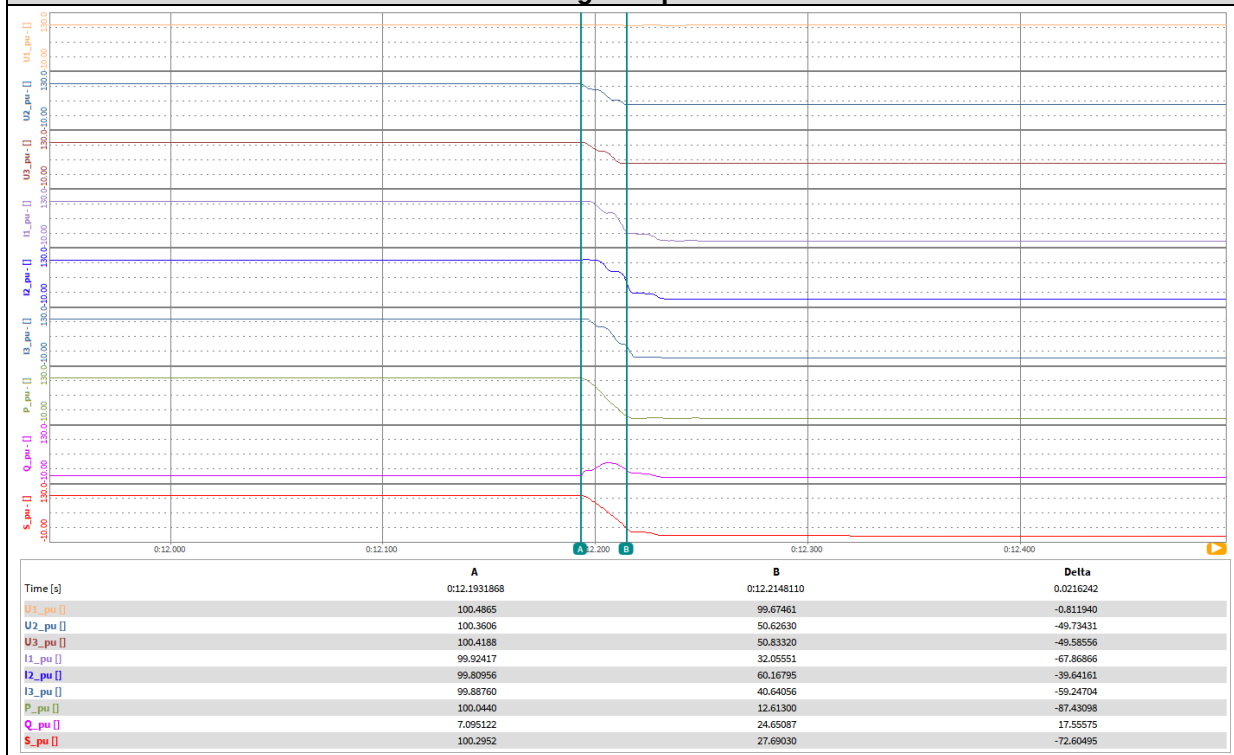


Two-Phase fault

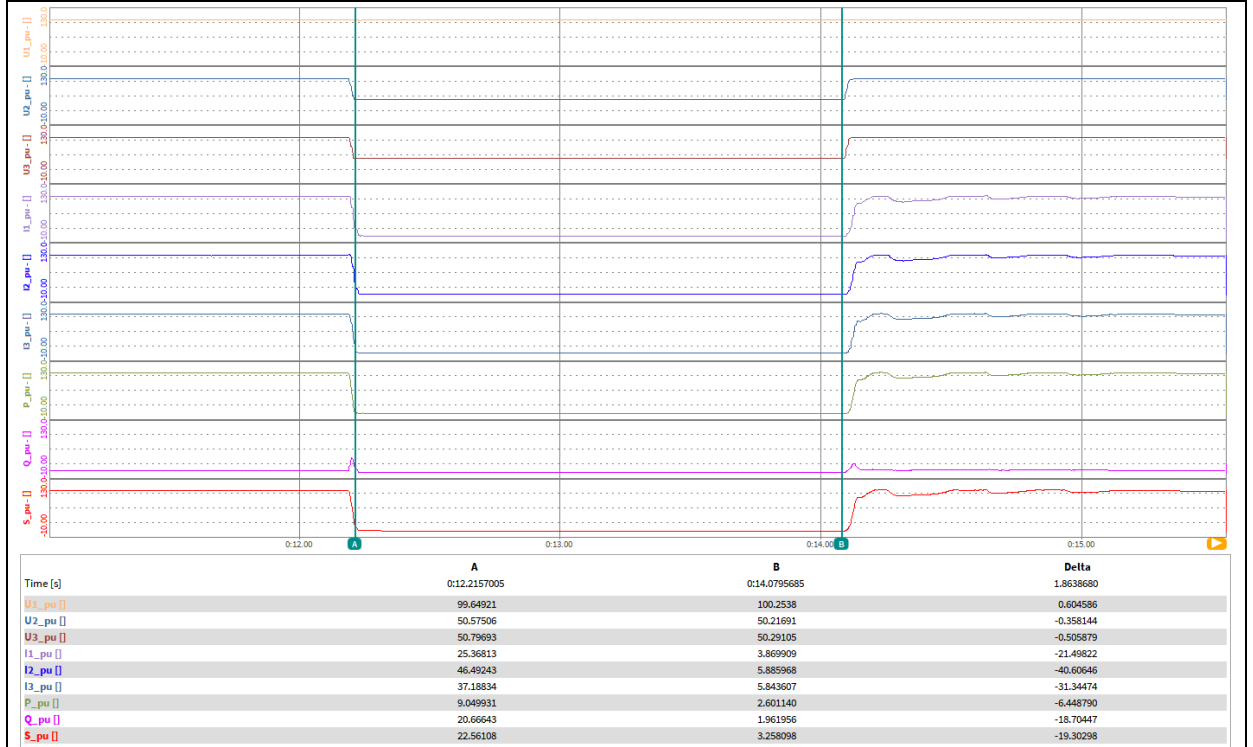
100%Pn Load

Residual voltage: 50%Un

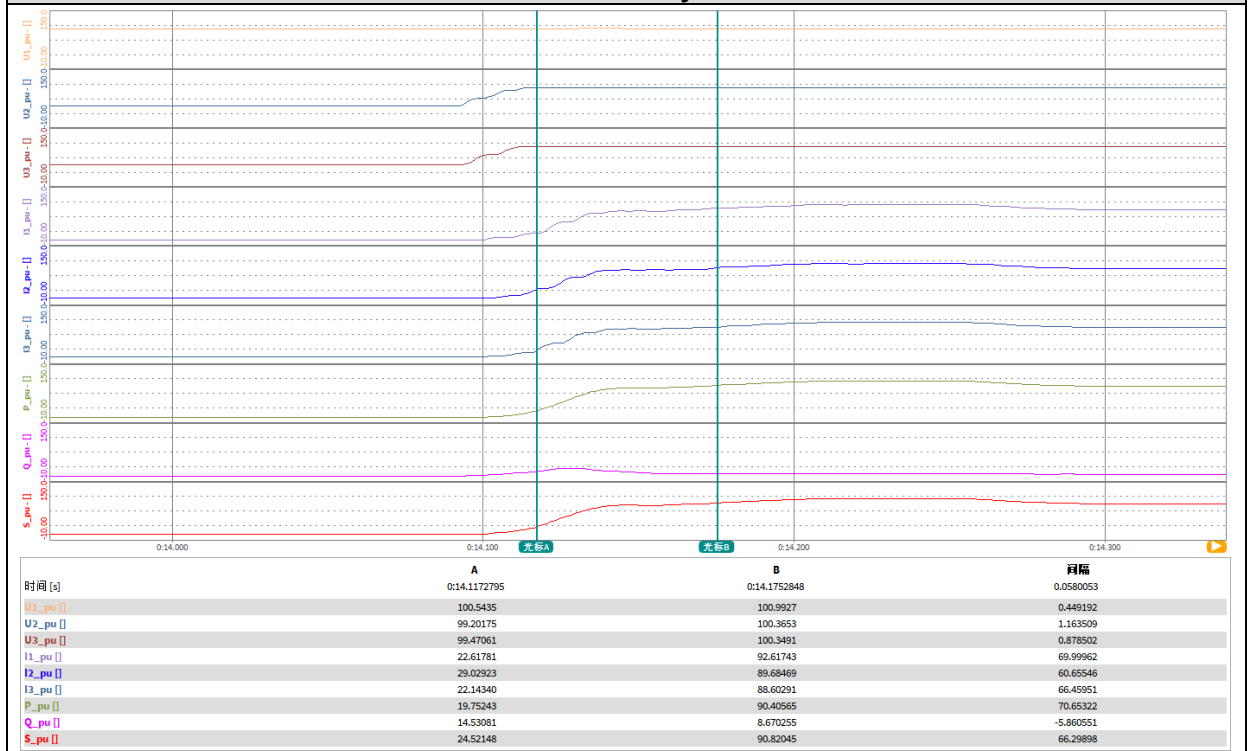
Voltage Drop time

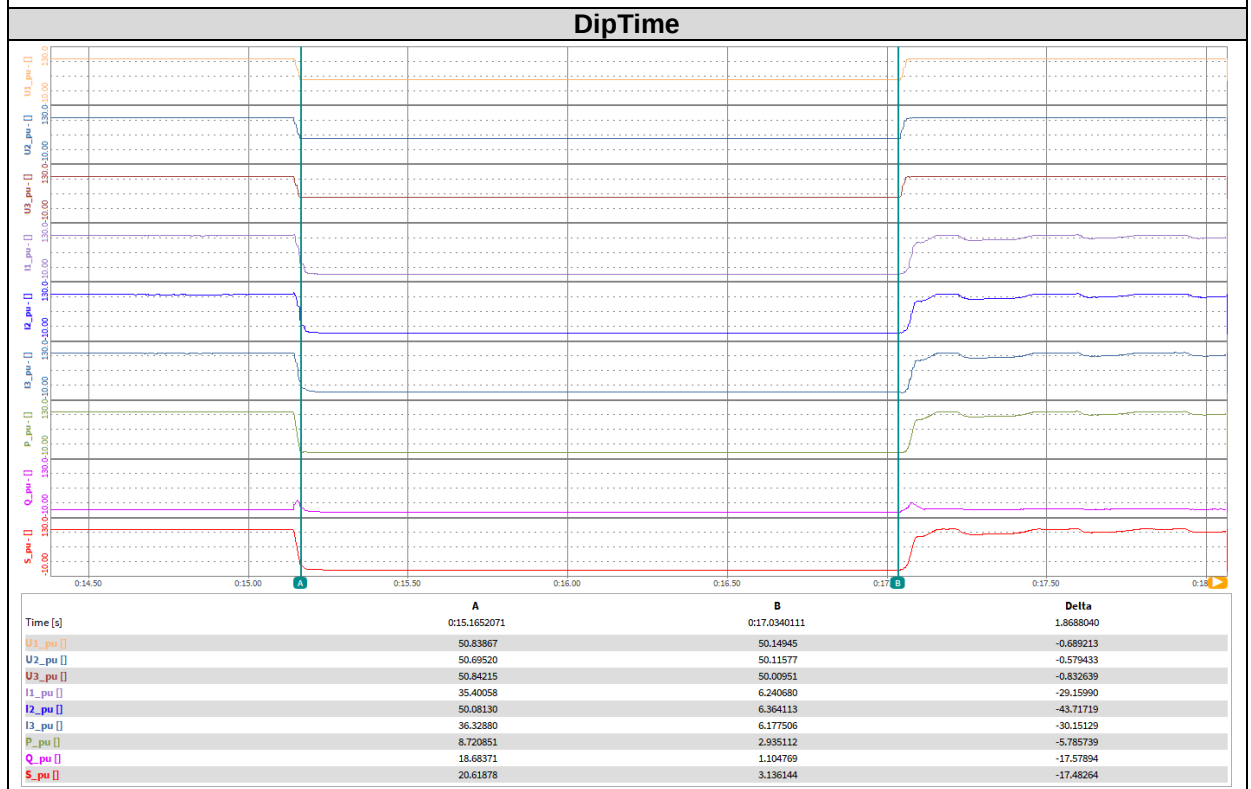
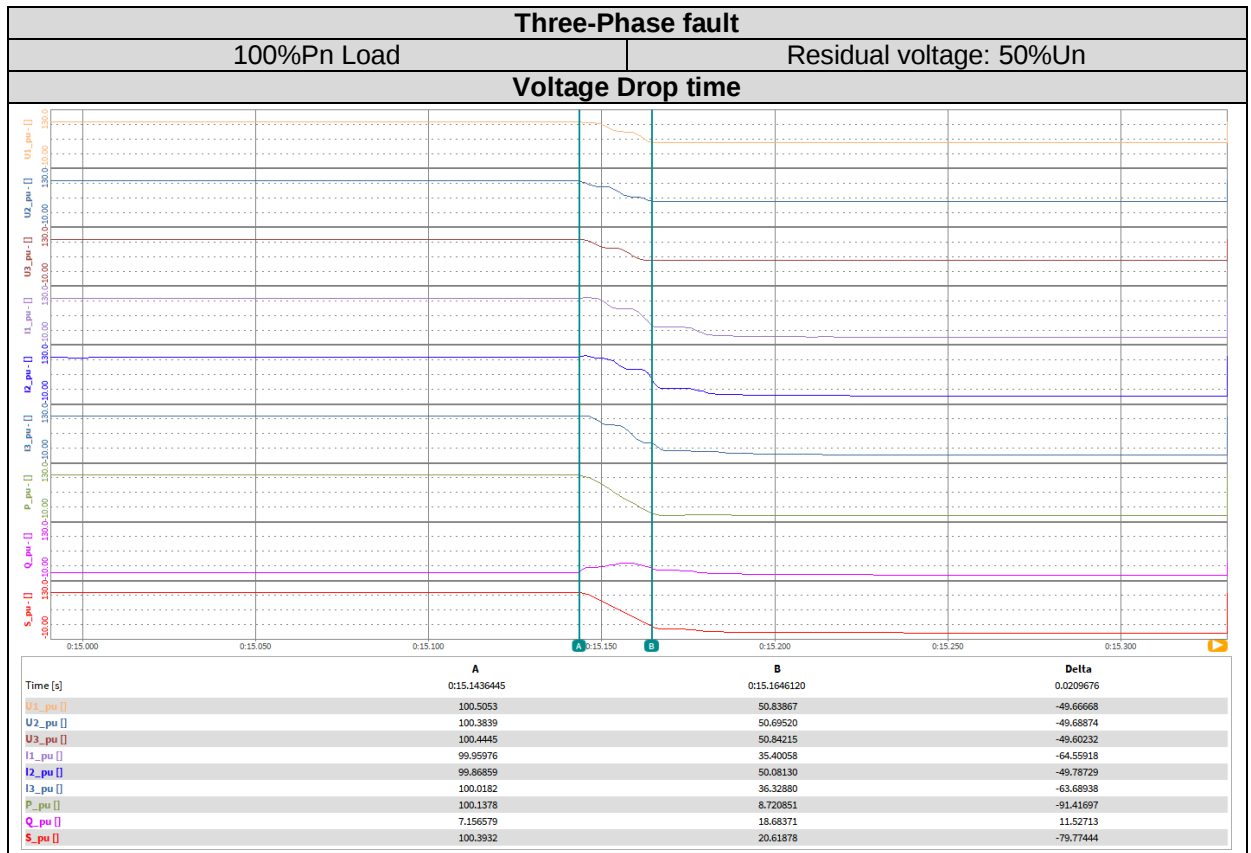


DipTime

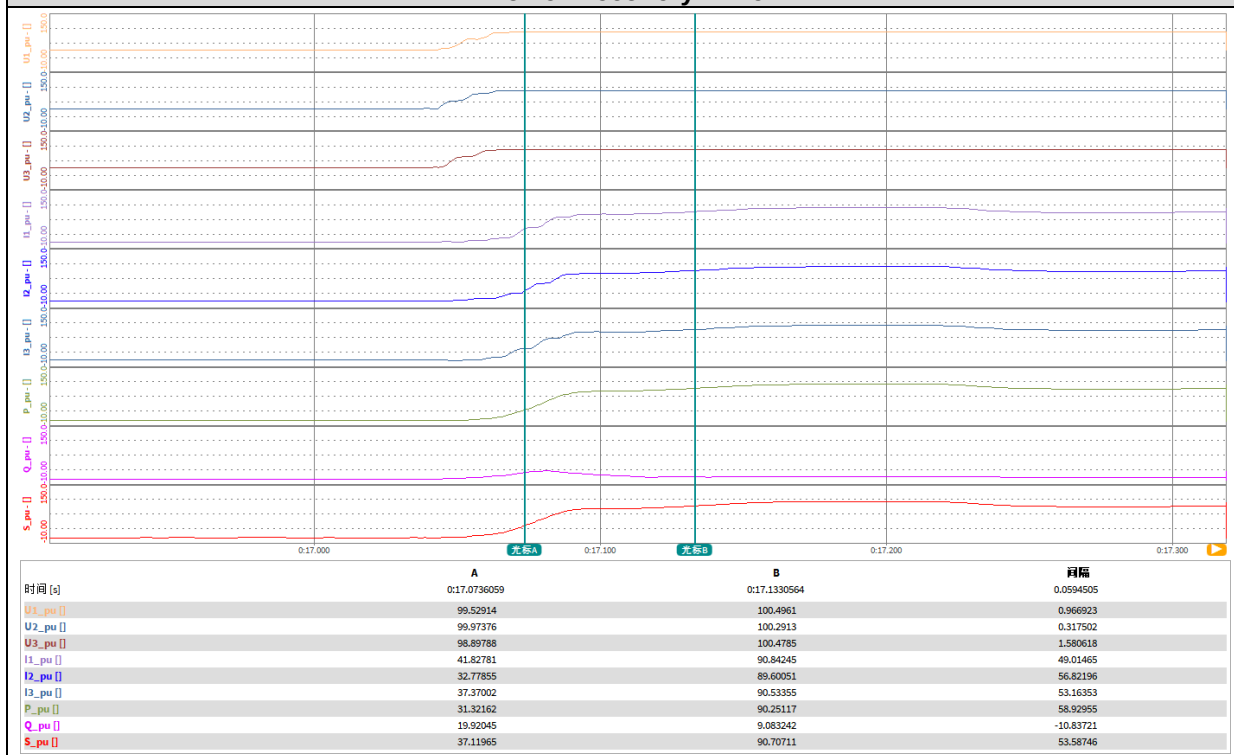


Power Recovery Time





Power Recovery Time

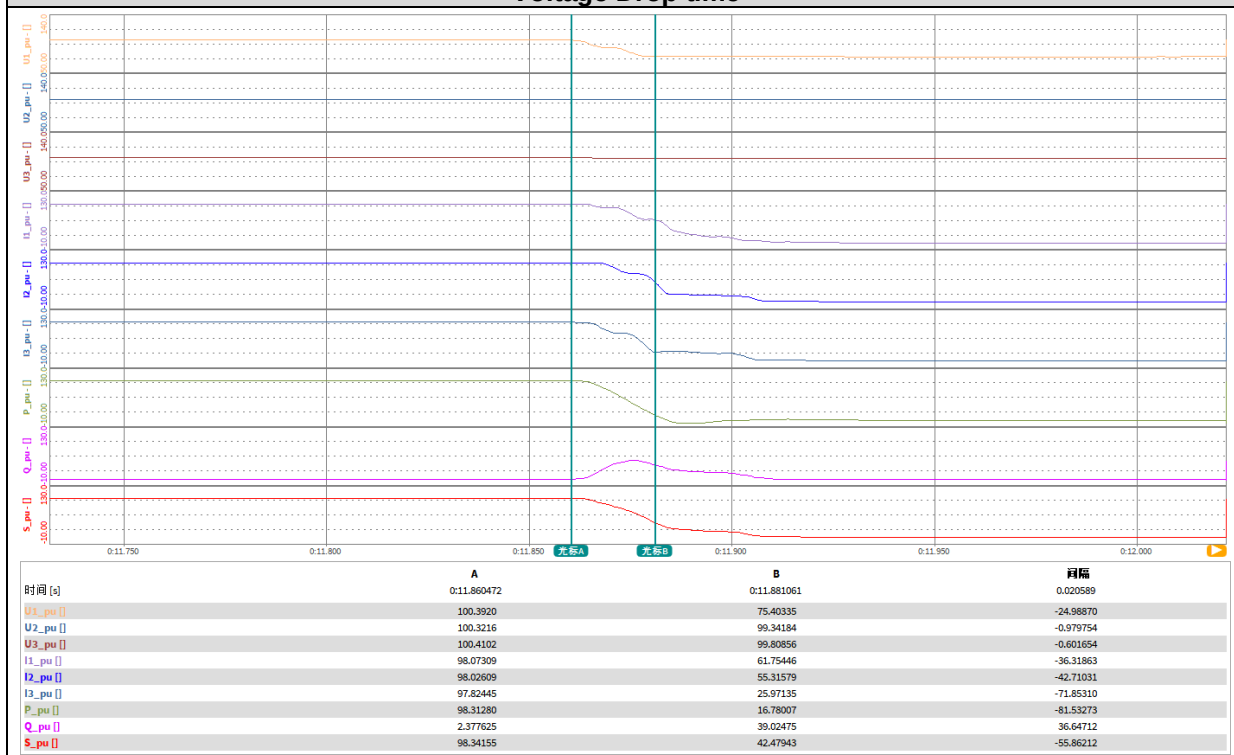


Single-Phase fault

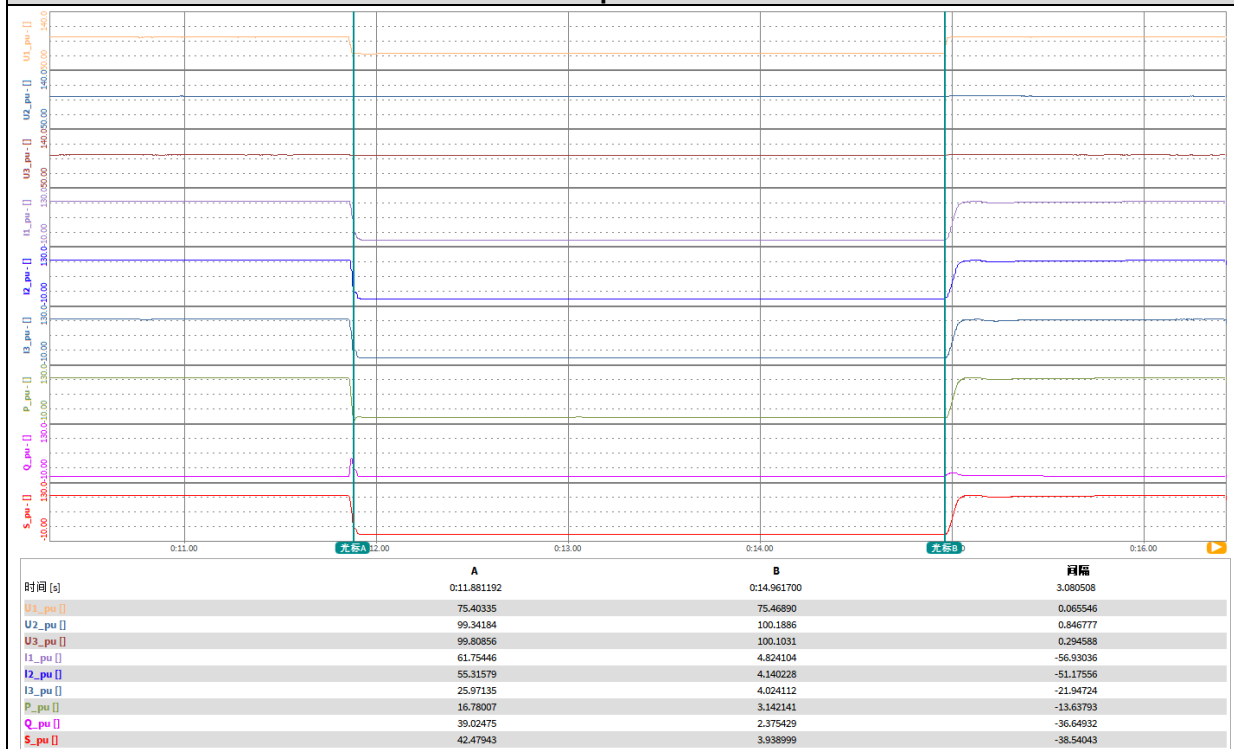
100%Pn Load

Residual voltage: 75%Un

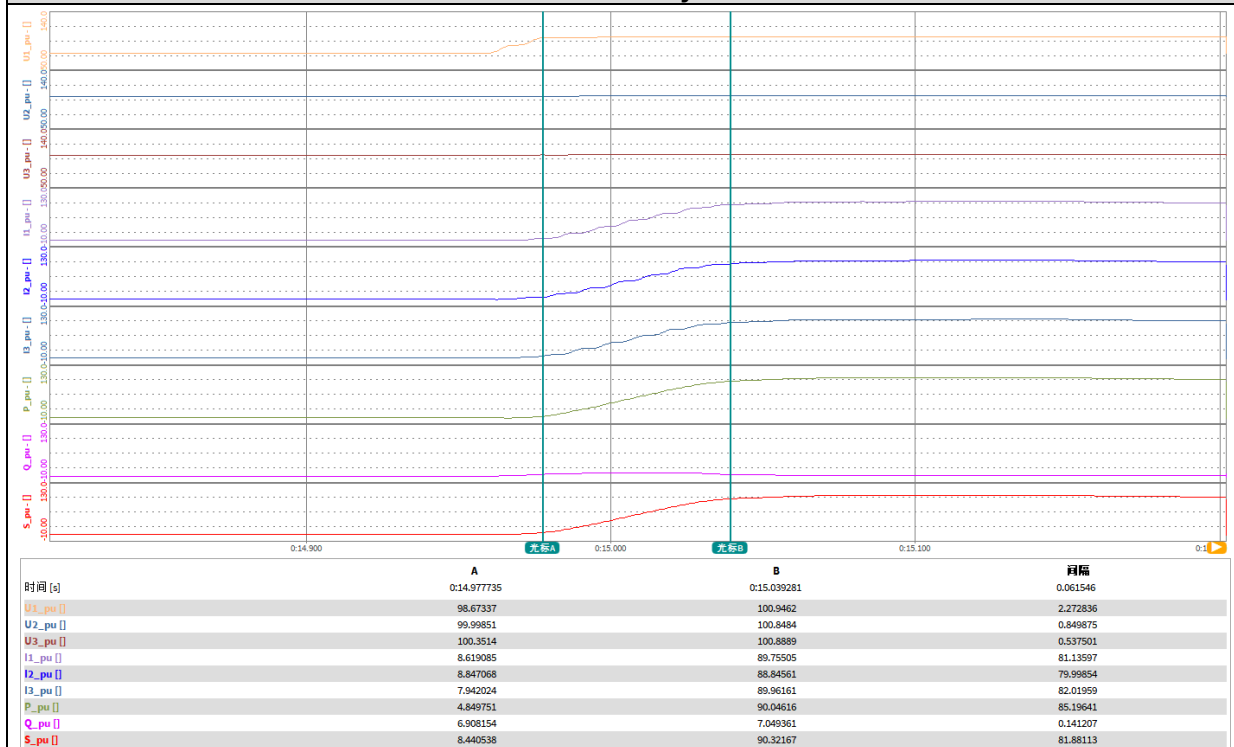
Voltage Drop time

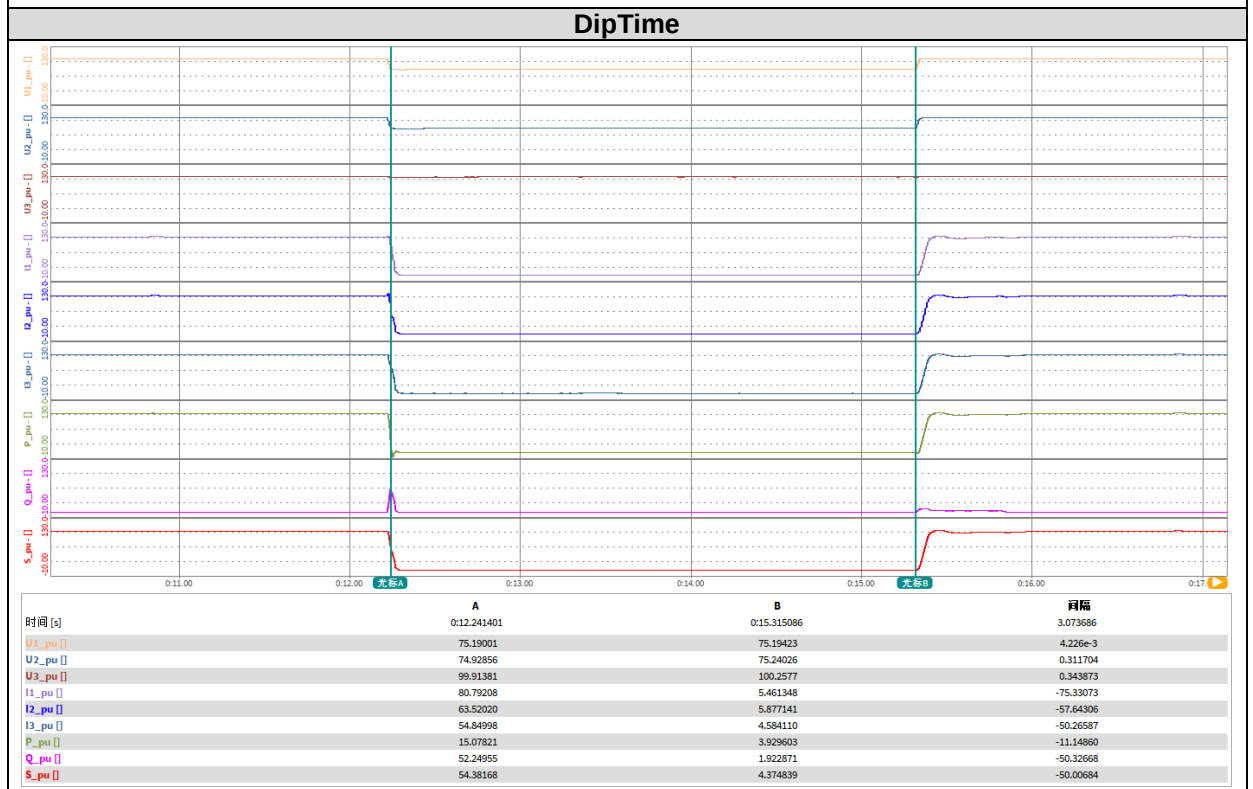
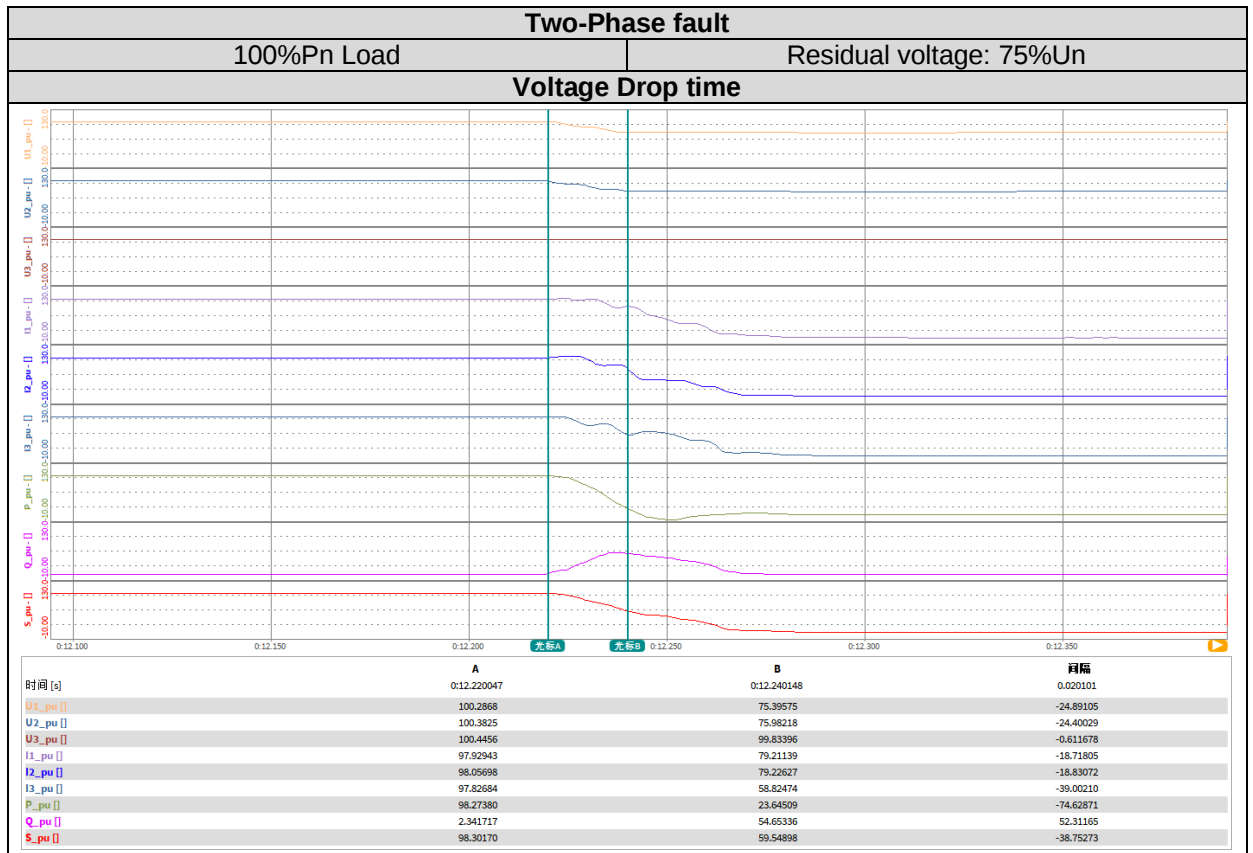


DipTime

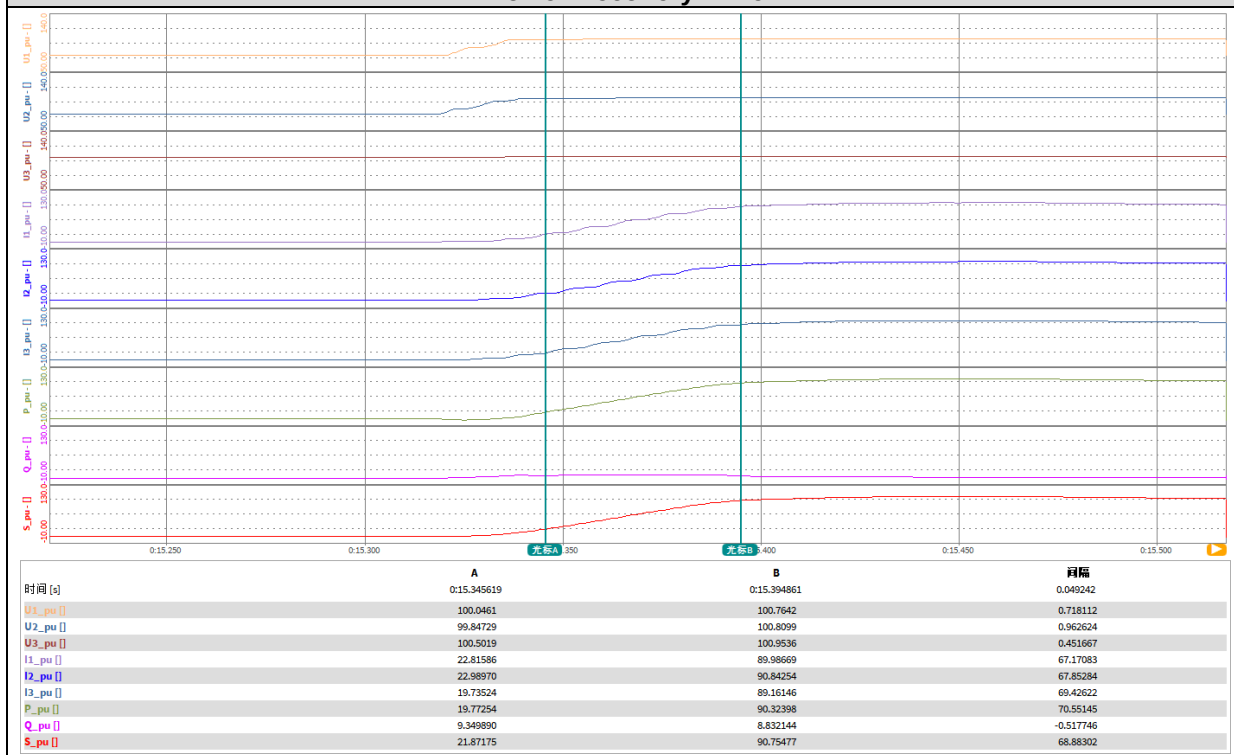


Power Recovery Time





Power Recovery Time

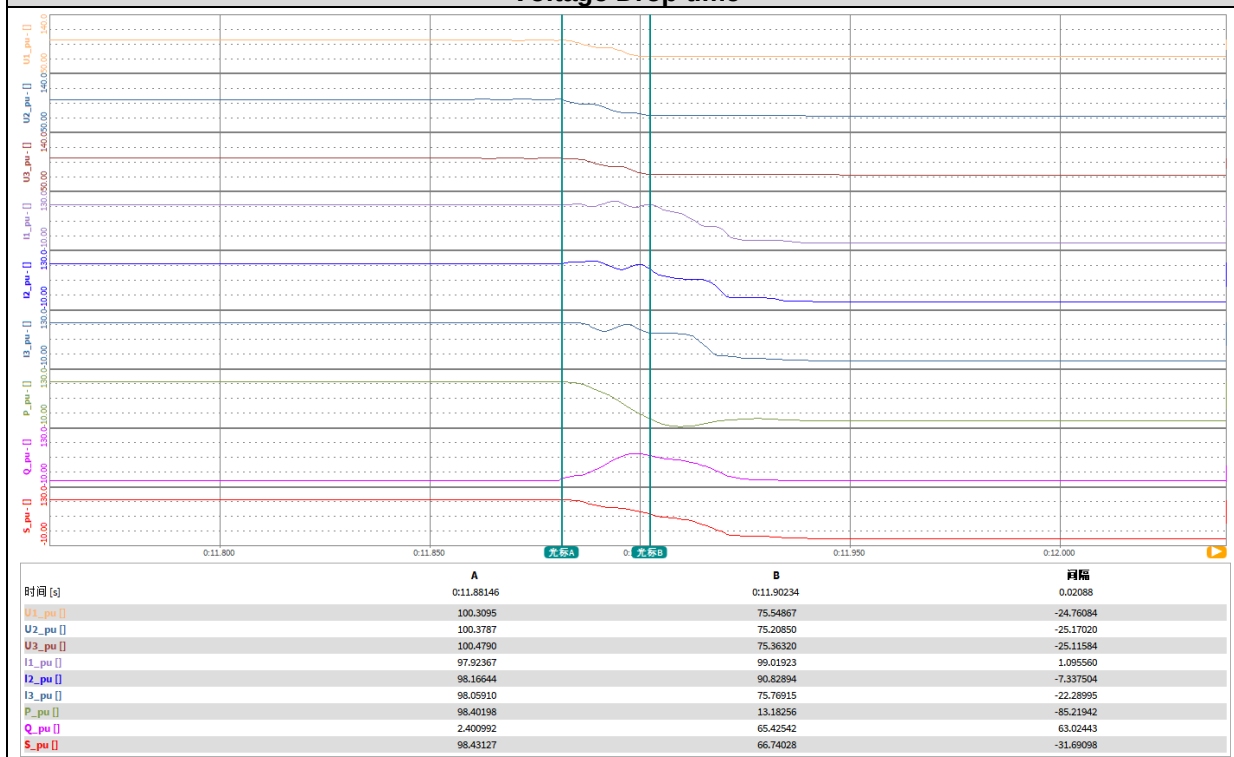


Three-Phase fault

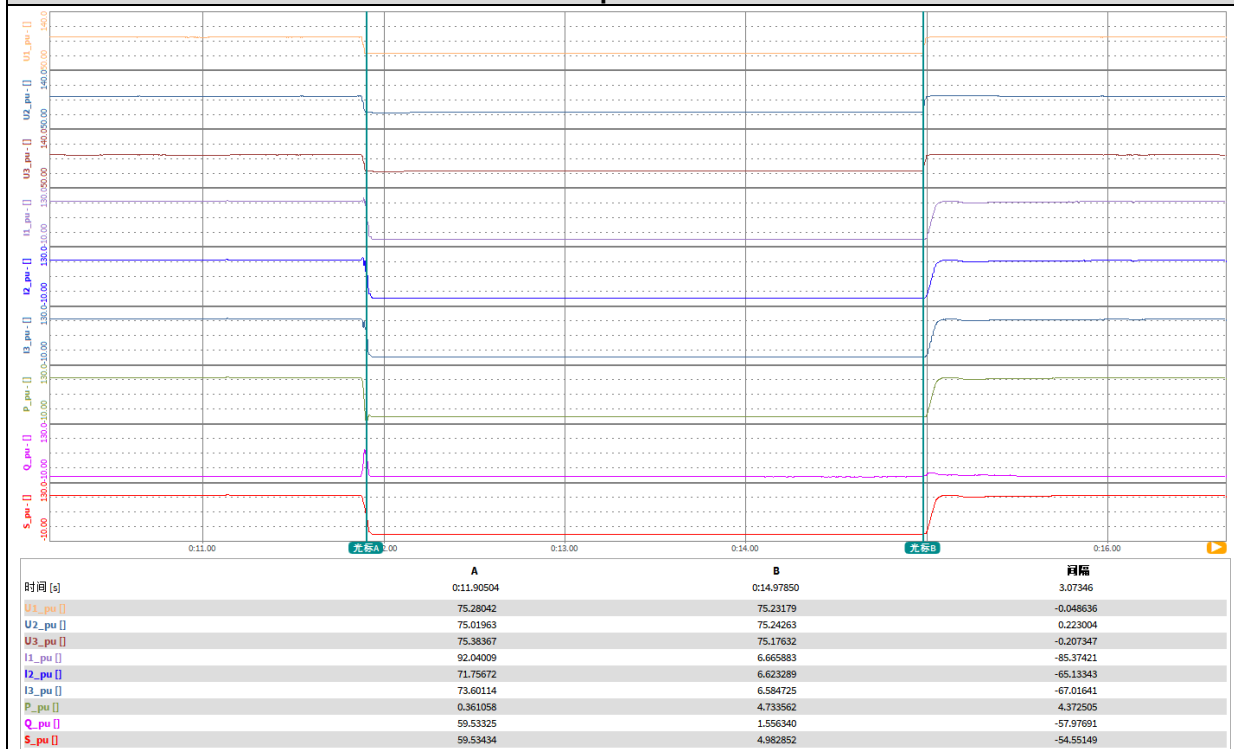
100%Pn Load

Residual voltage: 75%Un

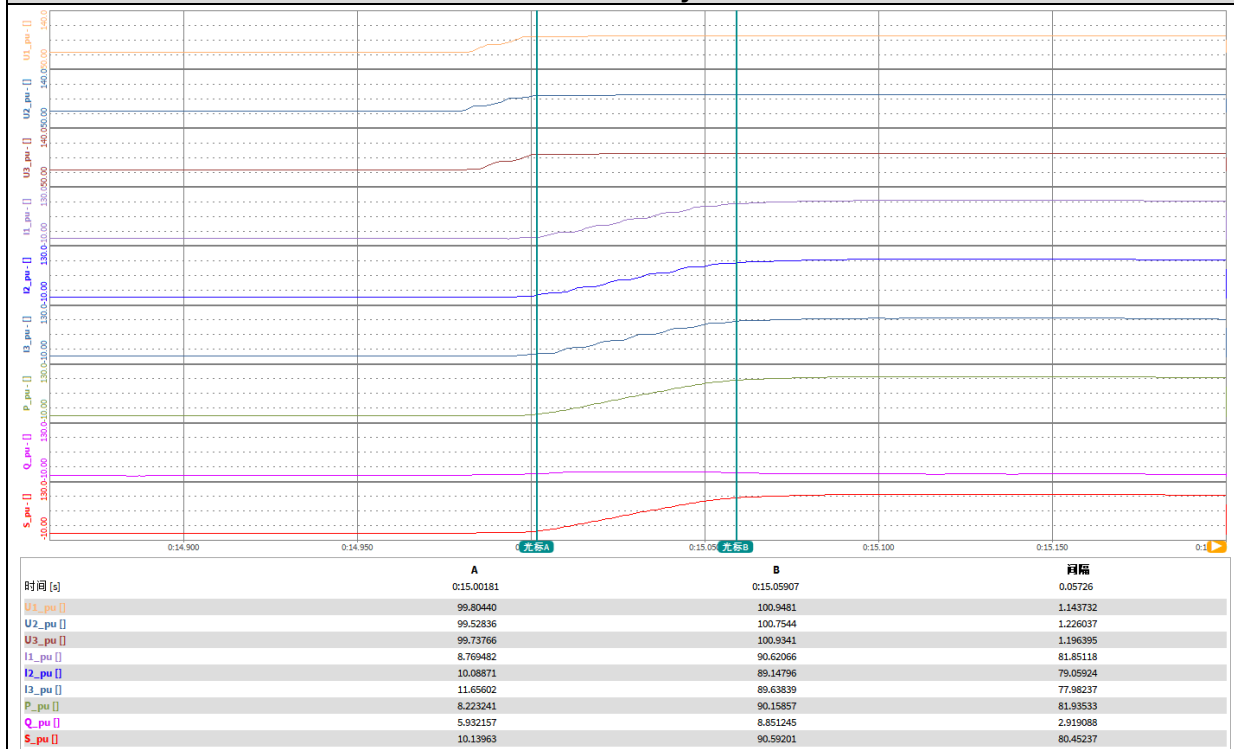
Voltage Drop time



DipTime



Power Recovery Time



4.2.3. Over-voltage ride through (OVRT)

The test has been done according to the clause 4.5.4 of the standard. The setting of over-voltage ride through capability is as follows:

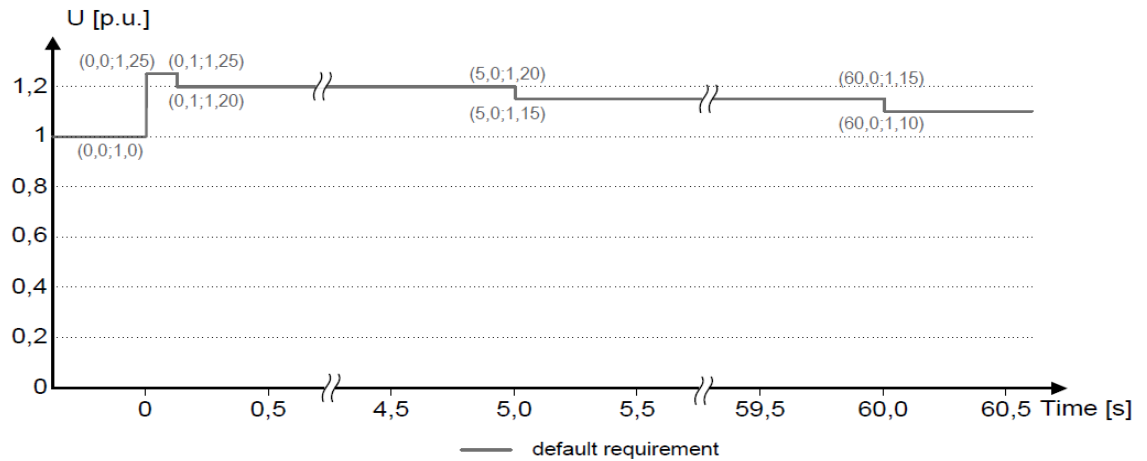
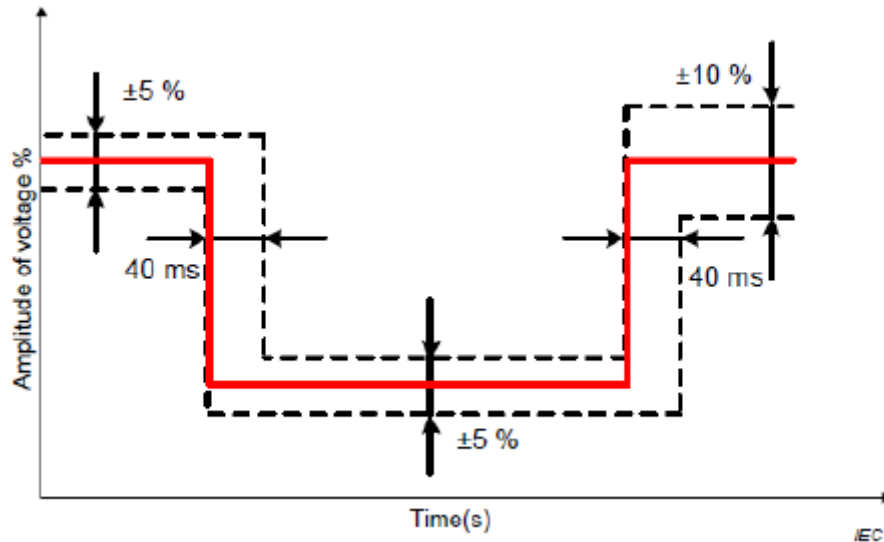


Figure 8 — Over-voltage ride through capability

4.2.3.1 No load Test

It is not specified in the reference standard, but following tolerances have been applied. Tolerances for drop depth and duration during no-load tests shall not exceed the values shown in the next figure:



The tolerance for voltage magnitude is $\pm 5\%U_n$ for the period before and during the voltage drop. The tolerance for voltage magnitude is $\pm 10\%U_n$ during the period after voltage is recovered. The tolerance range for both drop duration and rise time prefers 40 ms.

Test results of different no-load cases performed are offered below:

Single-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.0	21	125.0	> 120	131	--	100.0
120.0	100.0	20	120.0	> 5000	5062	--	100.0
115.0	100.0	19	114.9	> 60000	60069	--	100.0

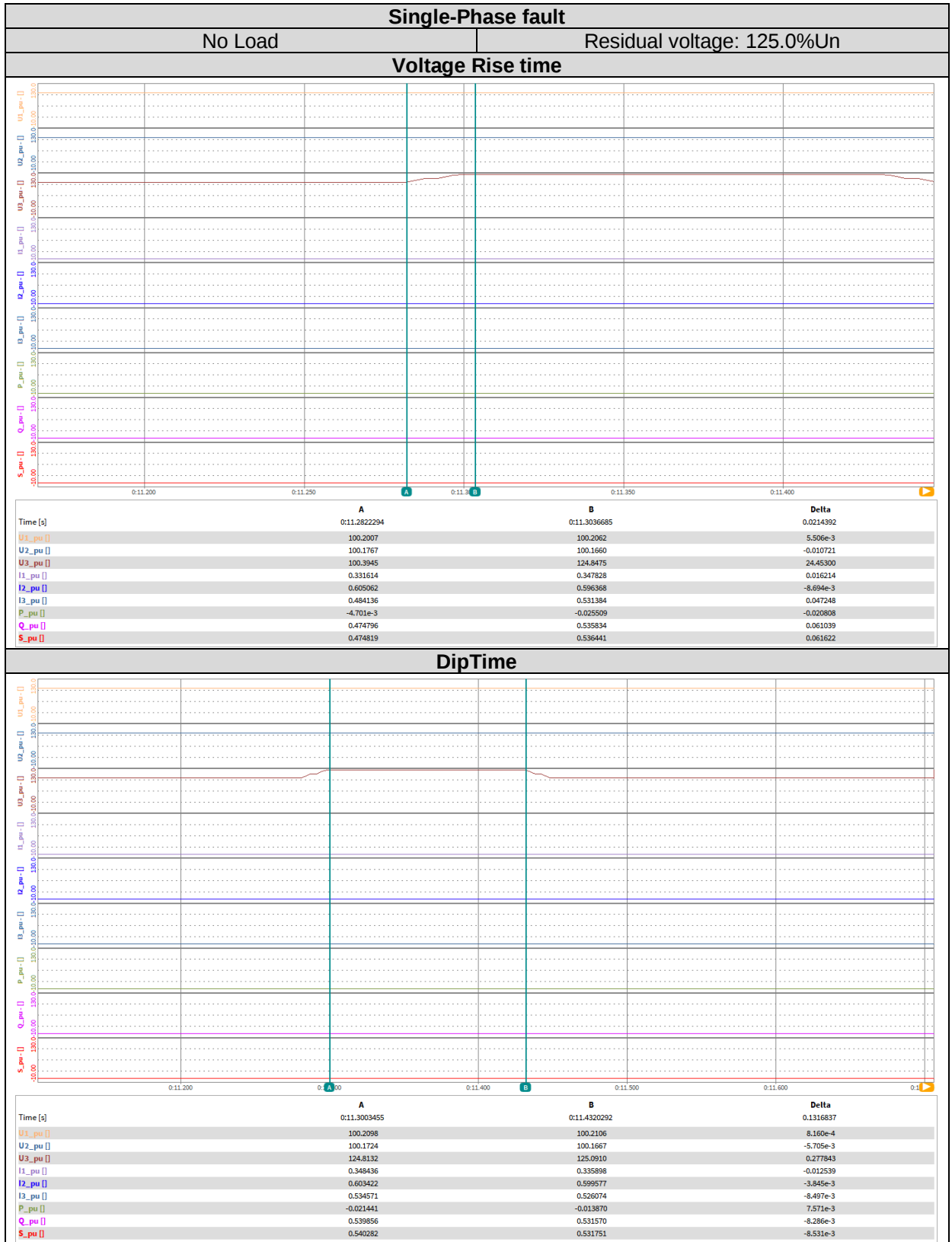
Two-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.0	23	124.7	> 120	128	--	100.0
120.0	100.0	20	119.8	> 5000	5060	--	100.0
115.0	100.0	22	115.0	> 60000	60076	--	100.0

Three-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.0	21	124.8	> 120	129	--	100.0
120.0	100.0	20	119.0	> 5000	5049	--	100.0
115.0	100.0	23	115.0	> 60000	60064	--	100.0

Remark: The virtual neutral line was used in the test.

Test results are graphically represented at following pages.

Note: The following test figure the voltage unit of U1_pu, U2_pu and U3_pu are %Un , the current unit of I1_pu, I2_pu and I3_pu are %In and the power unit is %Pn



Single-Phase fault

No Load

Residual voltage: 120.0%Un

Voltage Rise time



DipTime



Single-Phase fault

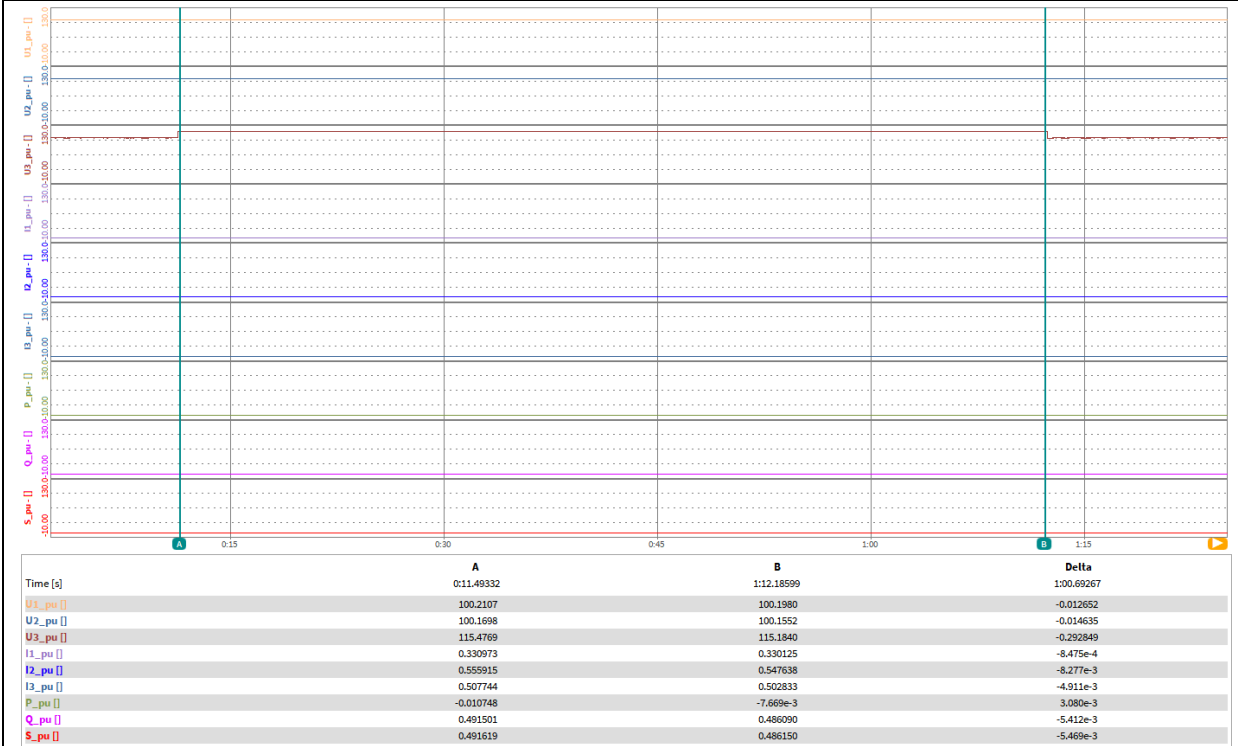
No Load

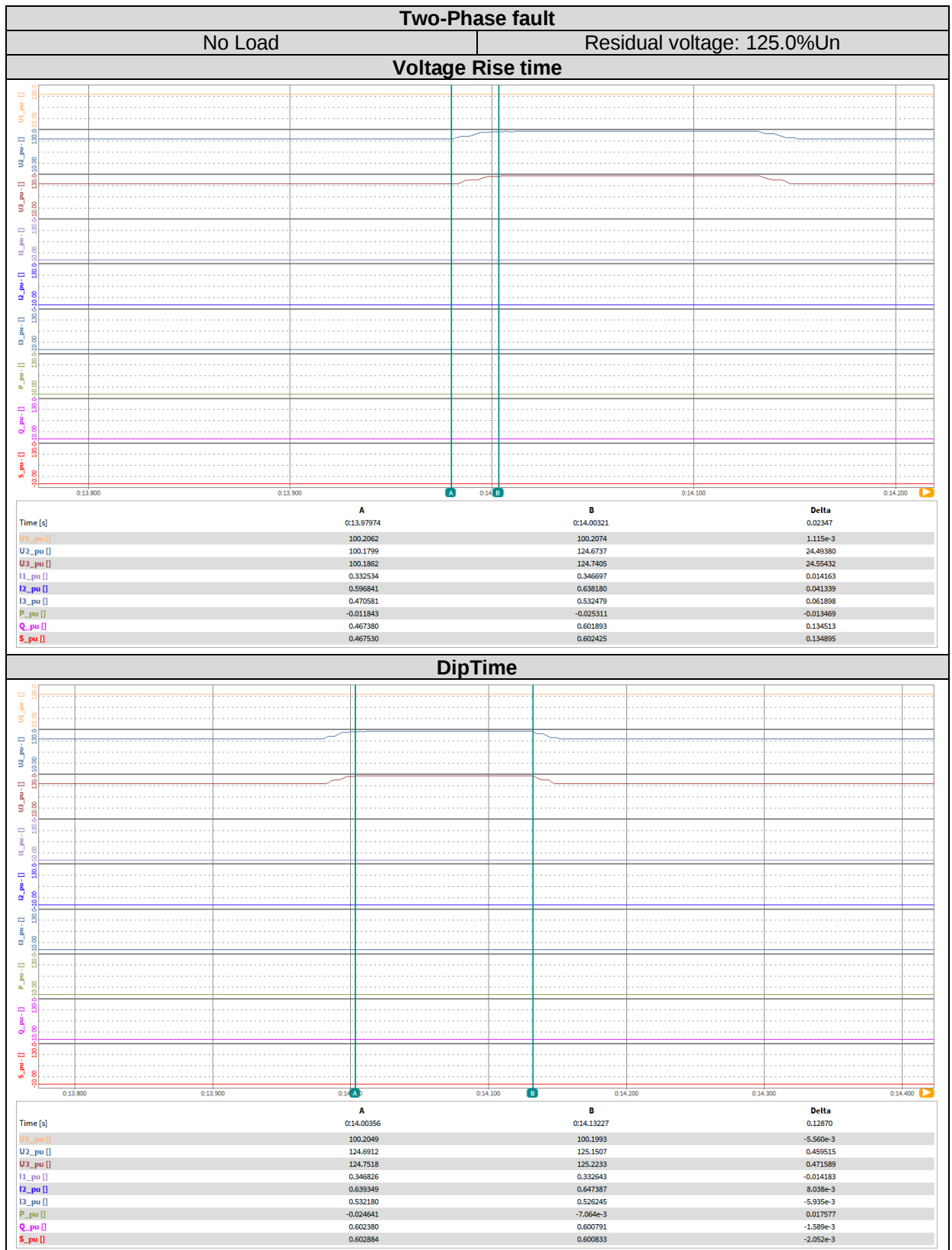
Residual voltage: 115.0%Un

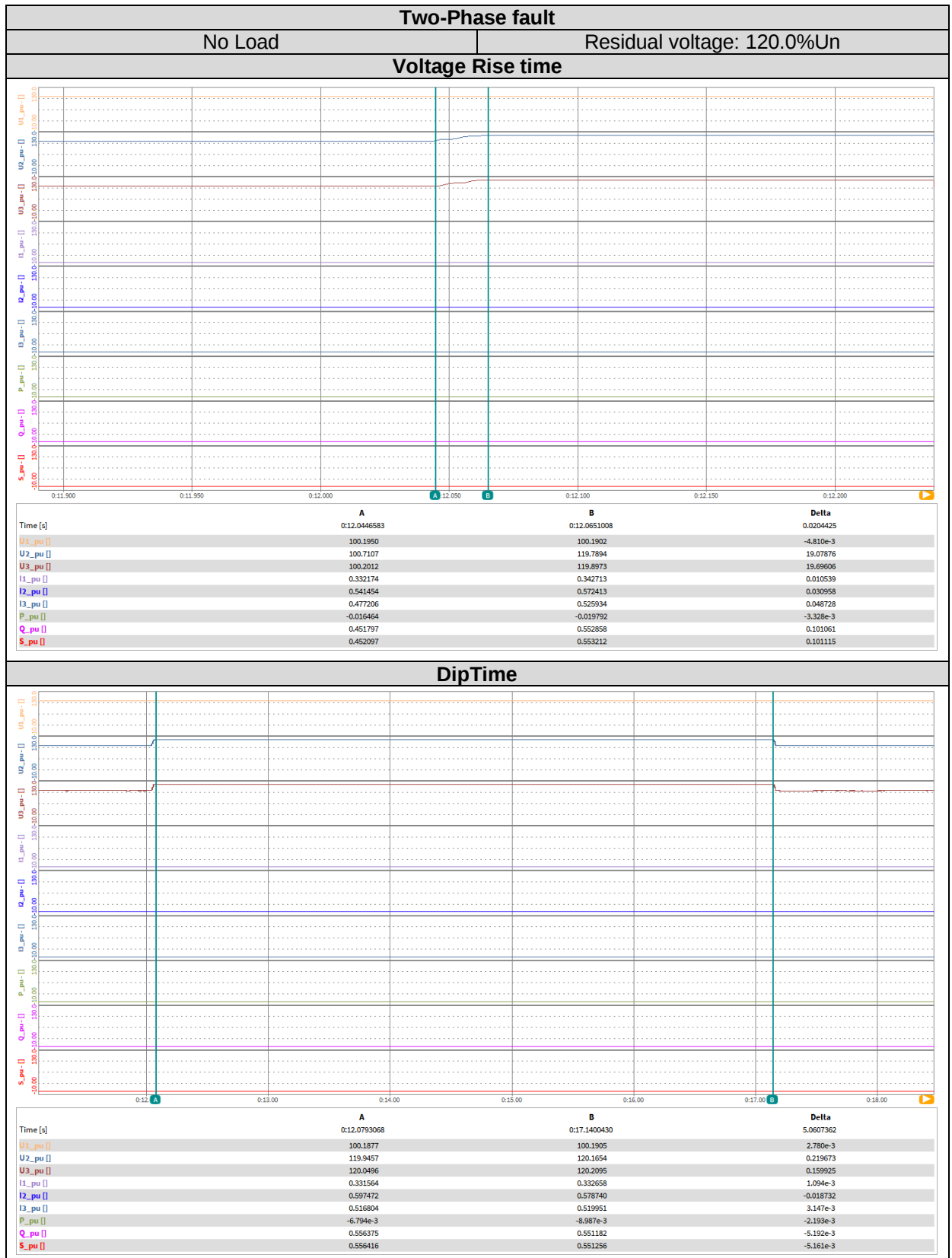
Voltage Rise time

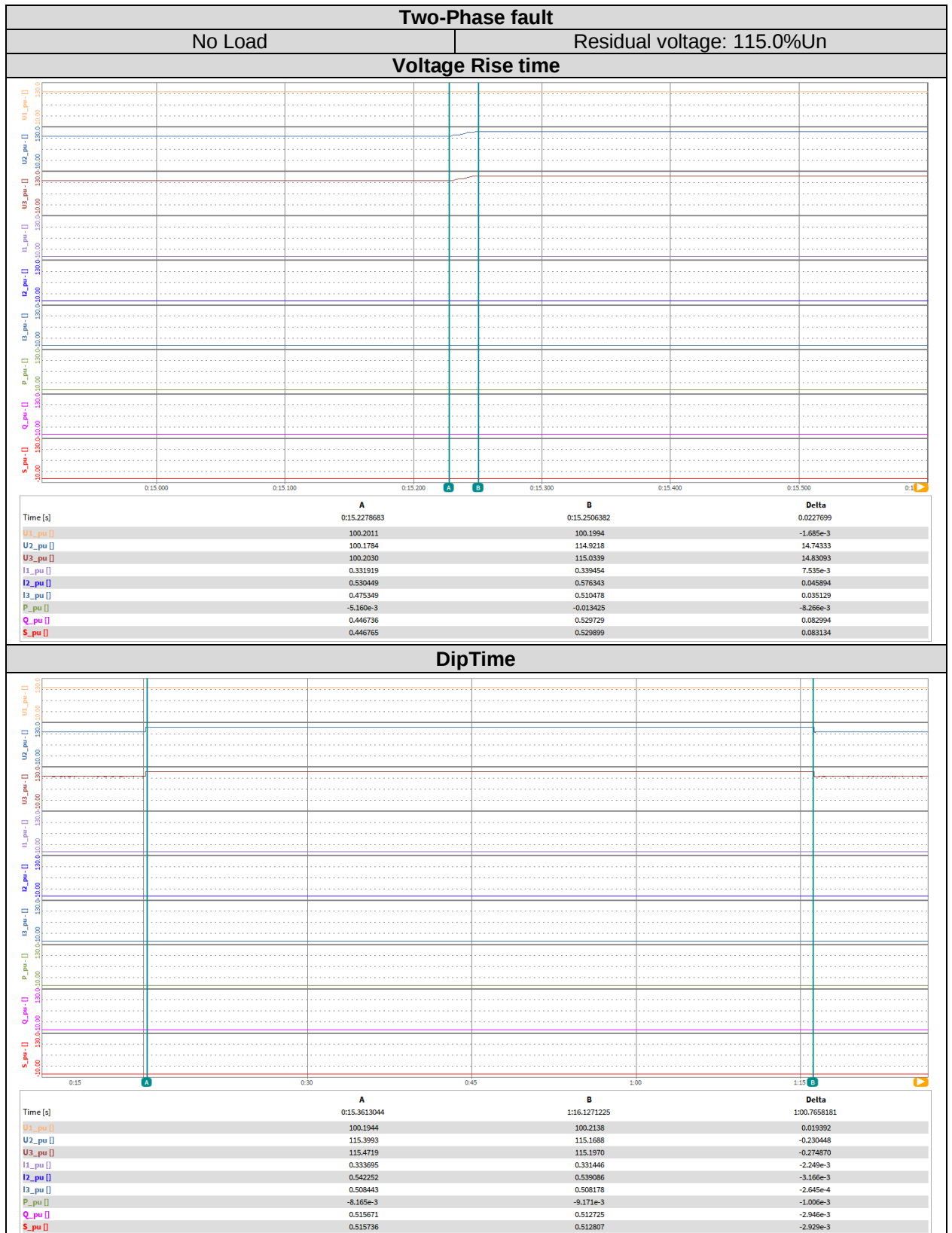


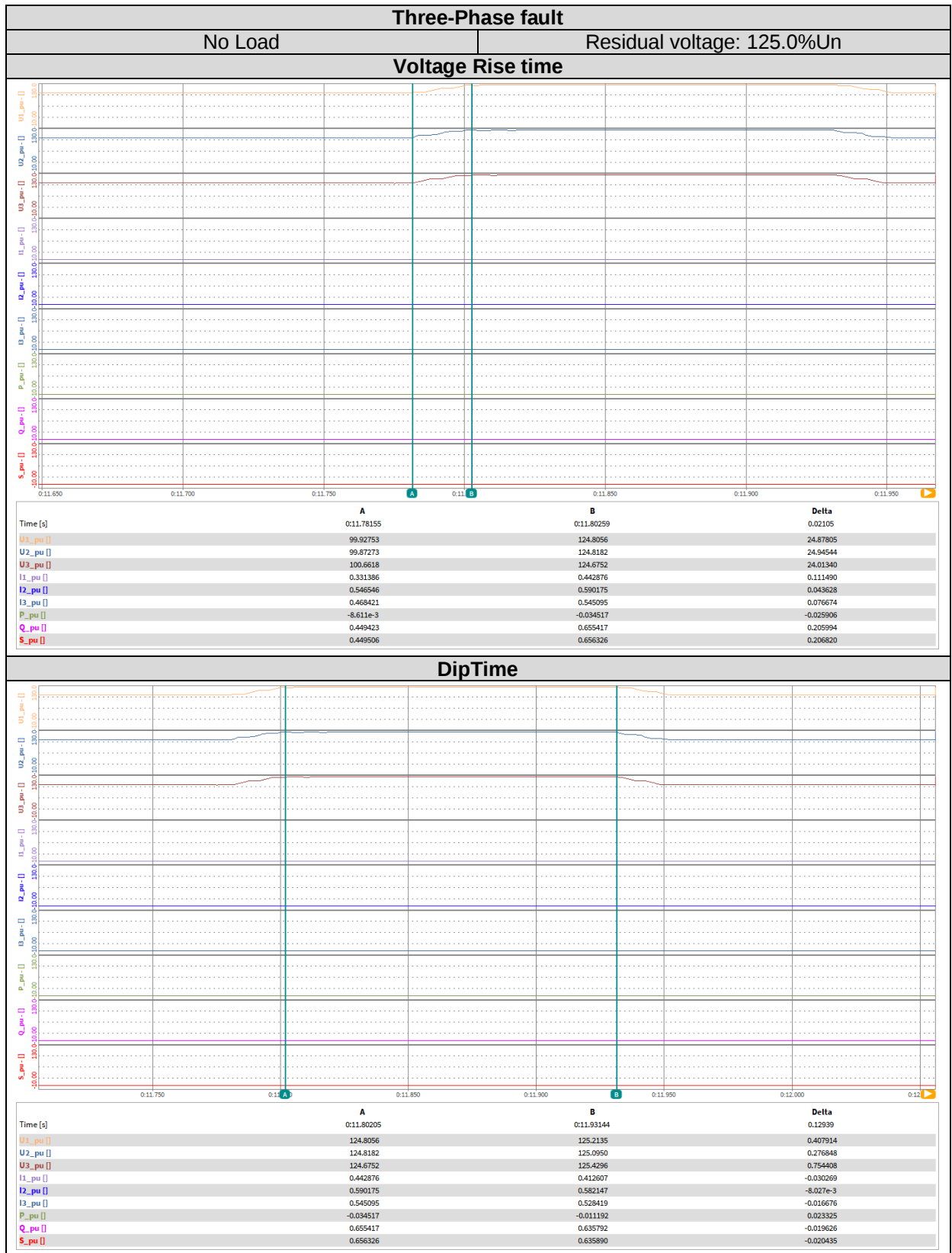
DipTime









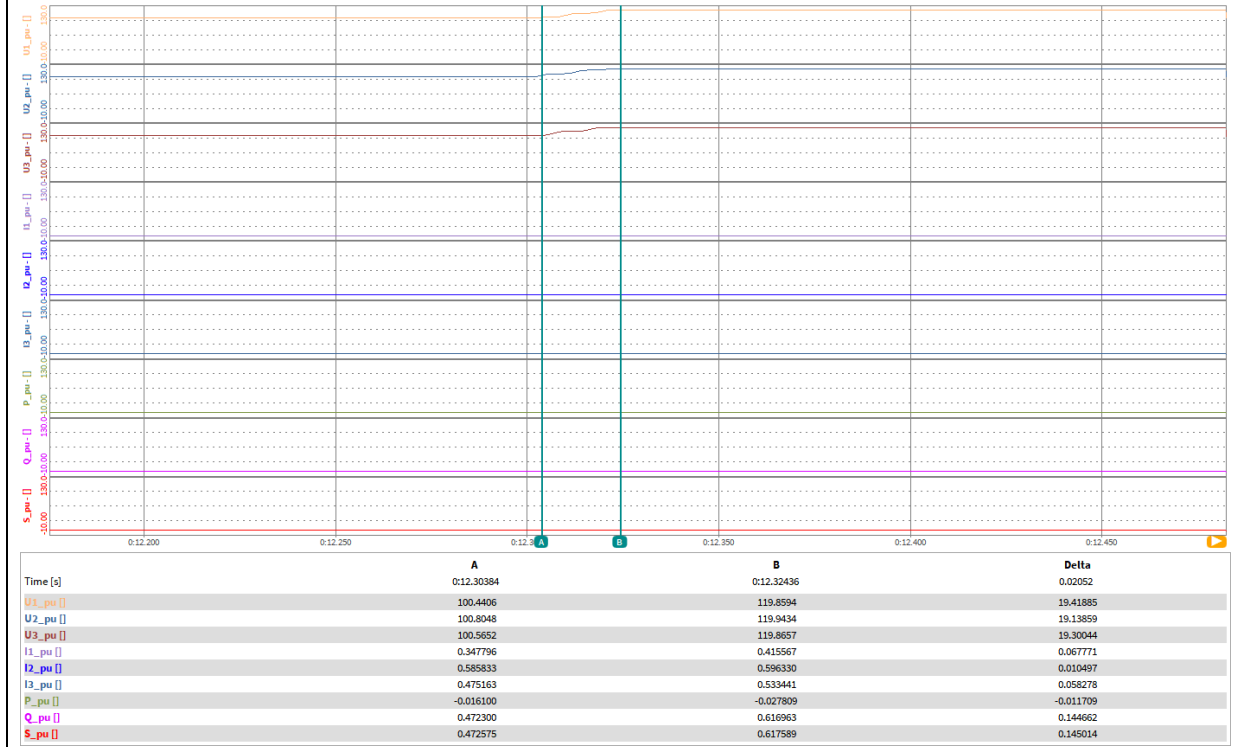


Three-Phase fault

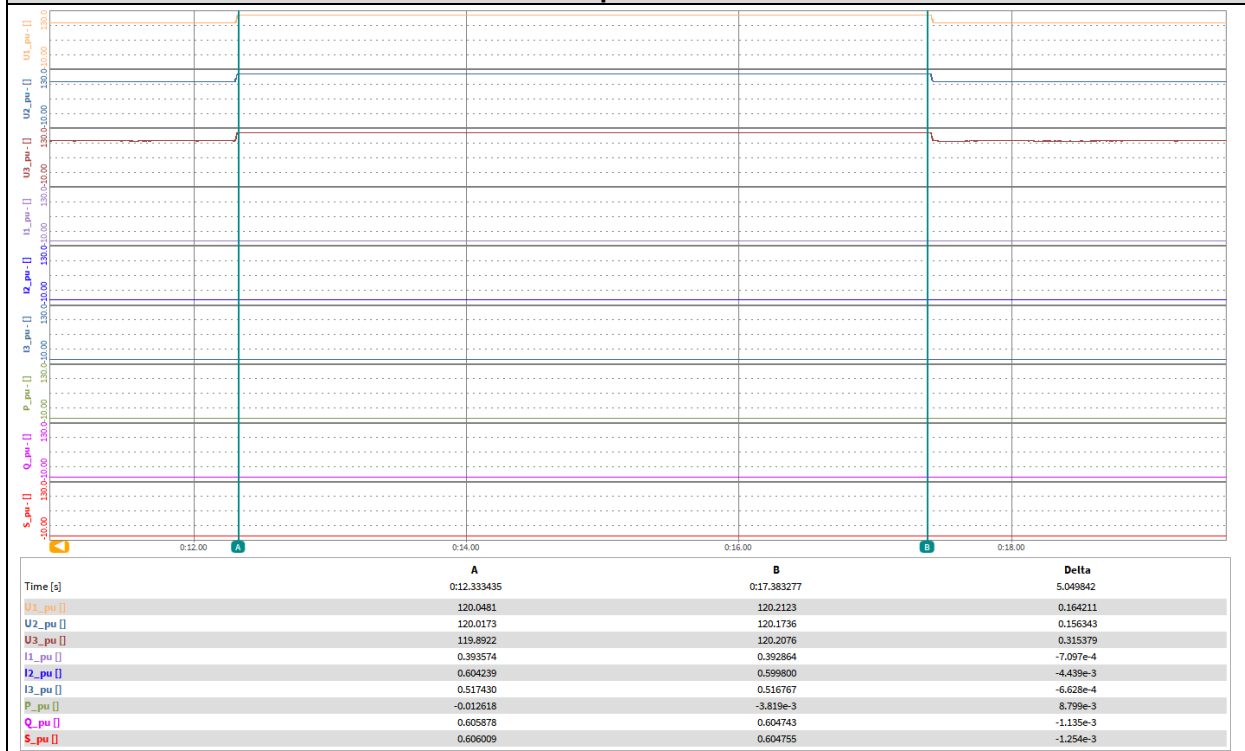
No Load

Residual voltage: 120.0%Un

Voltage Rise time



DipTime

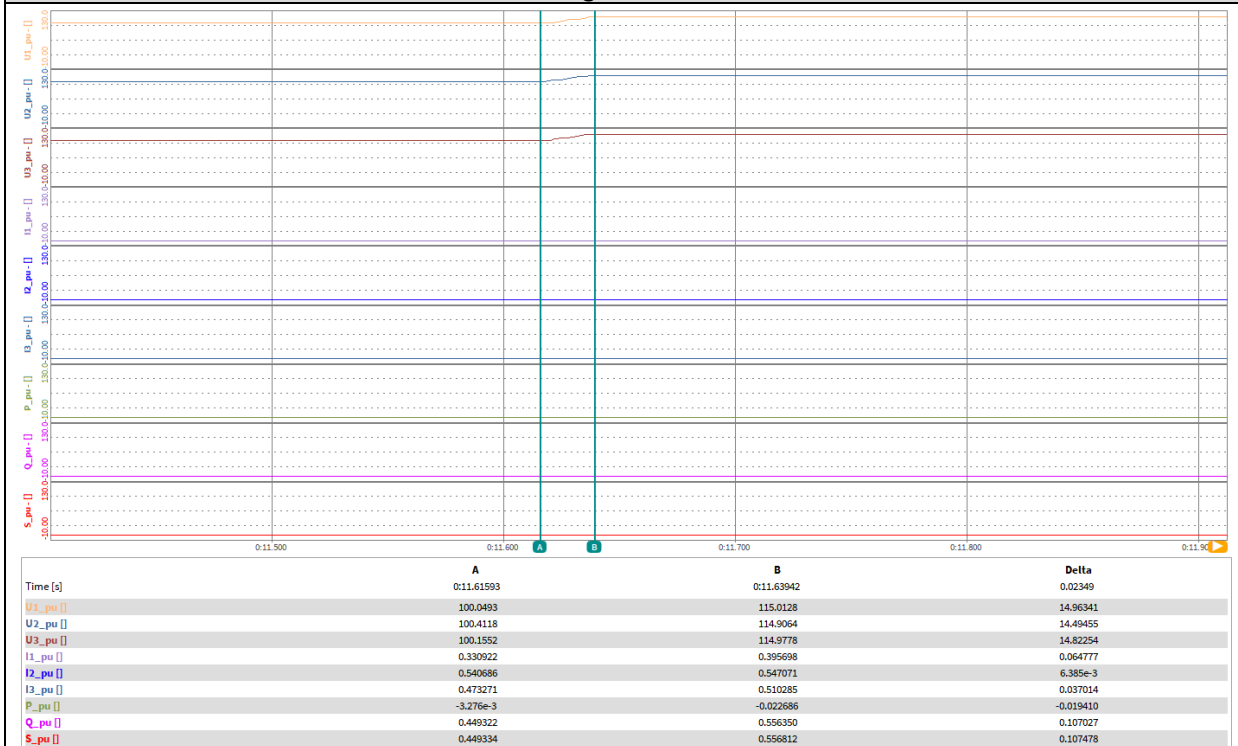


Three-Phase fault

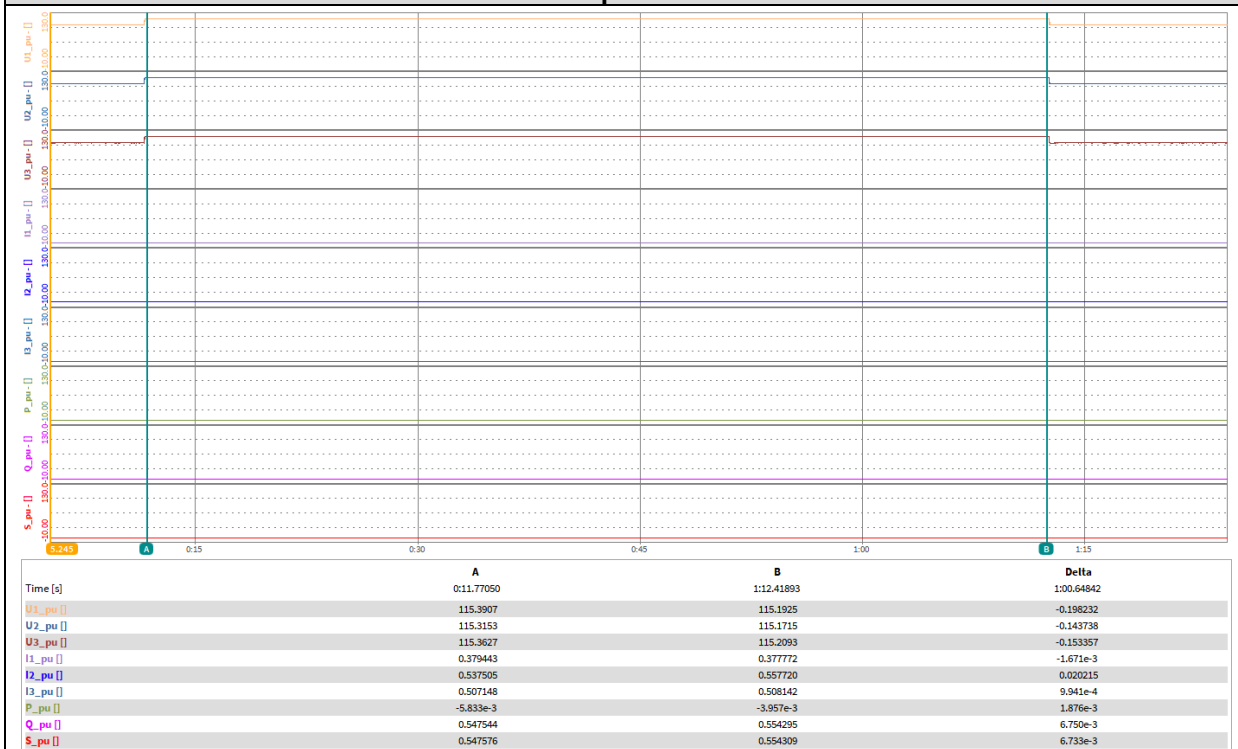
No Load

Residual voltage: 115.0%Un

Voltage Rise time



DipTime



4.2.3.2 Load Tests: Partial load (20%P_n)

Test results of full power cases performed are offered below:

Single-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.0	21	124.6	> 120	152	20	100.6
120.0	100.0	20	119.7	> 5000	5076	16	100.5
115.0	100.0	19	114.8	> 60000	60005	17	100.4

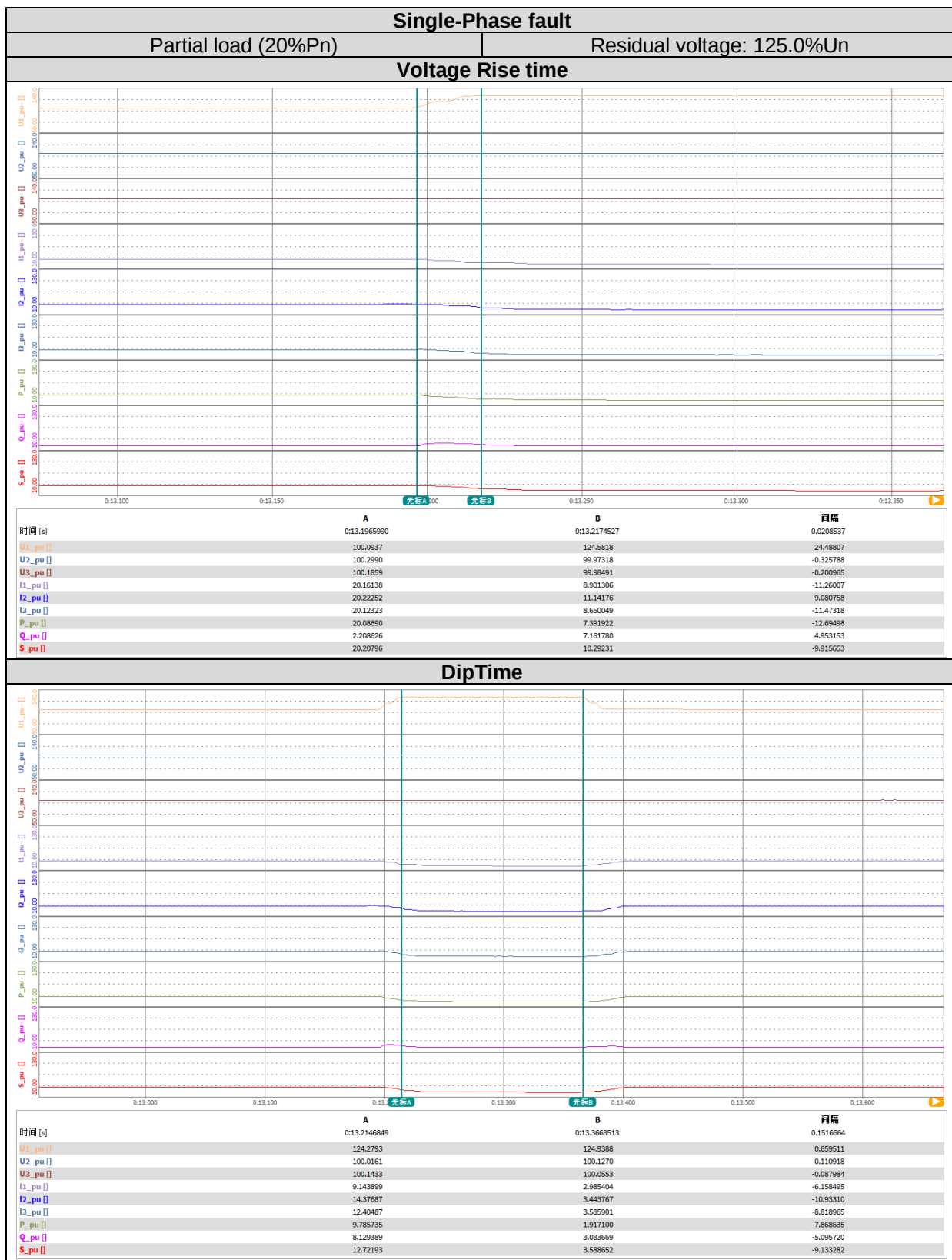
Two-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.0	21	124.5	> 120	150	37	100.4
120.0	100.0	20	119.6	> 5000	5076	16	100.6
115.0	100.0	20	114.8	> 60000	60007	388	100.2

Three-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.0	20	124.3	> 120	148	37	100.1
120.0	100.0	21	119.5	> 5000	5056	20	100.5
115.0	100.0	20	114.8	> 60000	60008	408	100.0

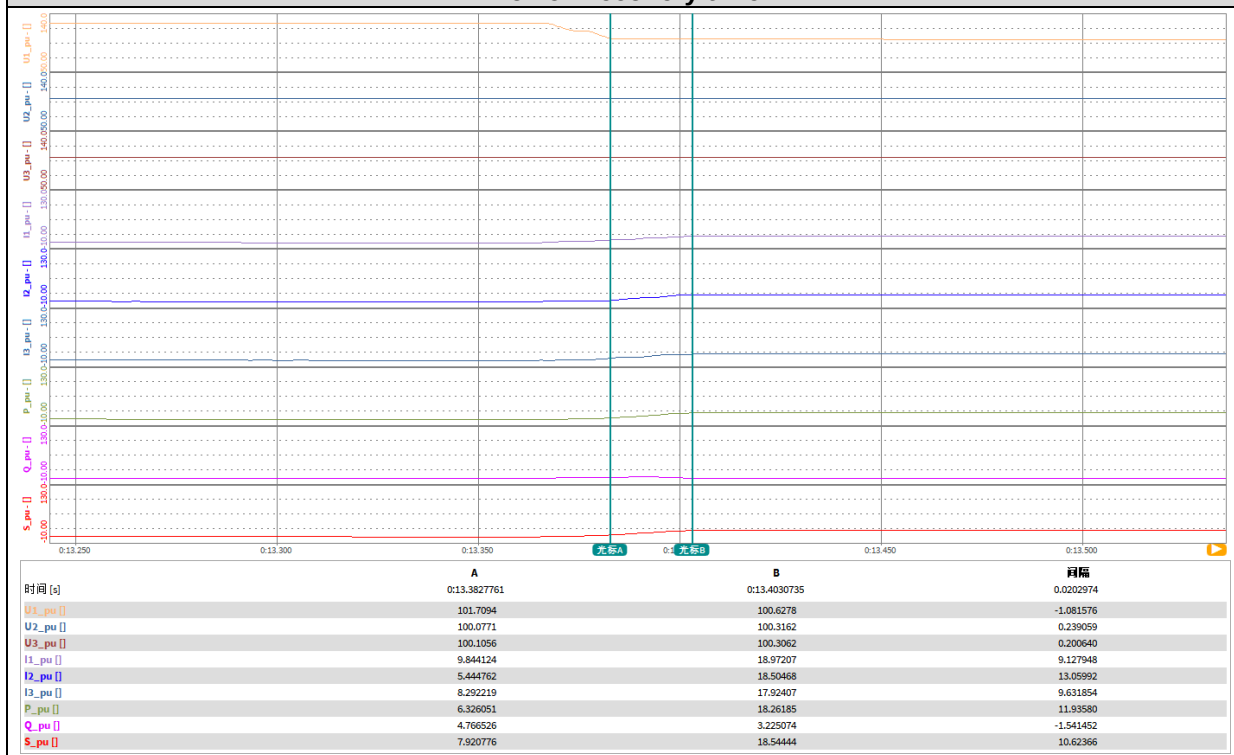
Remark: The virtual neutral line was used in the test.

Test results are graphically represented at following pages.

Note: The following test figure the voltage unit of U1_pu, U2_pu and U3_pu are %Un , the current unit of I1_pu, I2_pu and I3_pu are %In and the power unit is %Pn



Power Recovery time

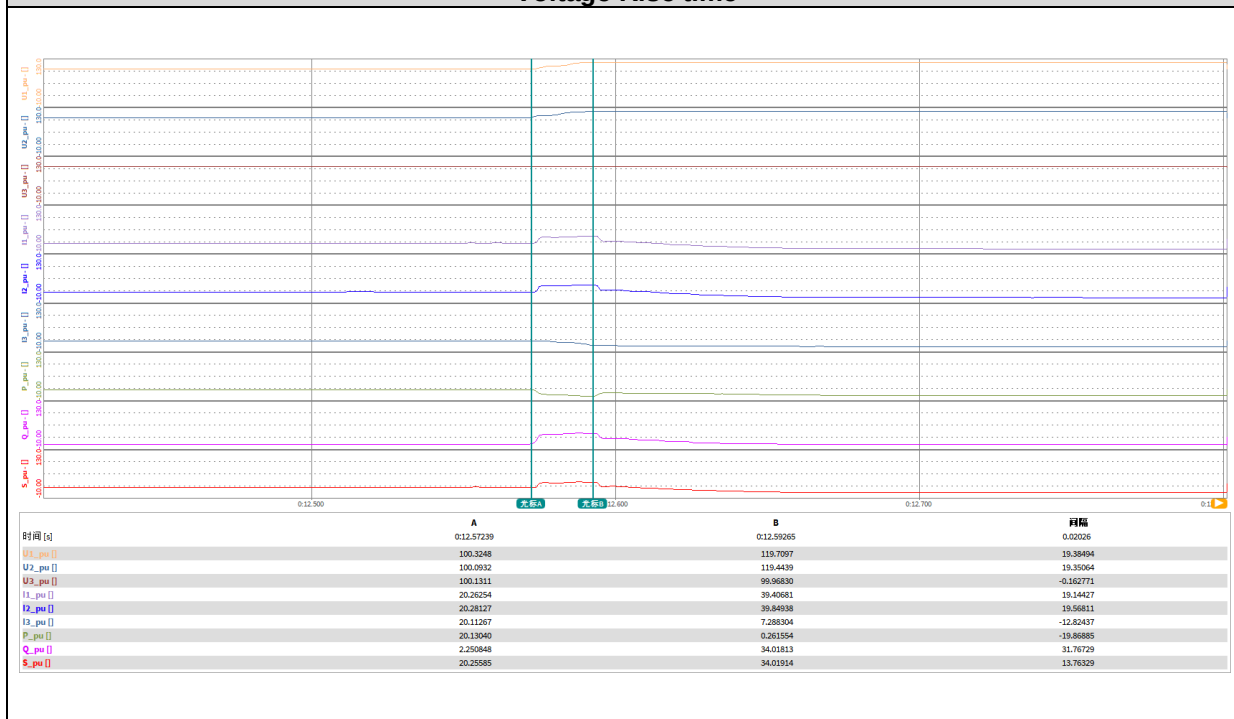


Single-Phase fault

Partial load (20%Pn)

Residual voltage: 120.0%Un

Voltage Rise time



DipTime



Power Recovery time

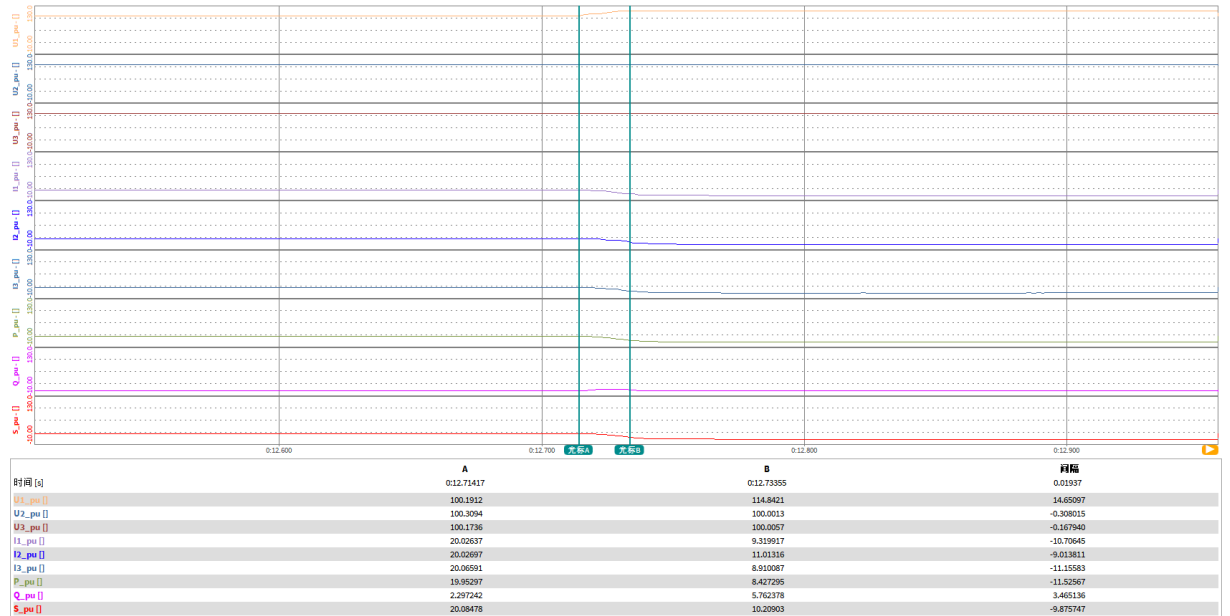


Single-Phase fault

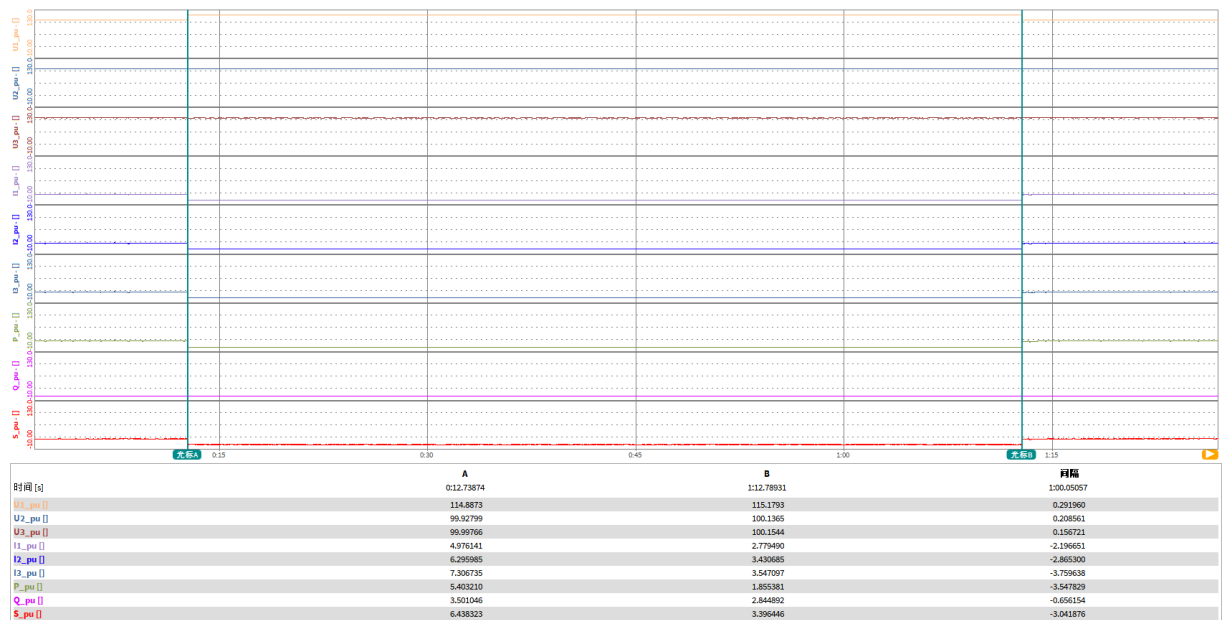
Partial load (20%Pn)

Residual voltage: 115.0%Un

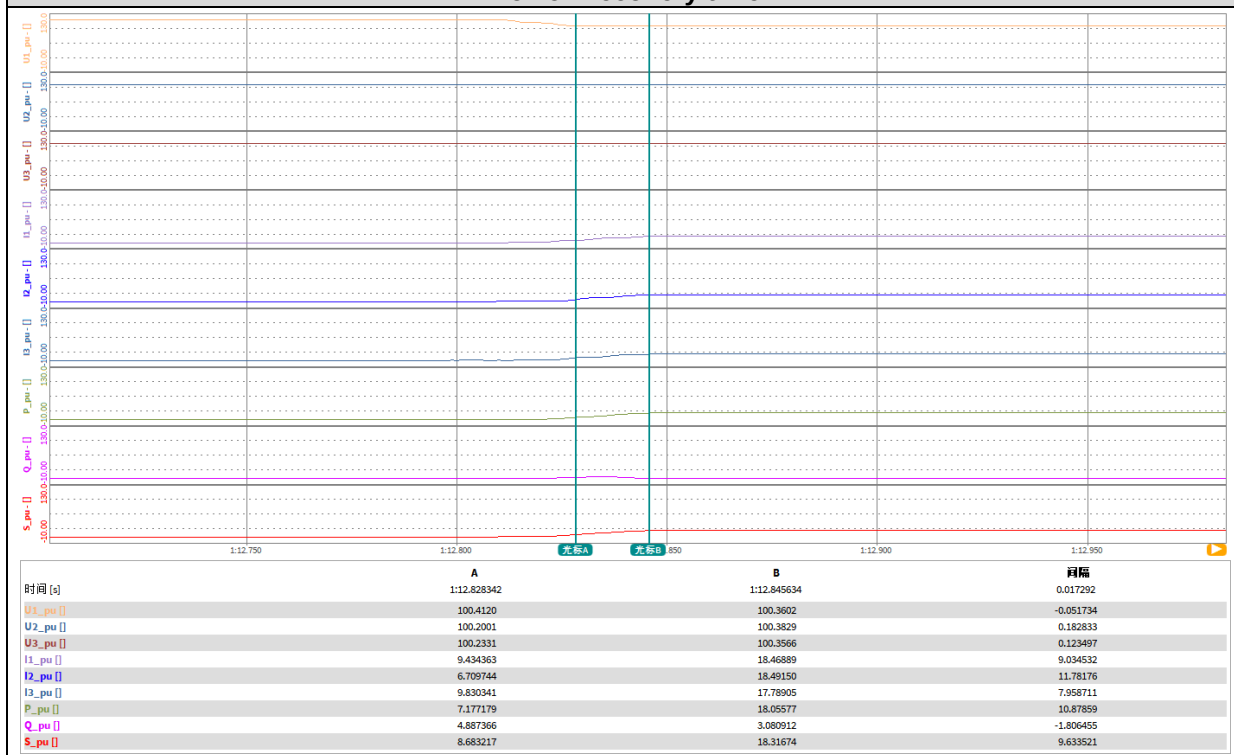
Voltage Rise time



DipTime



Power Recovery time

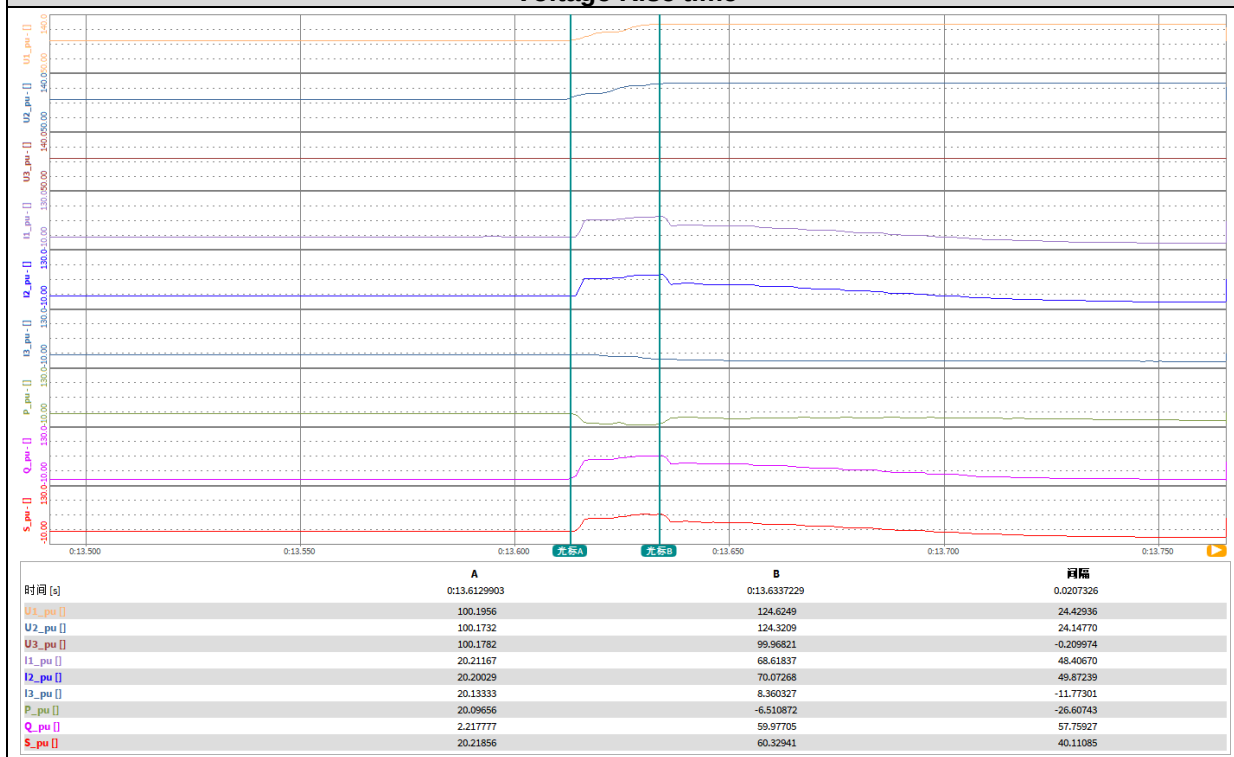


Two-Phase fault

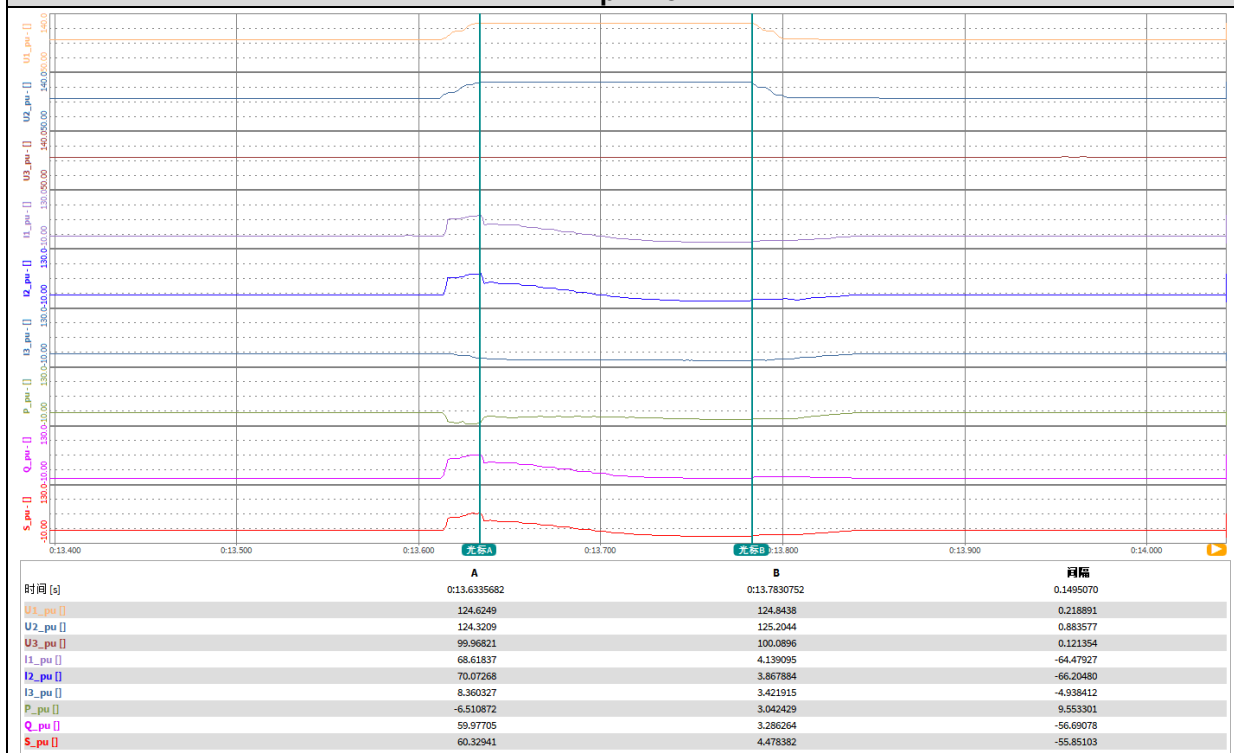
Partial load (20%Pn)

Residual voltage: 125.0%Un

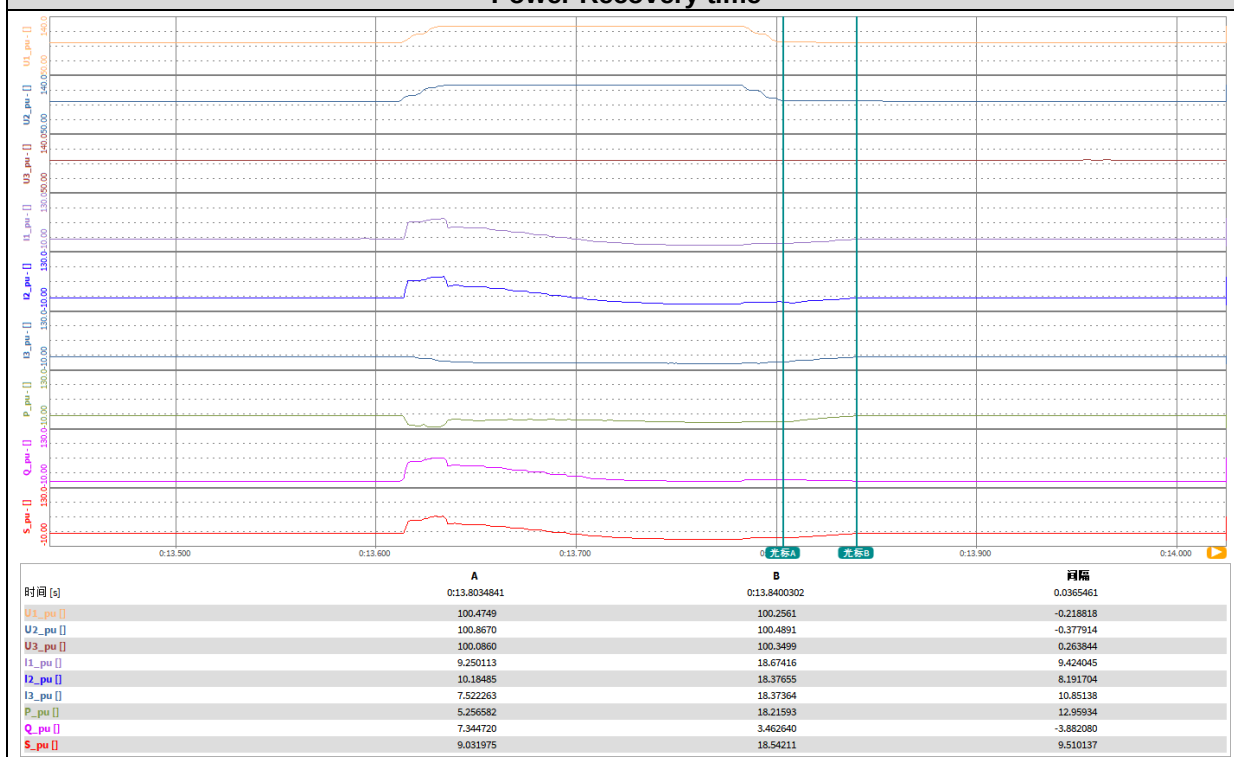
Voltage Rise time



DipTime



Power Recovery time

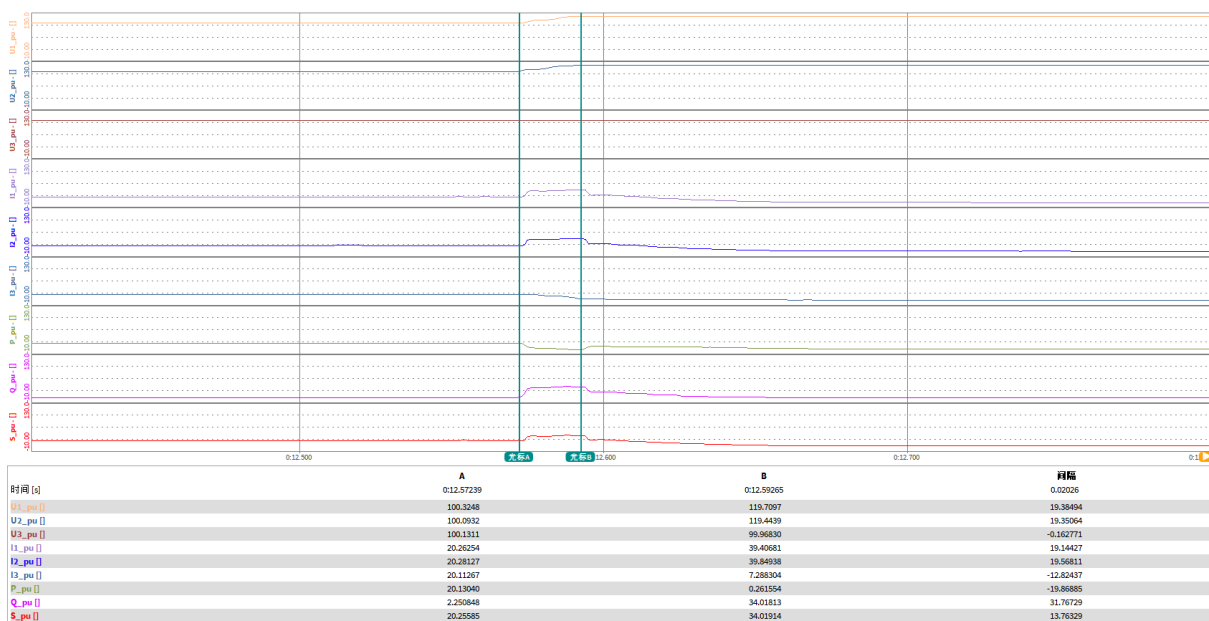


Two-Phase fault

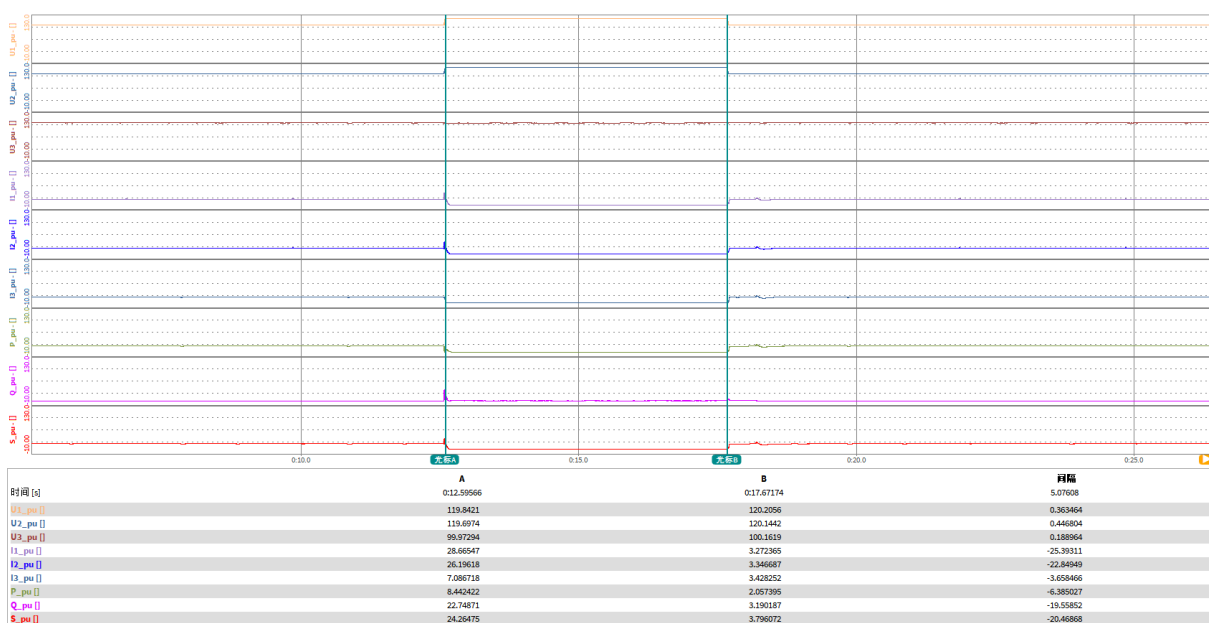
Partial load (20%Pn)

Residual voltage: 120.0%Un

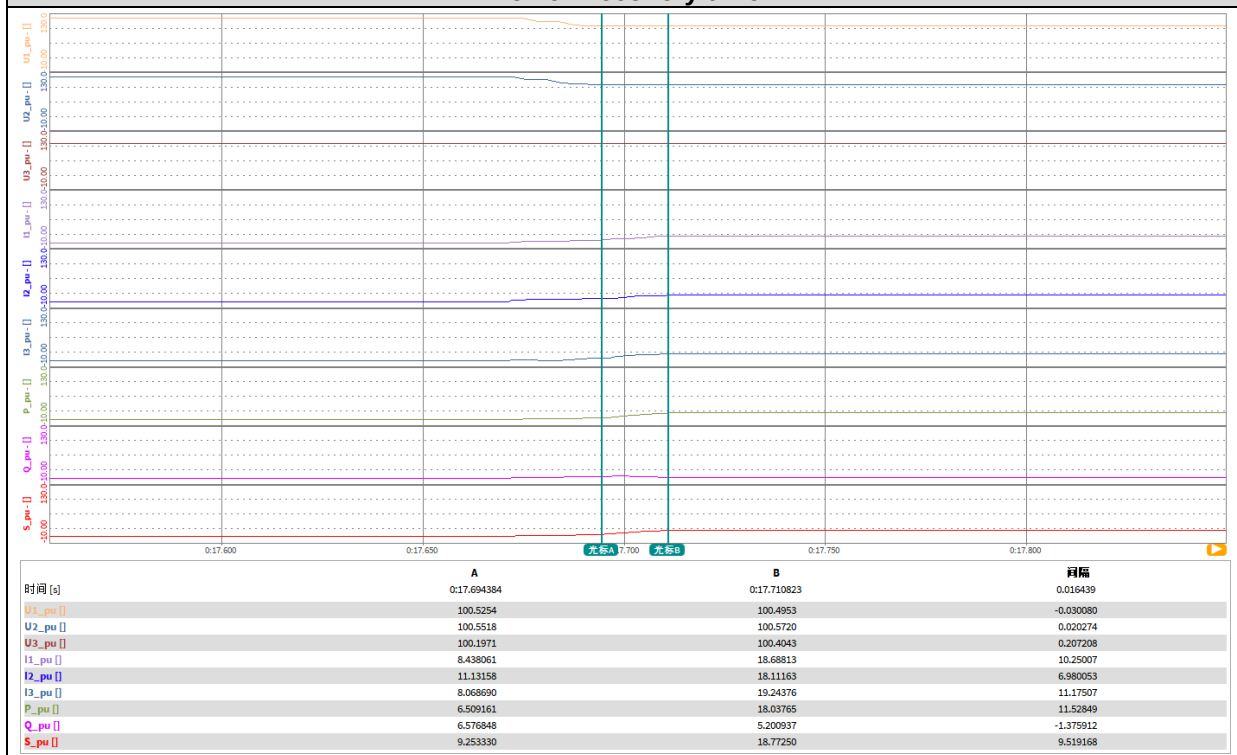
Voltage Rise time



DipTime



Power Recovery time

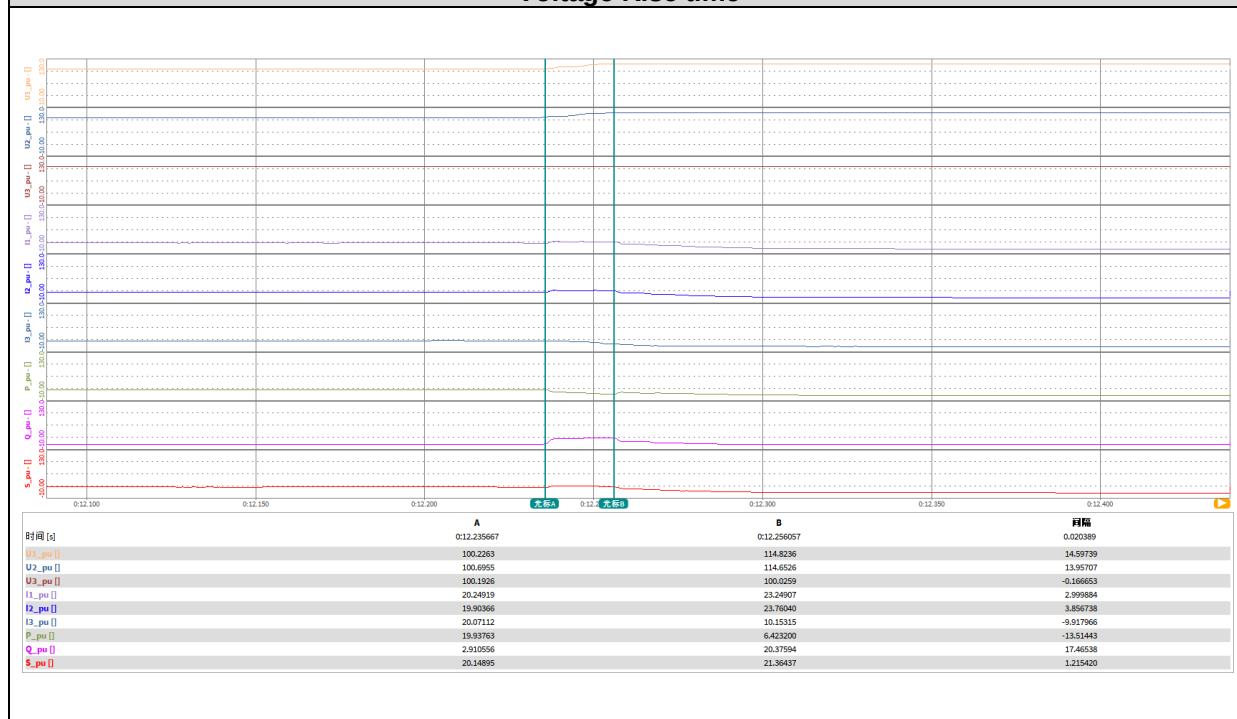


Two-Phase fault

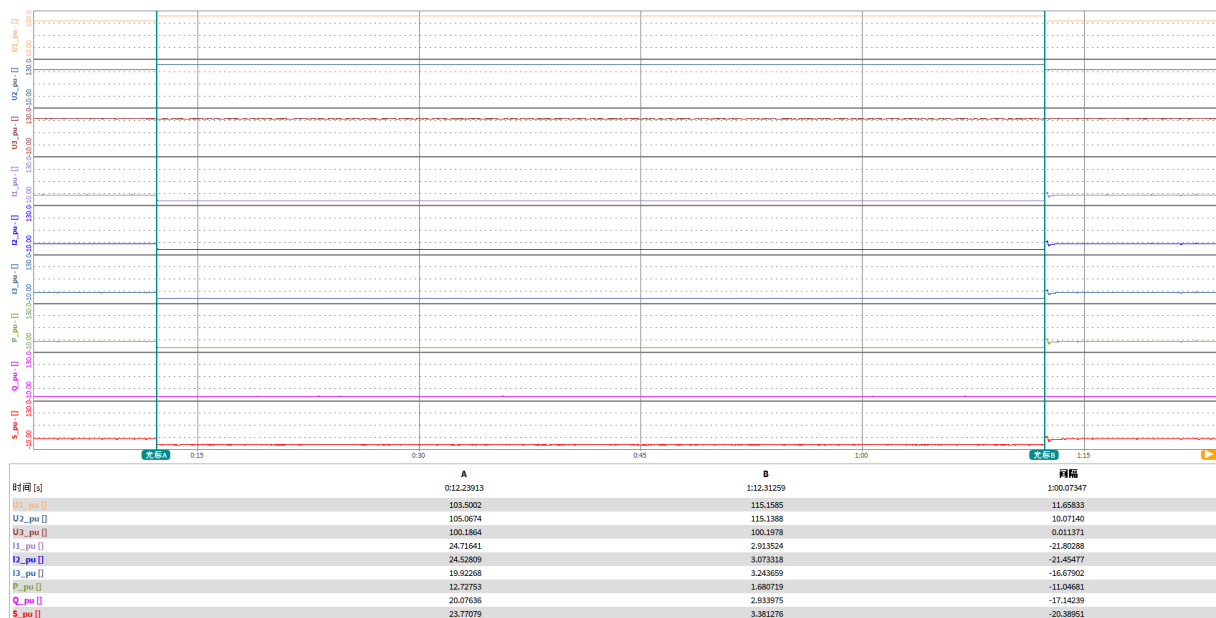
Partial load (20%Pn)

Residual voltage: 115.0%Un

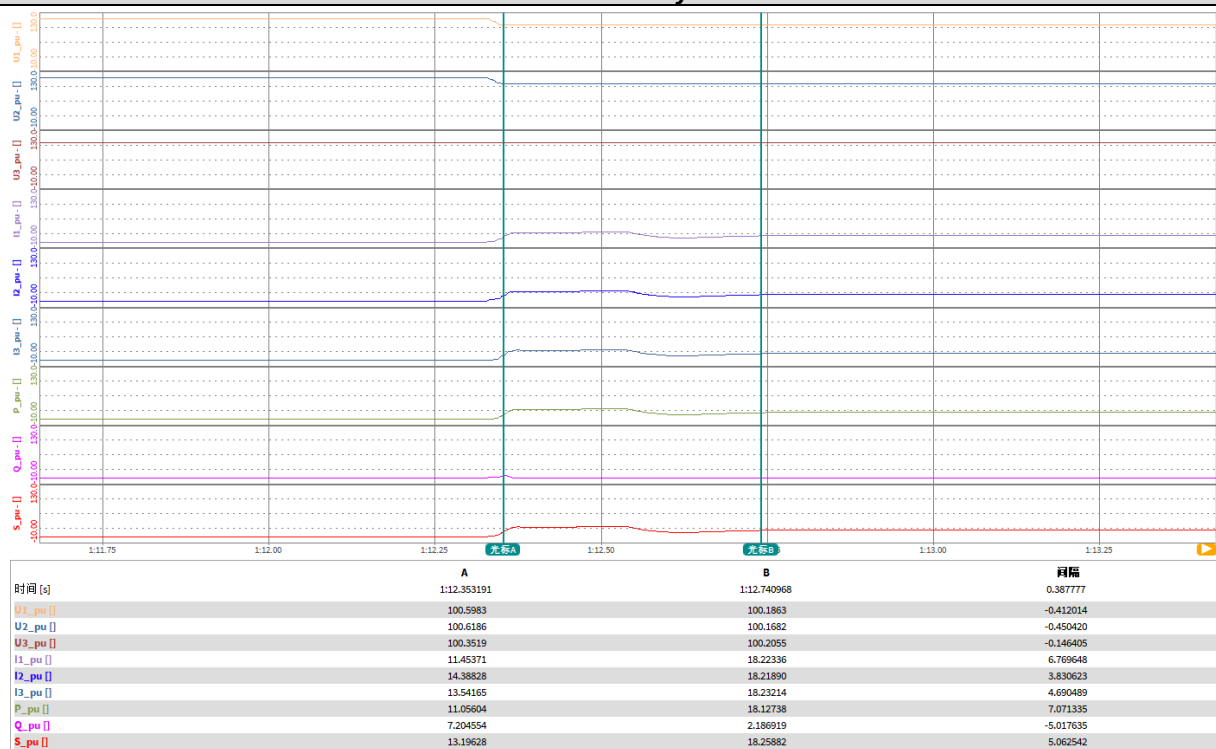
Voltage Rise time



DipTime



Power Recovery time

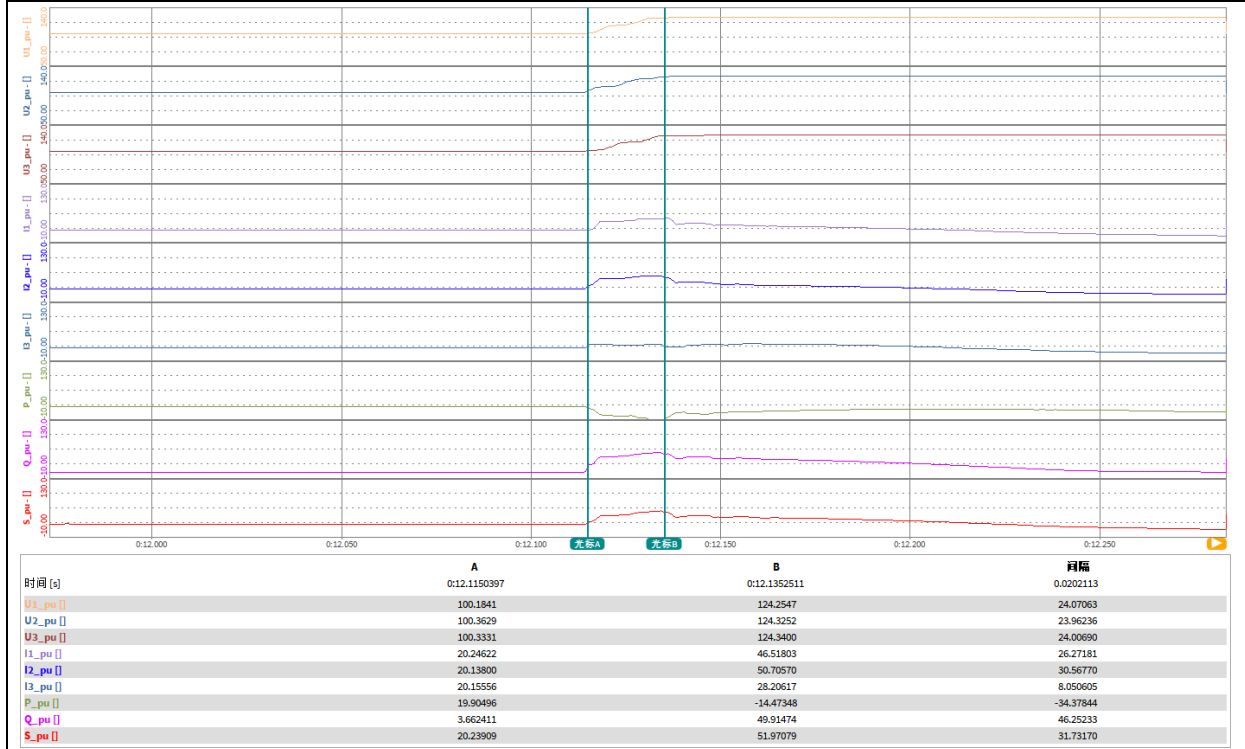


Three-Phase fault

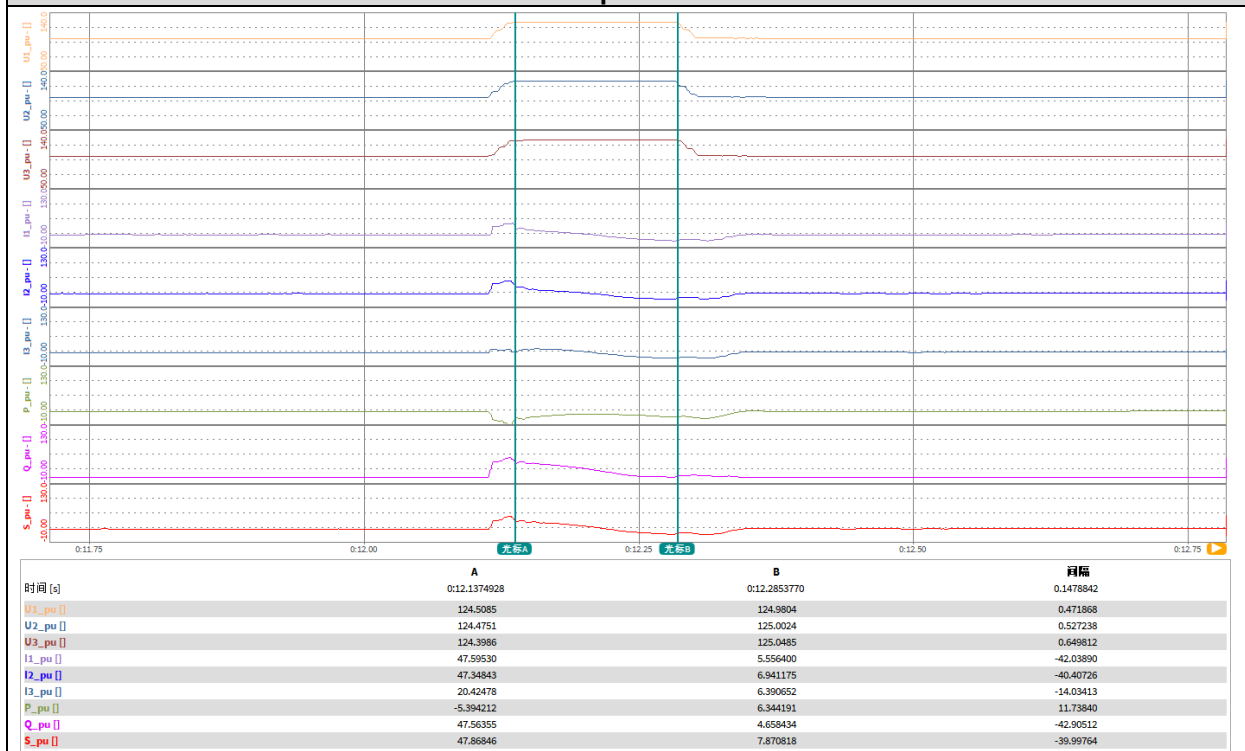
Partial load (20%Pn)

Residual voltage: 125.0%Un

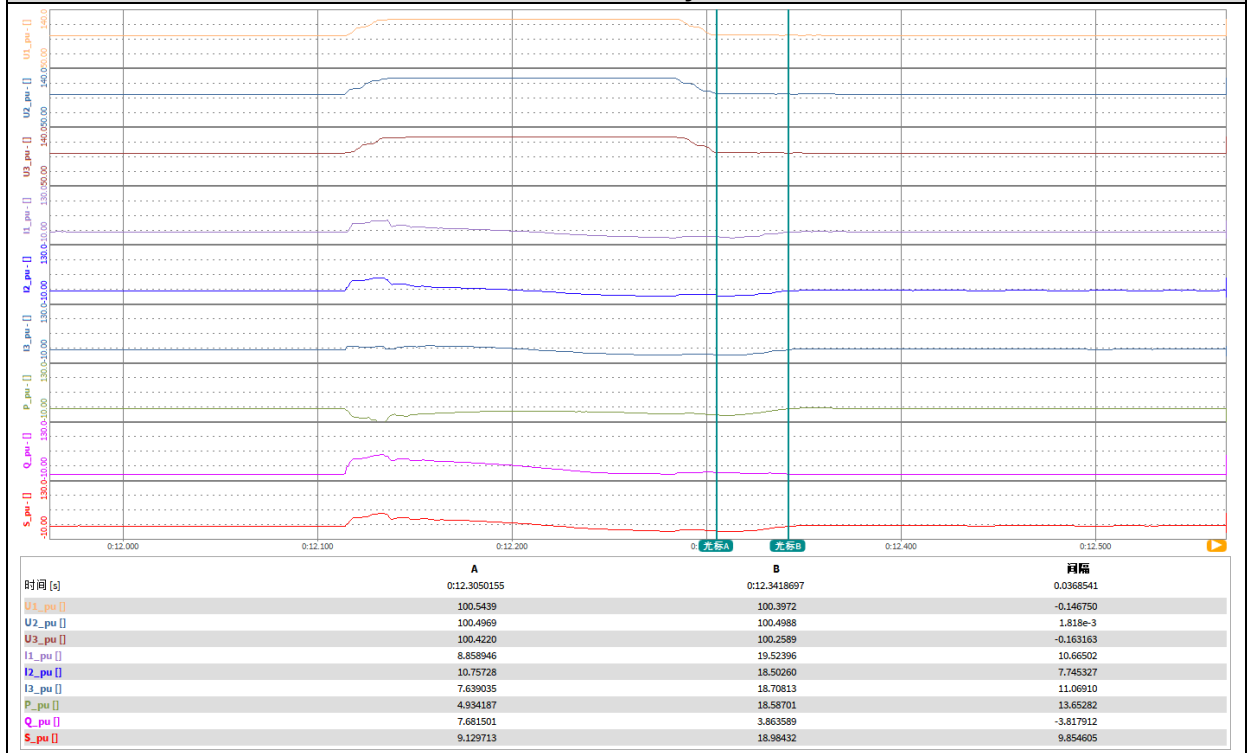
Voltage Rise time



DipTime



Power Recovery time

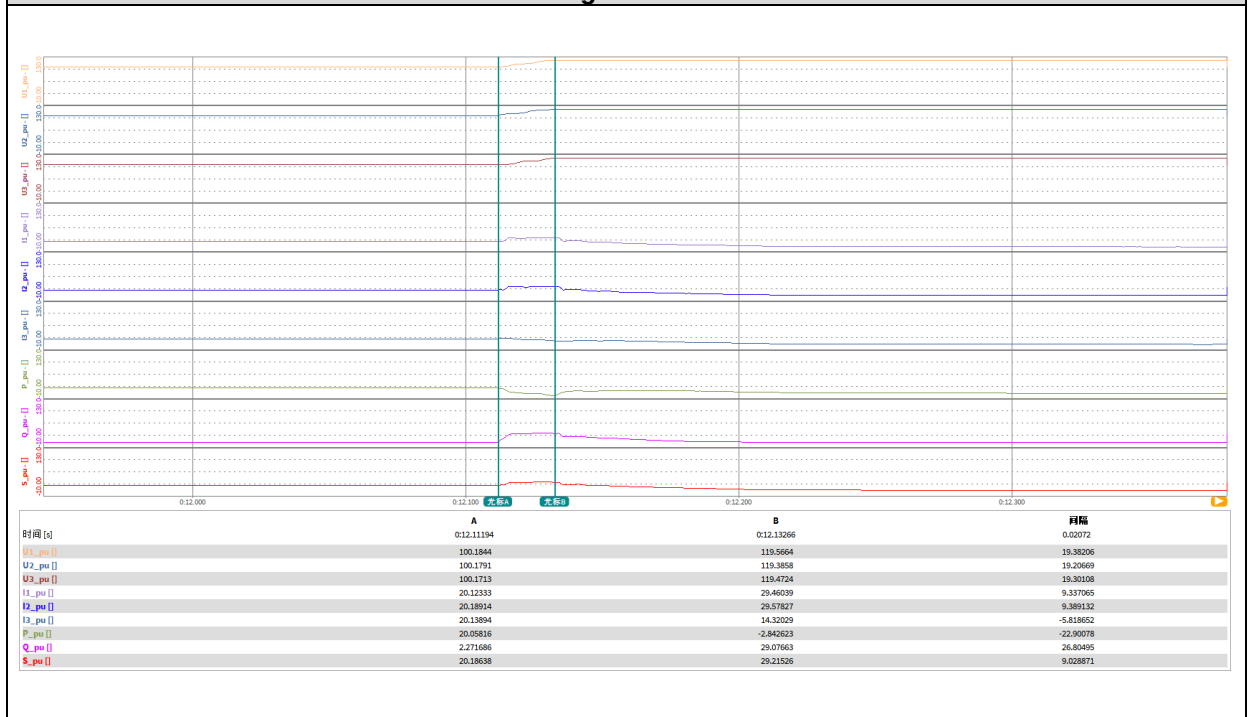


Three-Phase fault

Partial load (20%Pn)

Residual voltage: 120.0%Un

Voltage Rise time

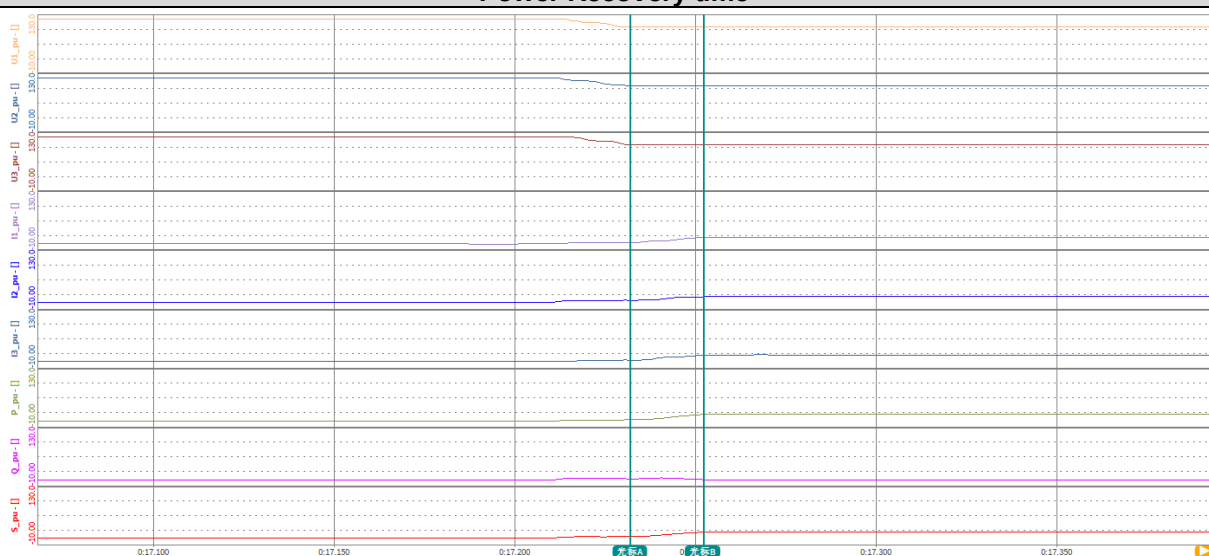


DipTime



时间 [s]	A	B	间隔
U1_pu []	0.12.13862	0.17.19552	5.05690
U2_pu []	119.8160	120.2030	0.386963
U3_pu []	119.8297	120.1177	0.288033
I1_pu []	119.5898	119.9371	0.347356
I2_pu []	20.76598	3.521938	-17.24405
I3_pu []	20.98224	3.757718	-17.22463
P_pu []	13.17767	3.683302	-9.494372
Q_pu []	9.839708	2.944551	-6.895157
S_pu []	19.59602	3.253694	-16.34233
S_pu []	21.92770	4.388270	-17.53943

Power Recovery time



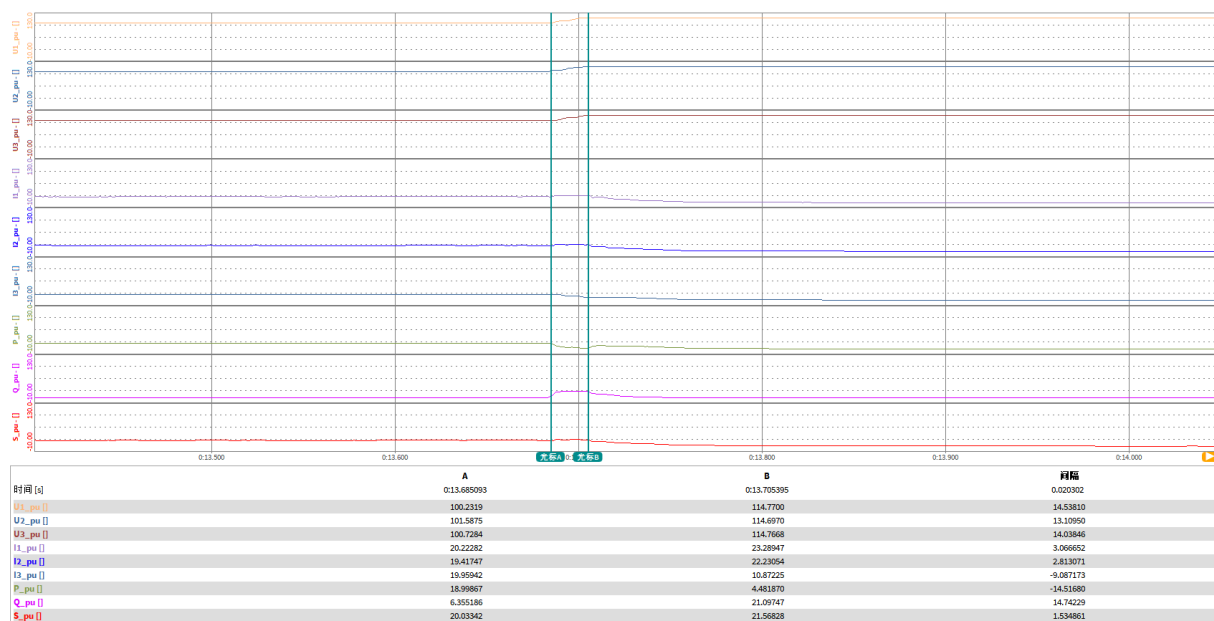
时间 [s]	A	B	间隔
U1_pu []	0.17.232087	0.17.252195	0.020108
U2_pu []	100.4762	100.4905	0.014264
U3_pu []	100.5218	100.5574	0.035626
I1_pu []	100.4555	100.4648	9.295e-3
I2_pu []	6.878630	18.29461	11.41598
I3_pu []	9.249329	18.08606	8.836727
P_pu []	7.593068	19.51686	11.92379
Q_pu []	5.779276	18.34699	12.56771
S_pu []	5.452676	3.749817	-1.702858
S_pu []	7.945546	18.72627	10.78072

Three-Phase fault

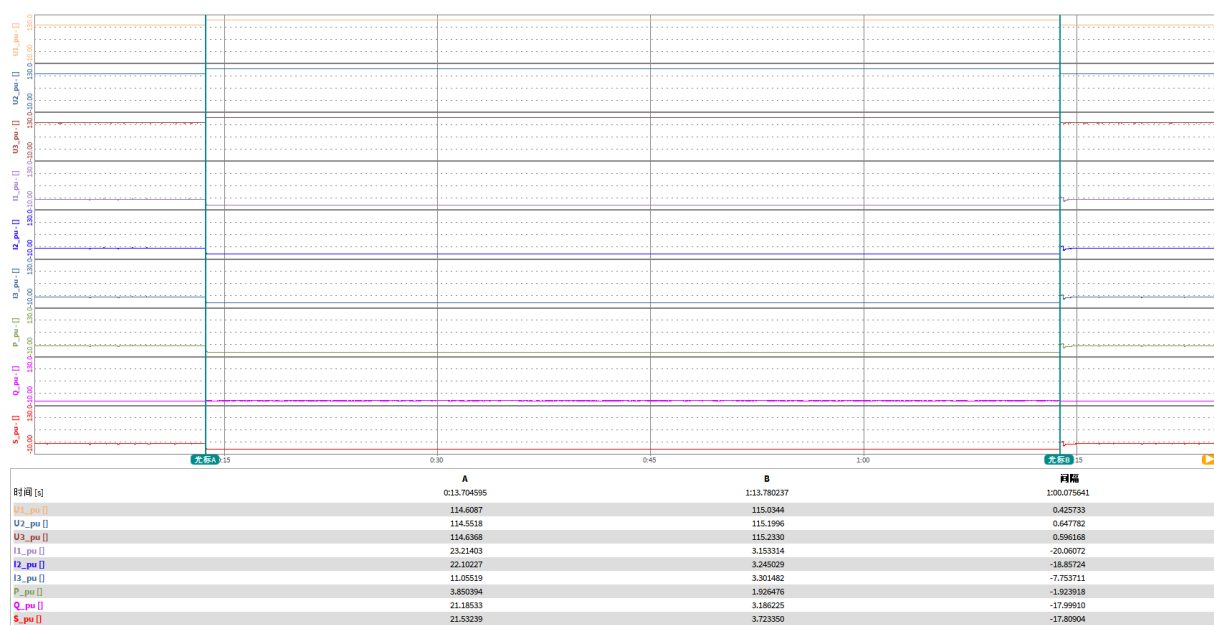
Partial load (20%Pn)

Residual voltage: 115.0%Un

Voltage Rise time



DipTime



Power Recovery time



4.2.3.2 Load Tests: Full Load (100%Pn)

Test results of full power cases performed are offered below:

Single-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.3	20	124.5	> 120	148	61	100.3
120.0	100.3	20	119.5	> 5000	5050	74	100.3
115.0	100.4	20	114.5	> 60000	60001	64	99.9

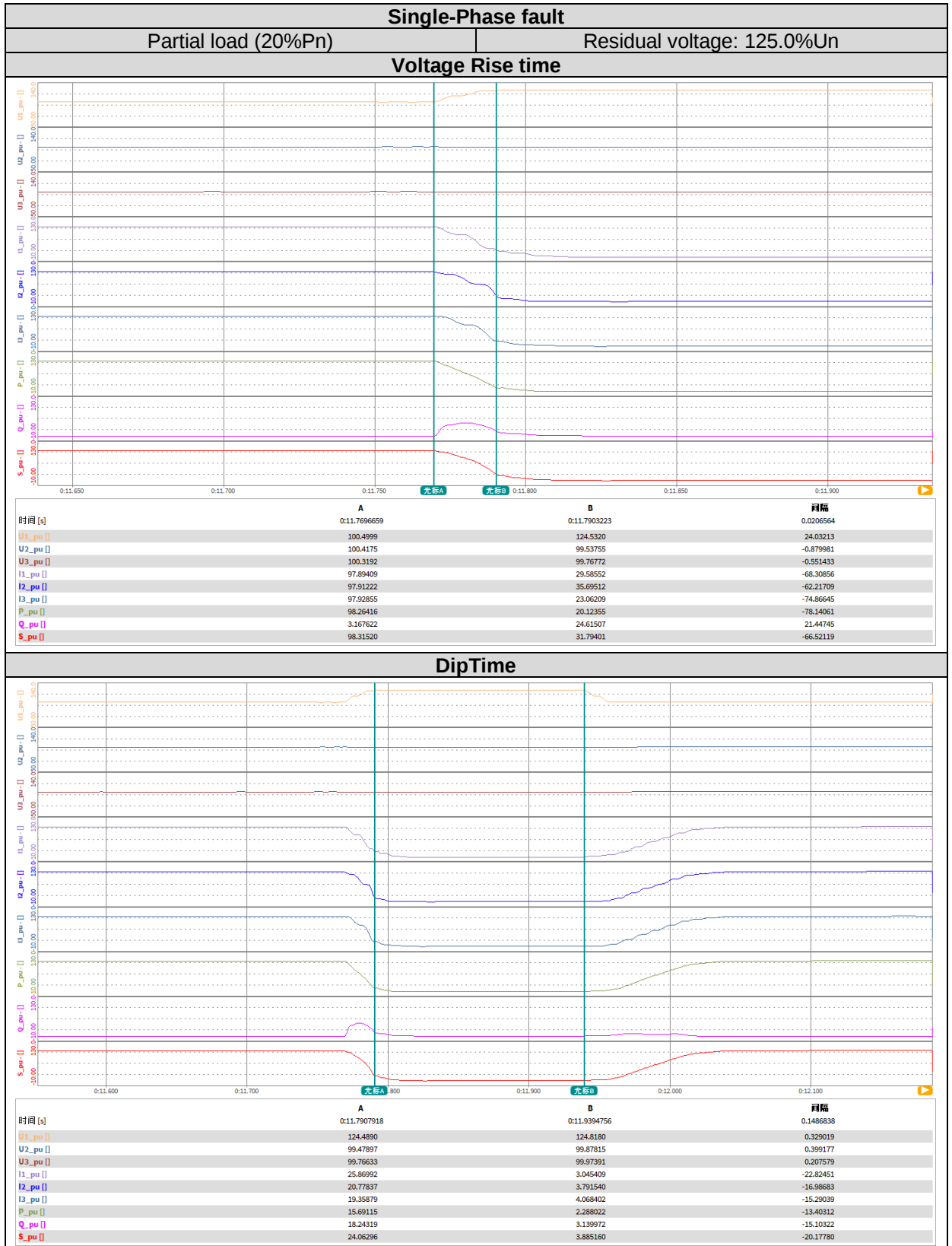
Two-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.3	20	124.3	> 120	145	94	100.3
120.0	100.3	20	119.3	> 5000	5062	78	100.3
115.0	100.4	20	114.3	> 60000	6001	72	99.9

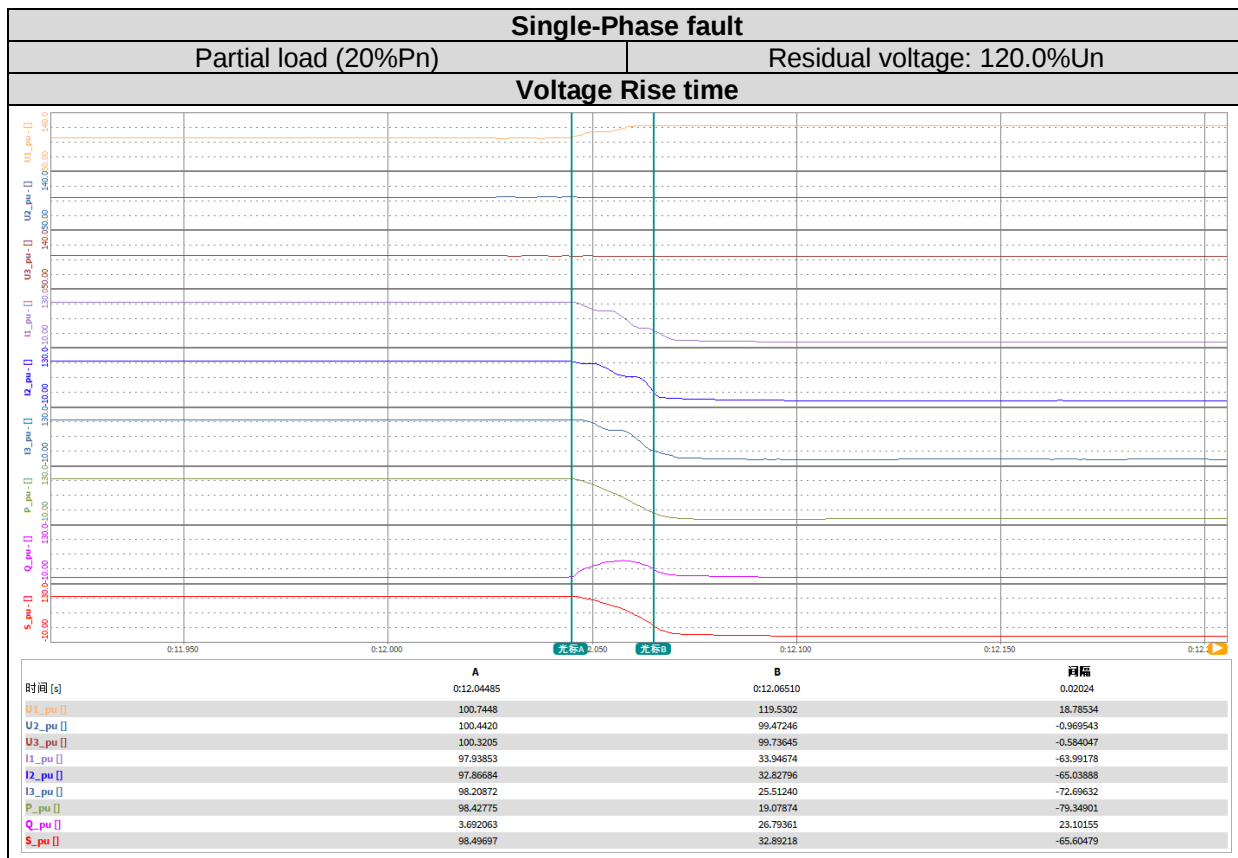
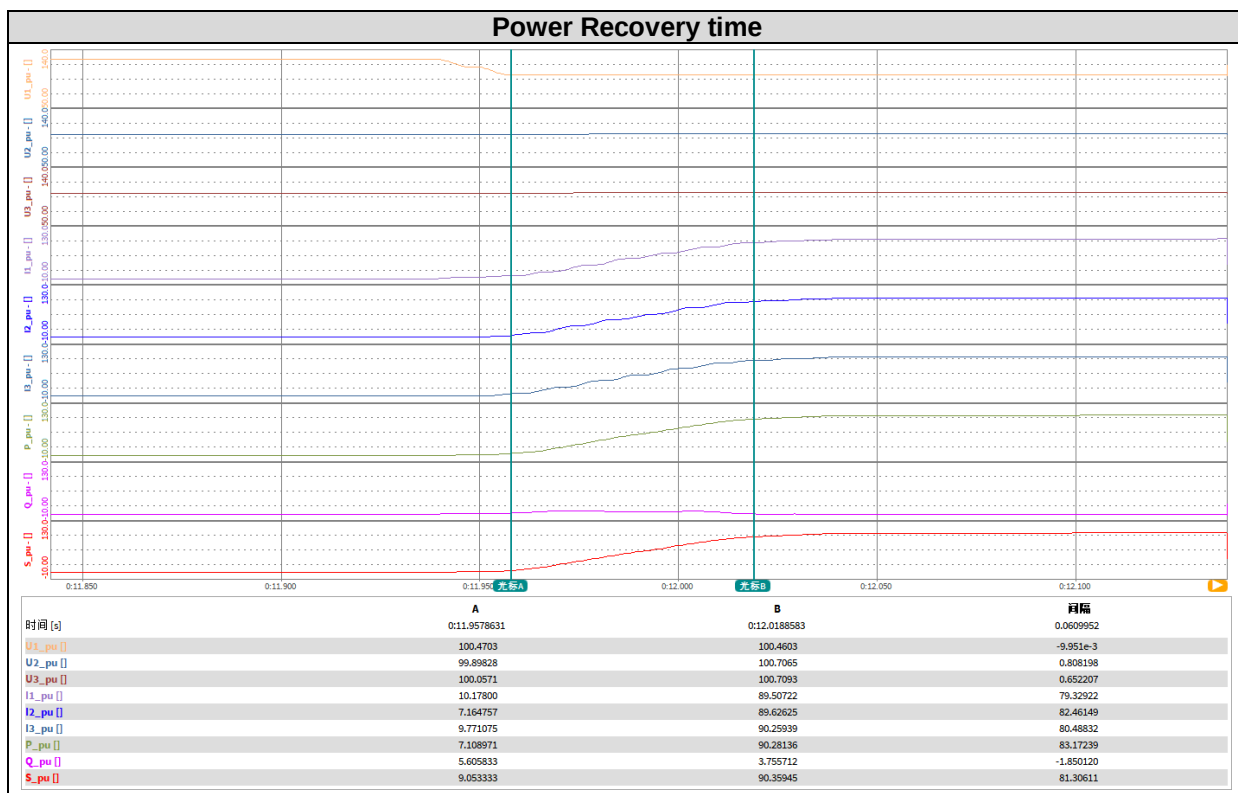
Three-Phase fault							
Residual voltage Desired (%Un)	Voltage before Recovery (%Un)	Voltage Rise time (ms)	Residual voltage Measured (%Un)	DipTime (ms)		Power Recovery time (ms)	Voltage after Recovery (%Un)
				Desired	Meas.		
125.0	100.3	21	124.3	> 120	146	86	100.3
120.0	100.3	20	119.3	> 5000	5050	92	100.3
115.0	100.4	20	114.5	> 60000	6004	76	99.9

Remark: The virtual neutral line was used in the test.

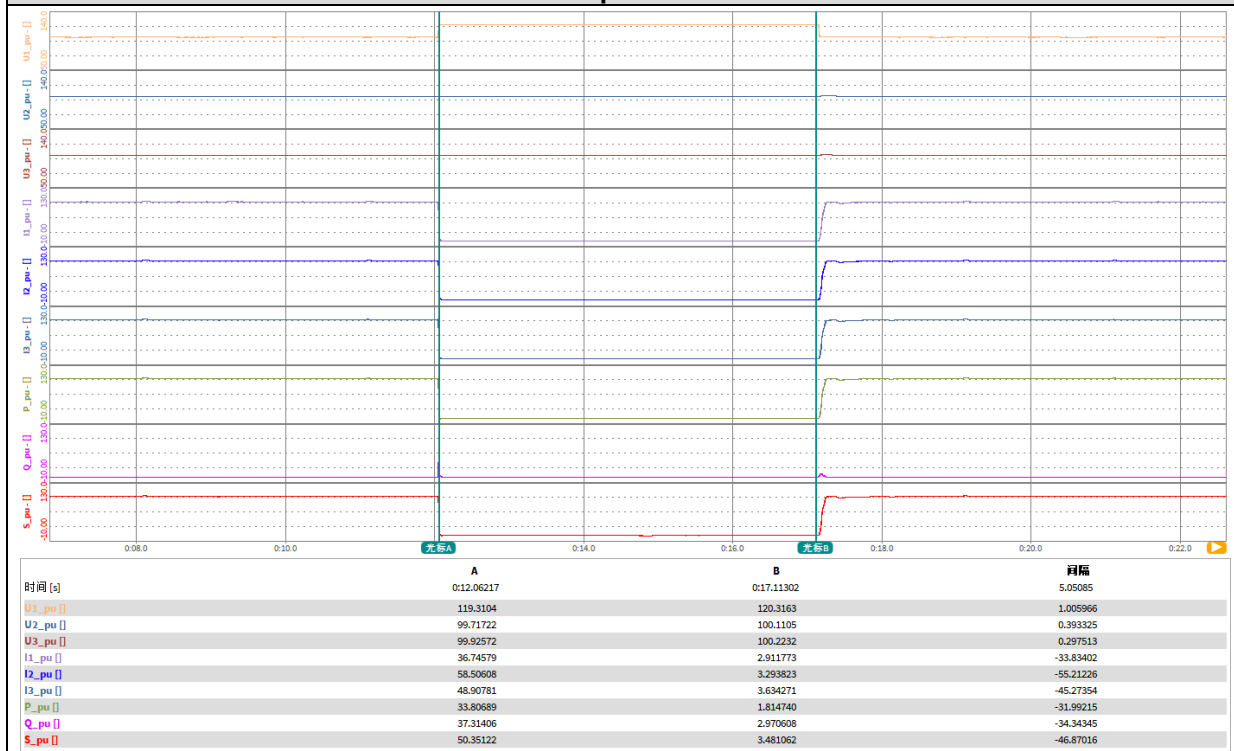
Test results are graphically represented at following pages.

Note: The following test figure the voltage unit of U1_pu, U2_pu and U3_pu are %Un , the current unit of I1_pu, I2_pu and I3_pu are %In and the power unit is %Pn

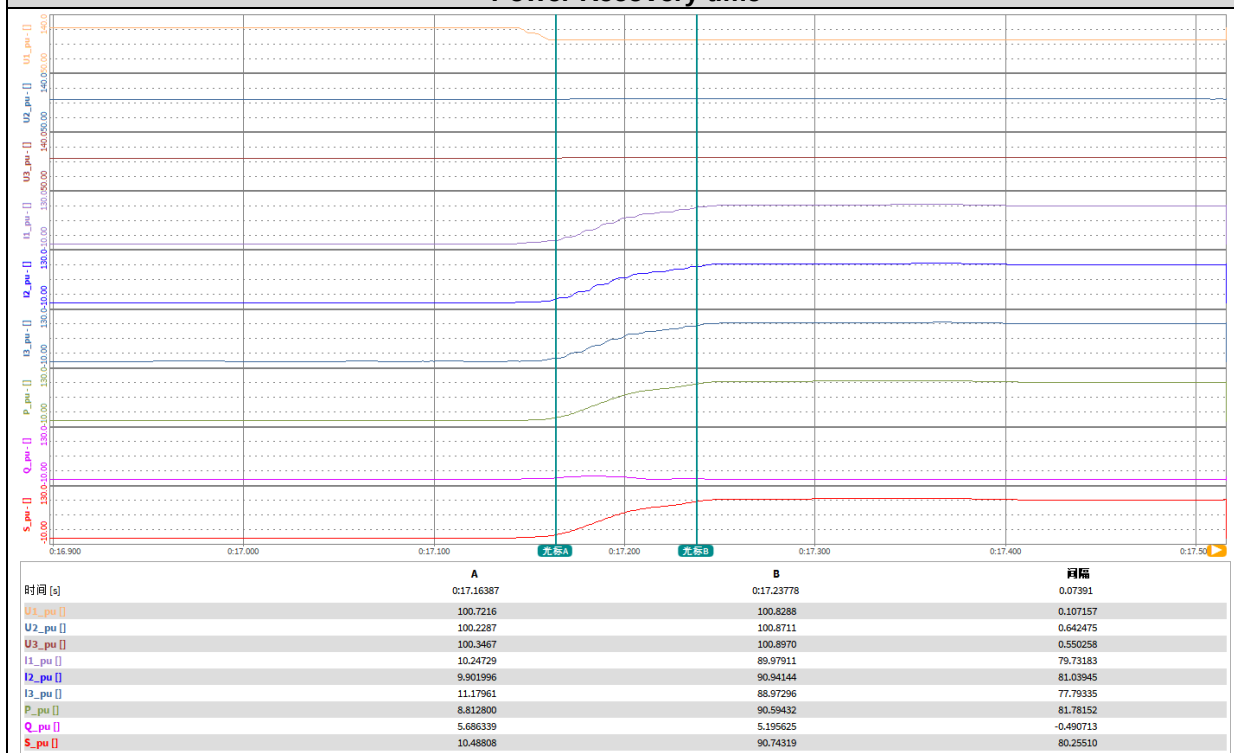




DipTime



Power Recovery time

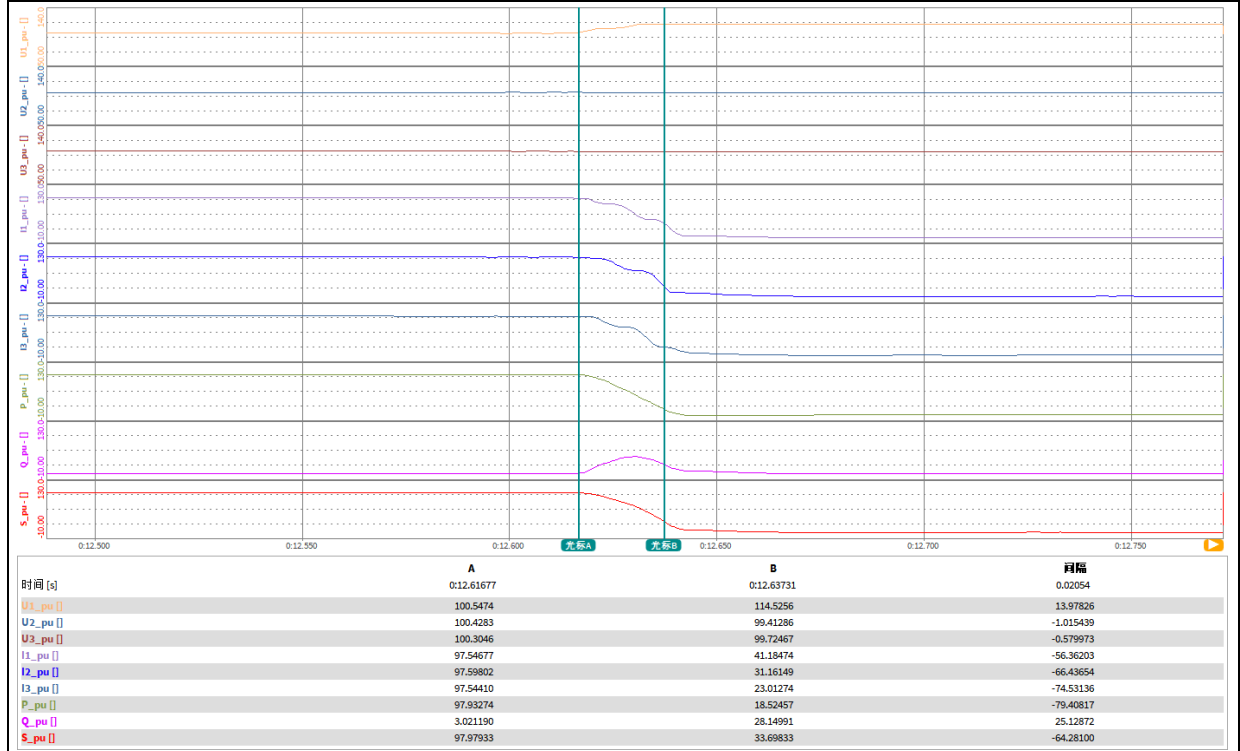


Single-Phase fault

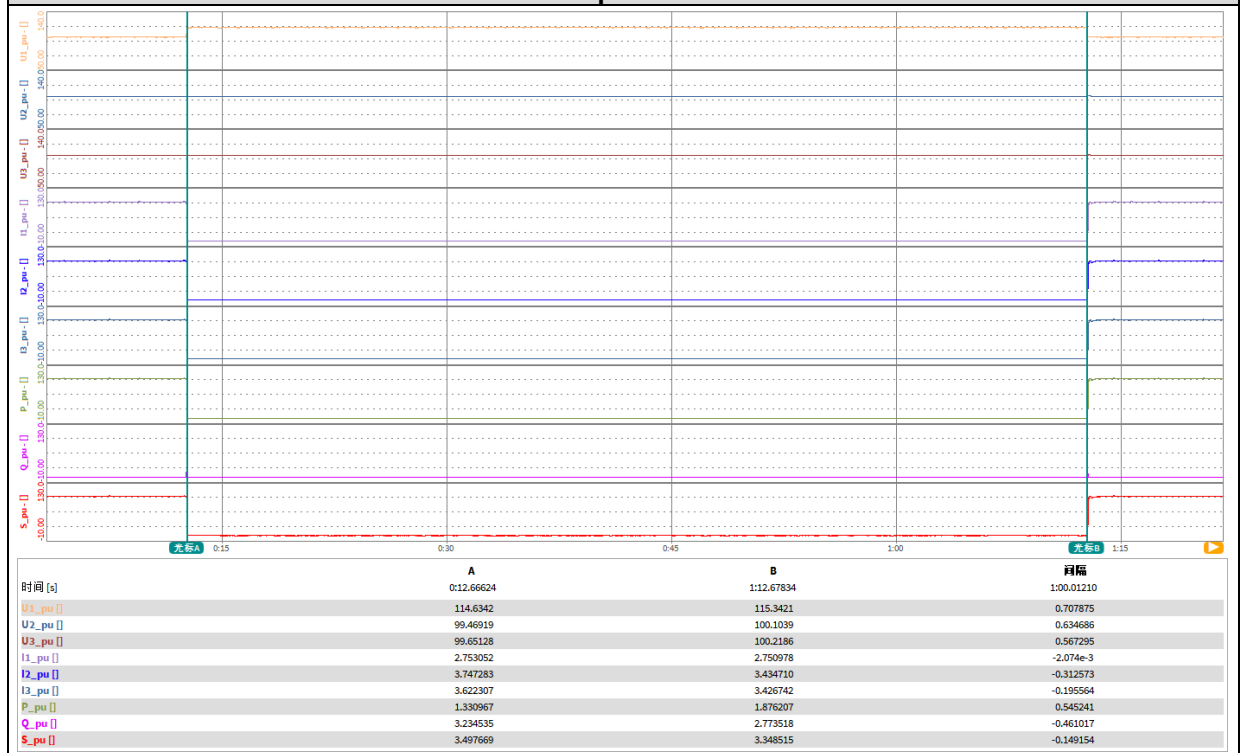
Partial load (20%Pn)

Residual voltage: 115.0%Un

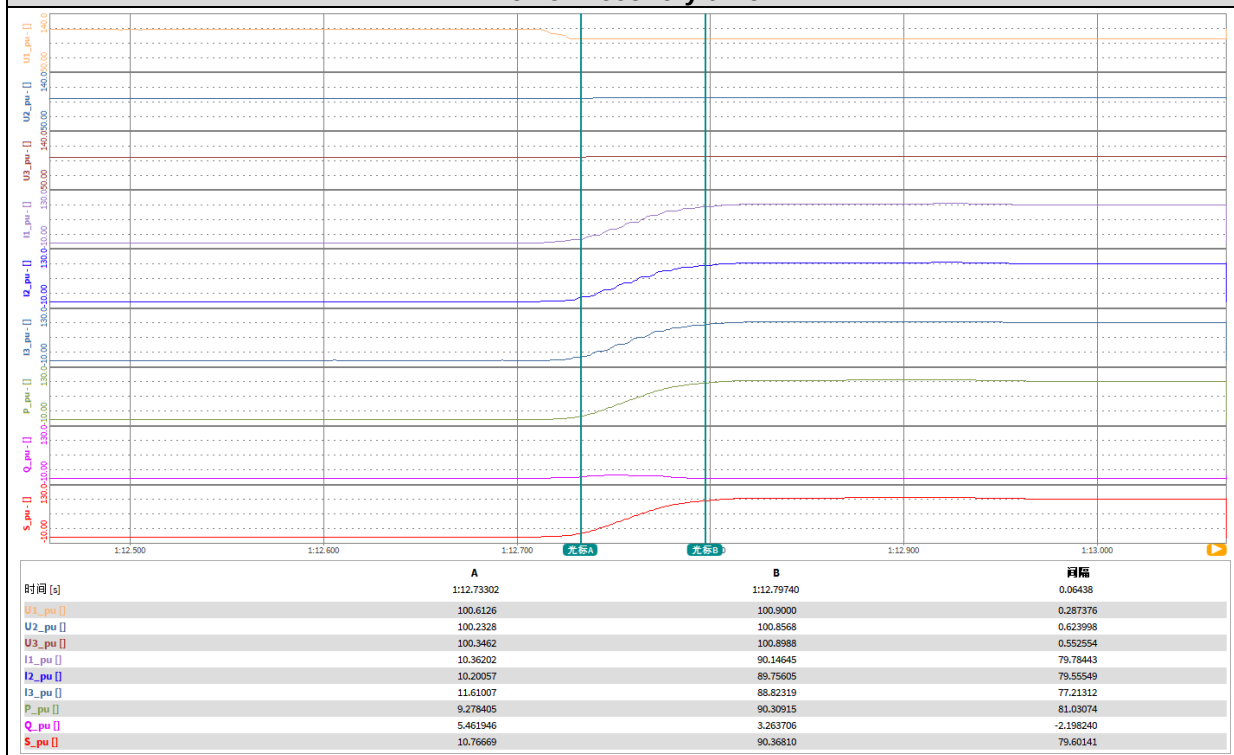
Voltage Rise time



DipTime



Power Recovery time

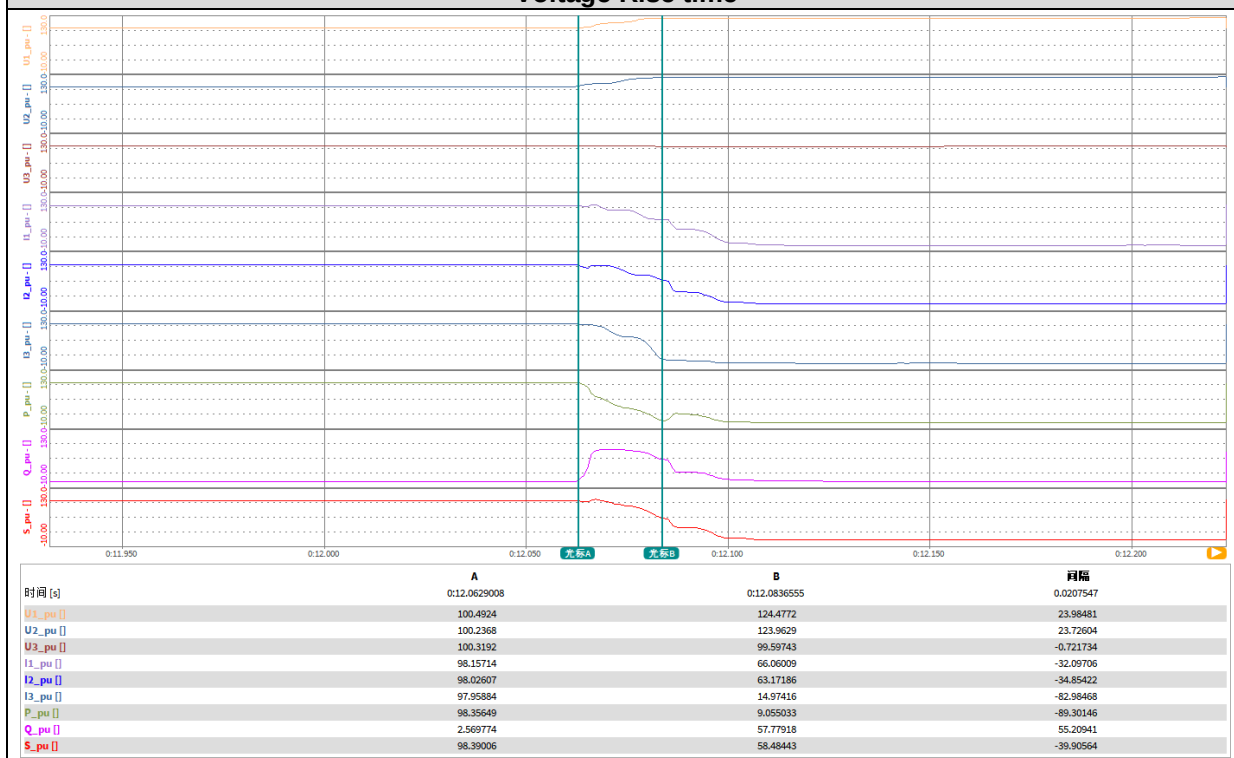


Two-Phase fault

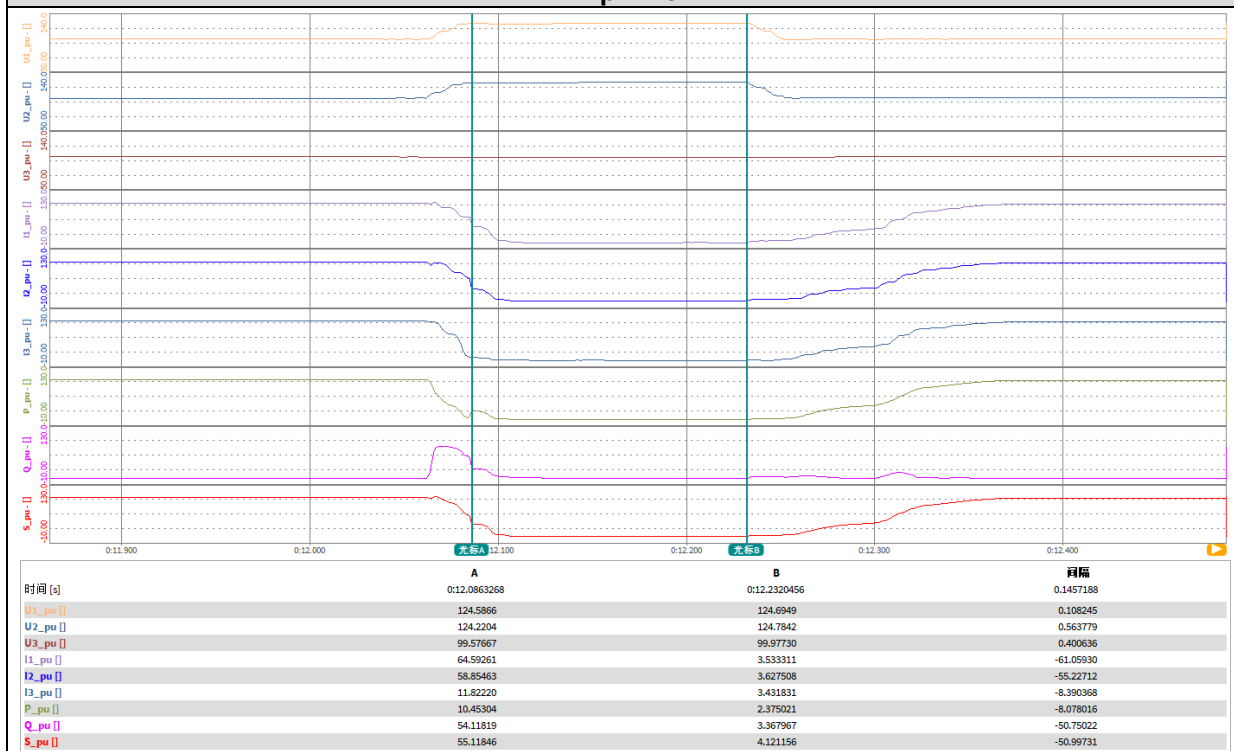
Partial load (20%Pn)

Residual voltage: 125.0%Un

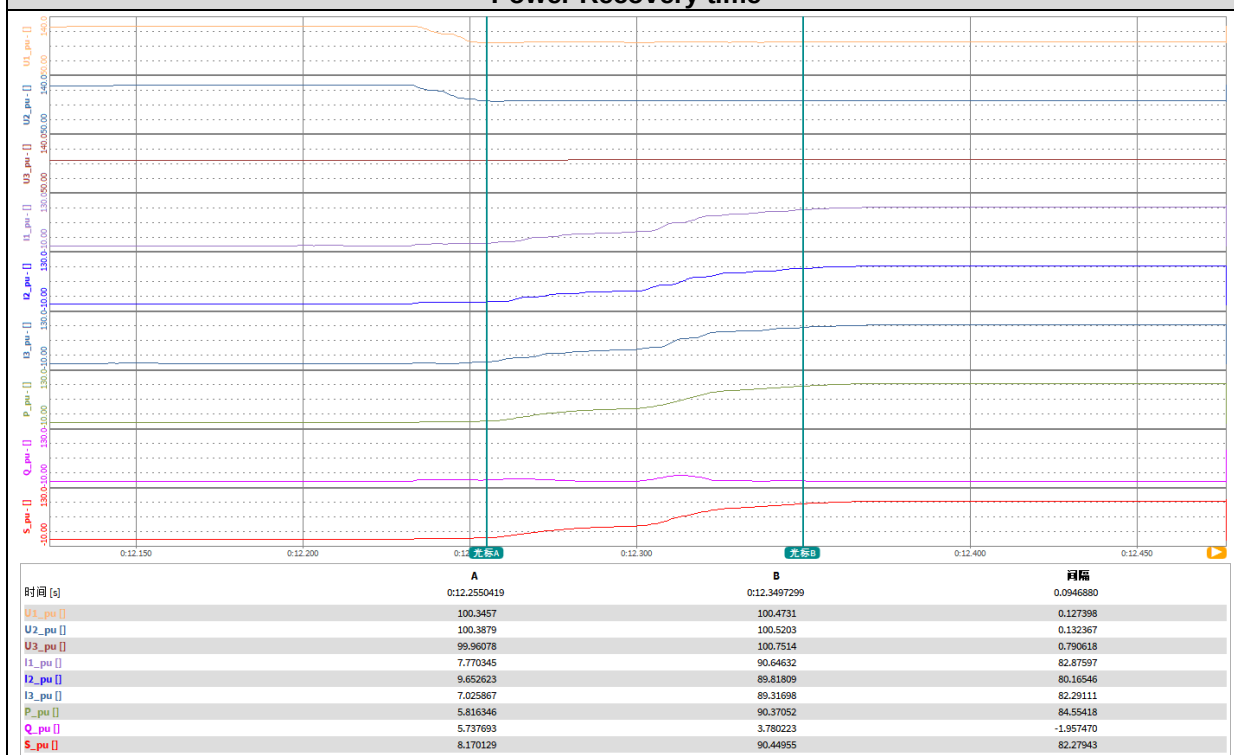
Voltage Rise time

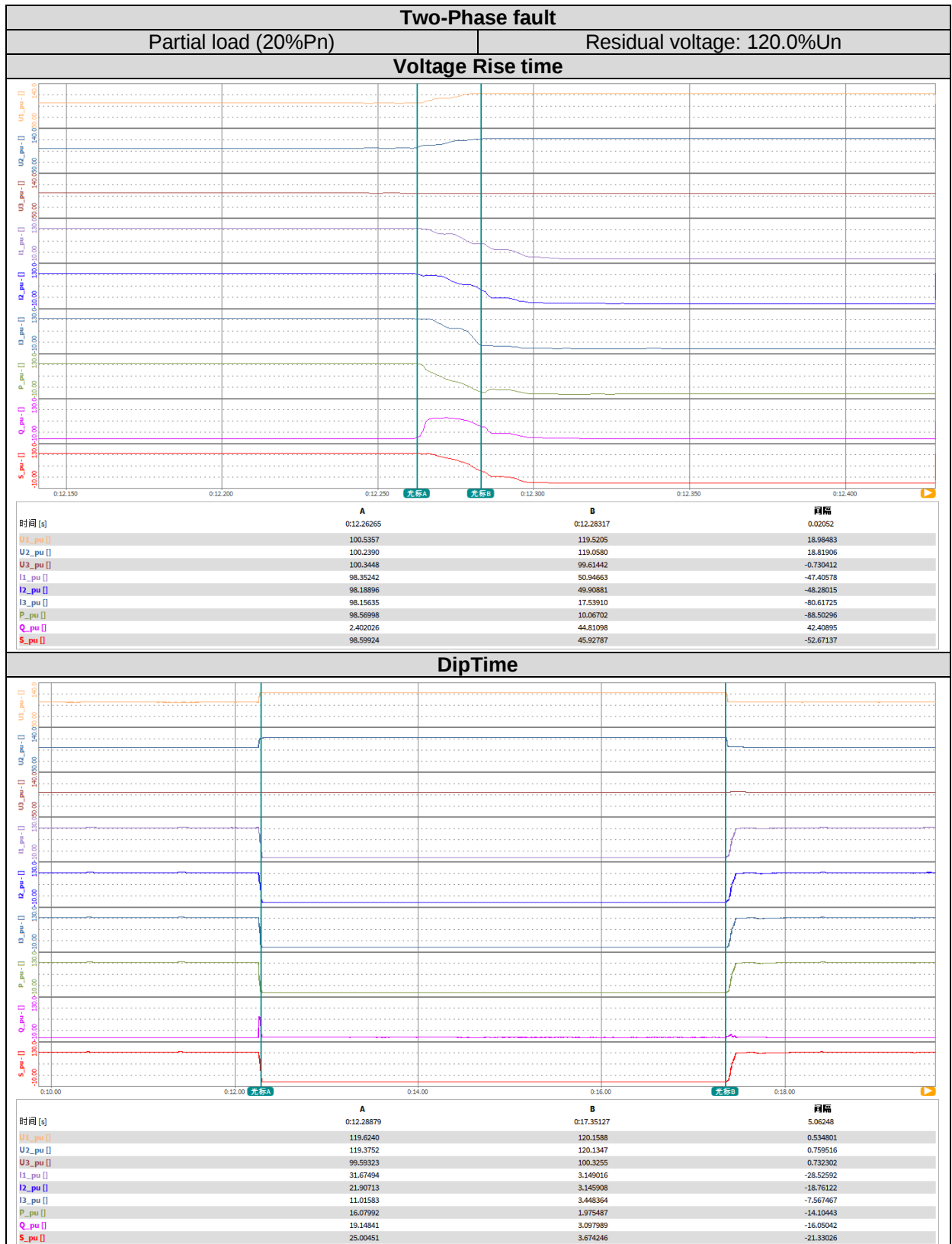


DipTime

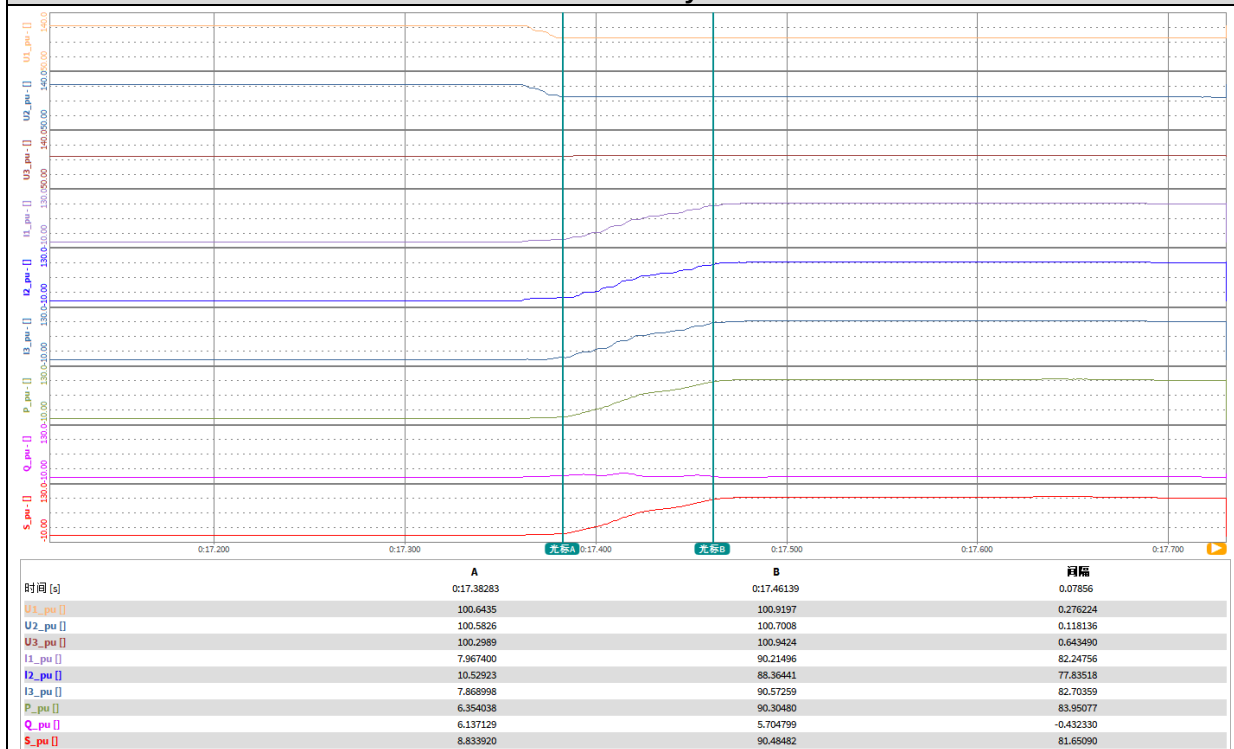


Power Recovery time





Power Recovery time

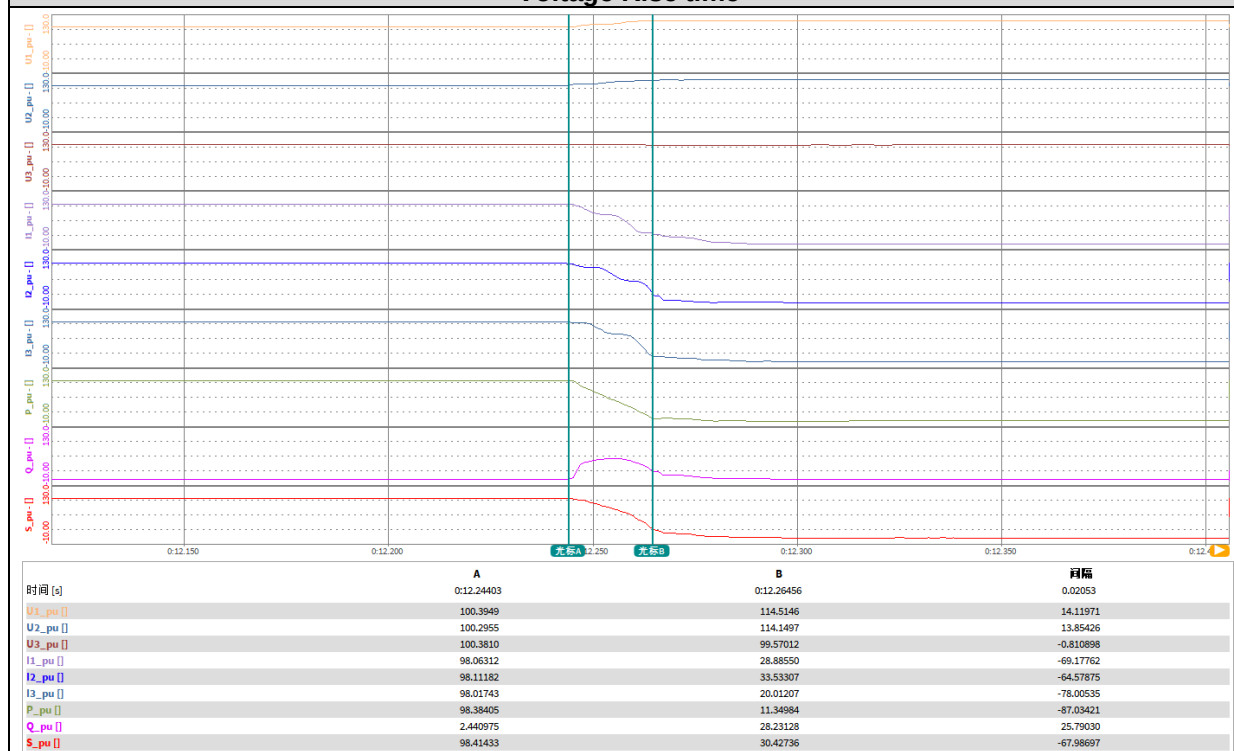


Two-Phase fault

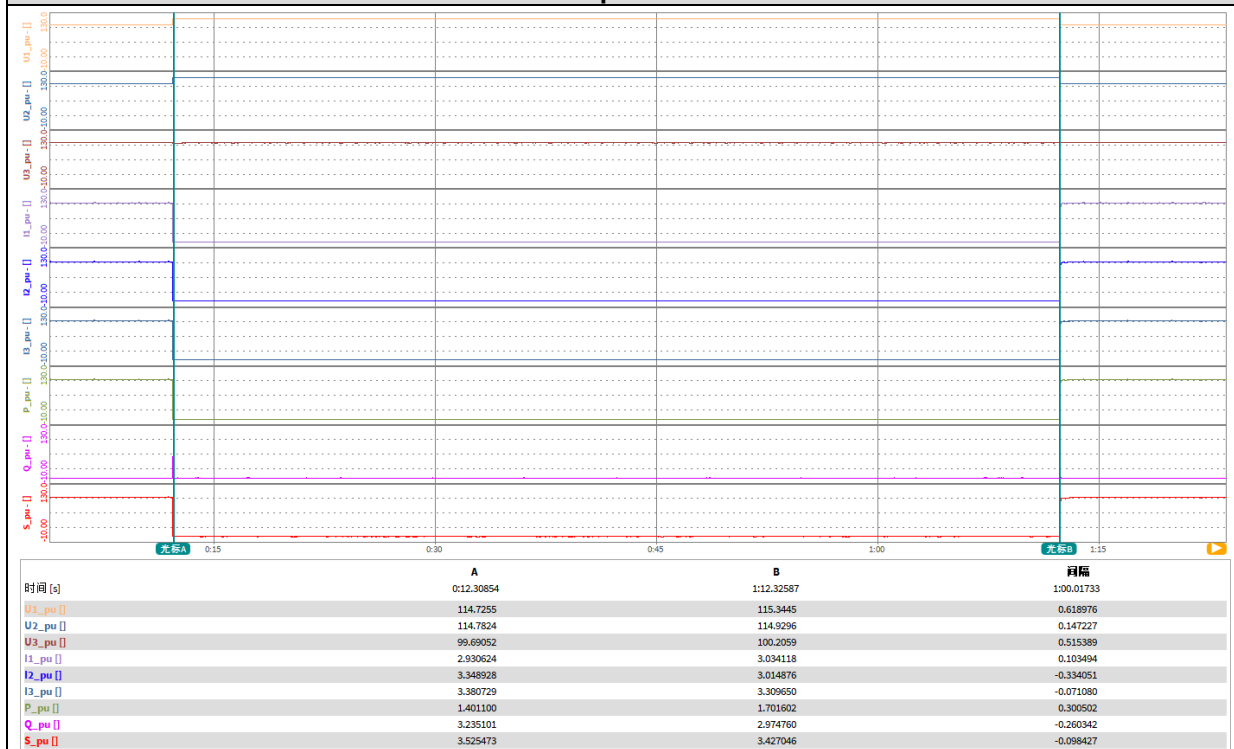
Partial load (20%Pn)

Residual voltage: 115.0%Un

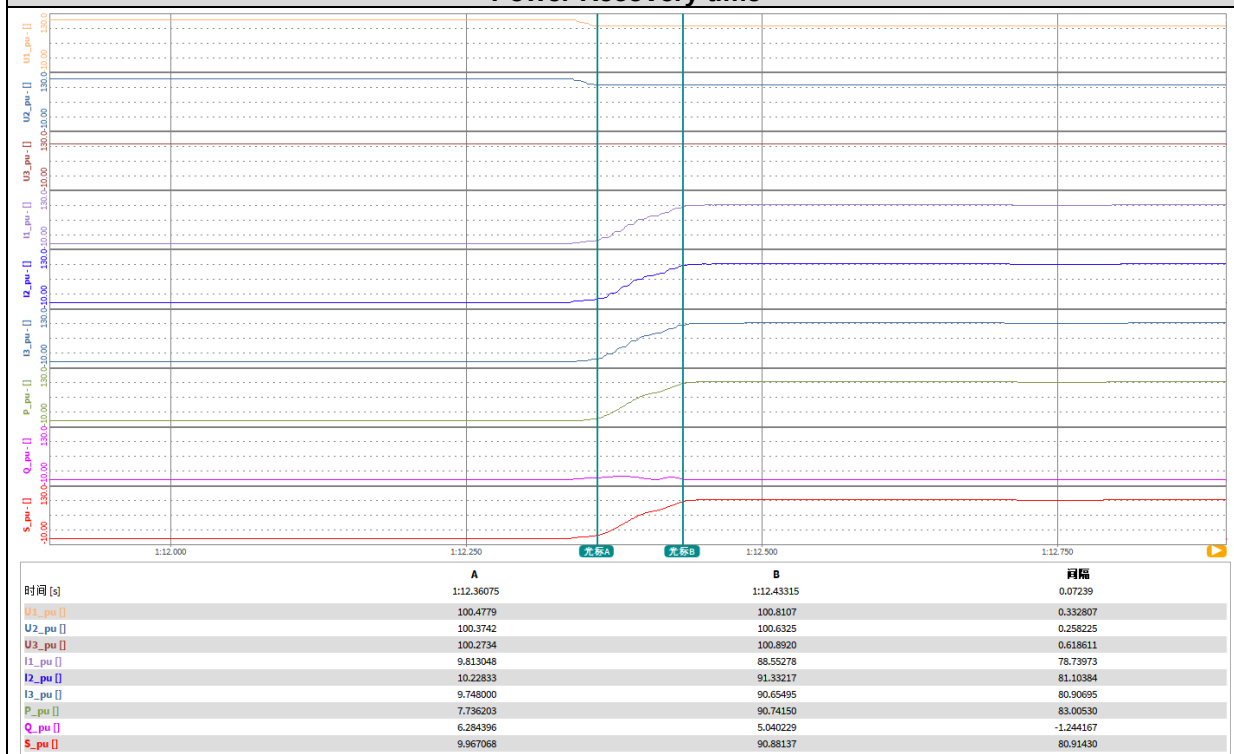
Voltage Rise time

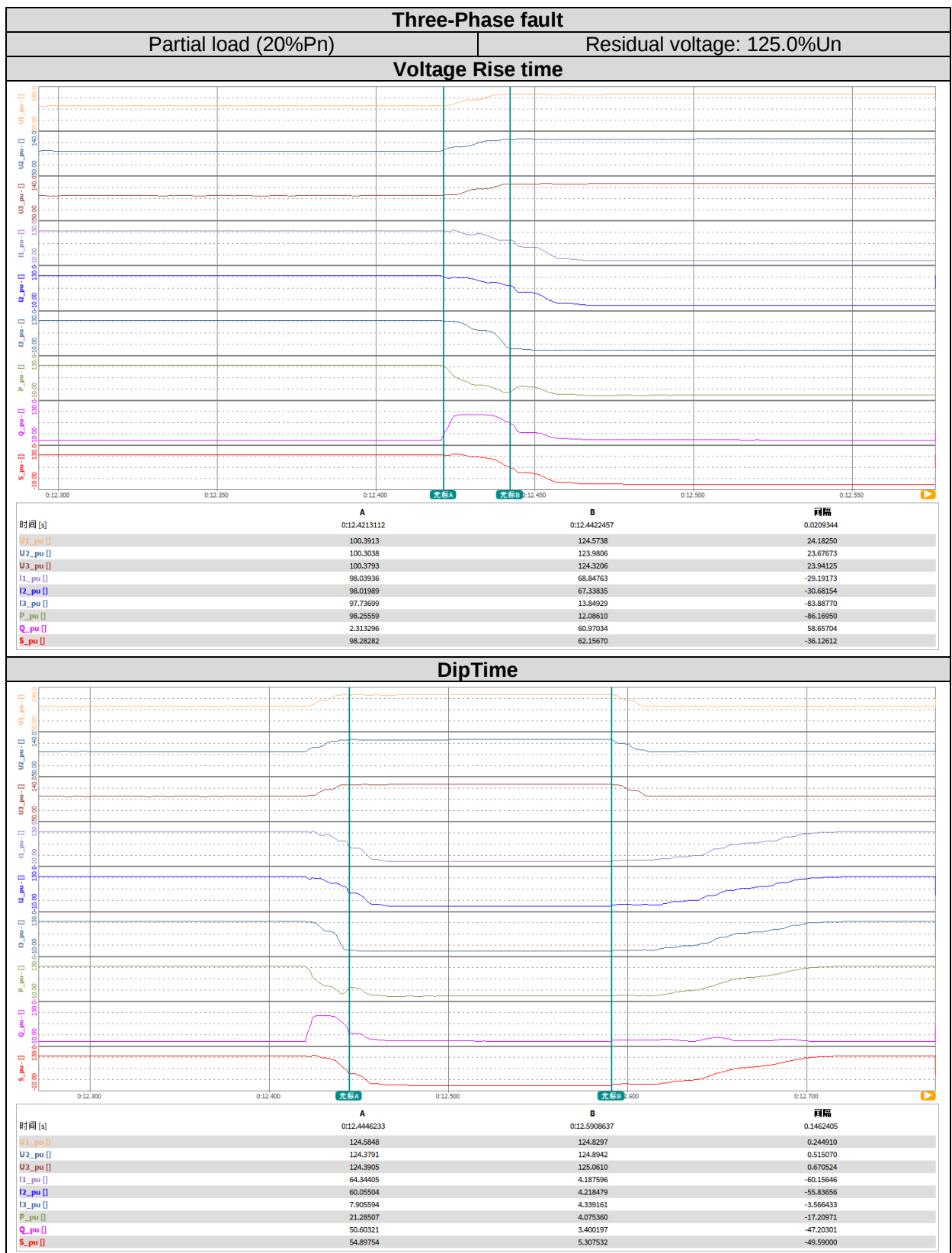


DipTime

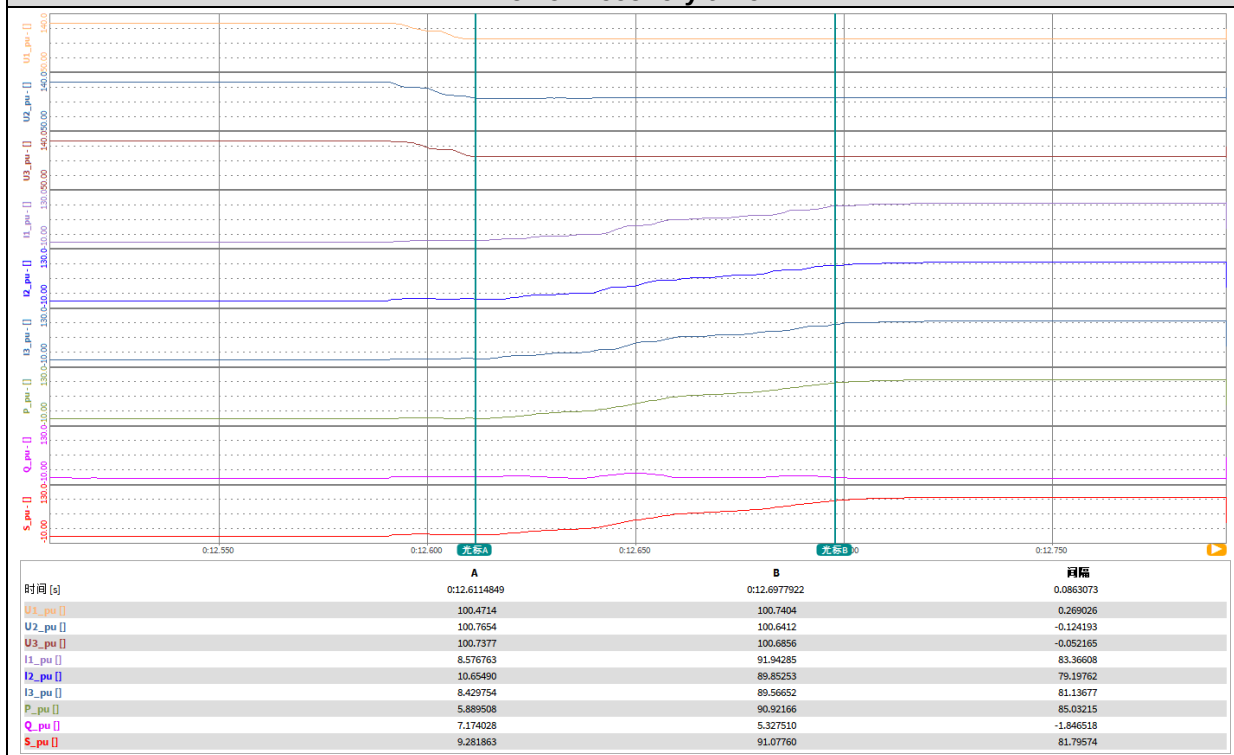


Power Recovery time





Power Recovery time

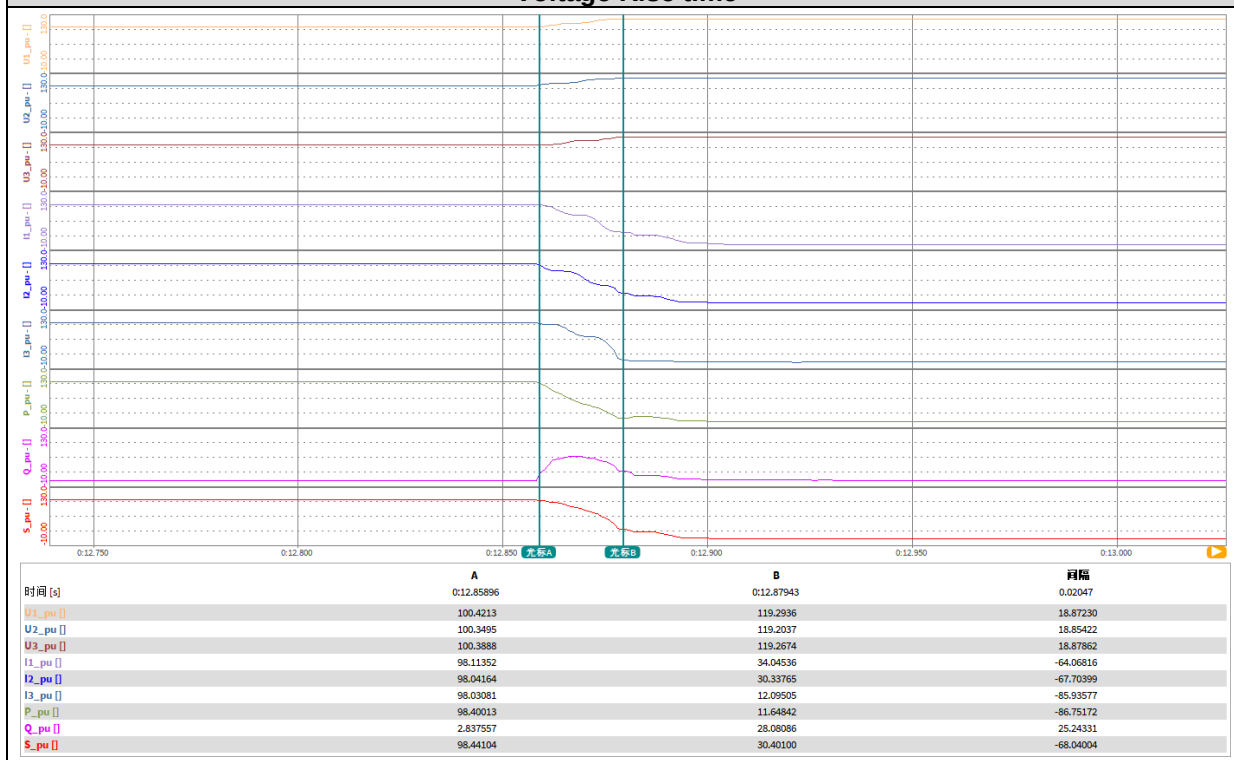


Three-Phase fault

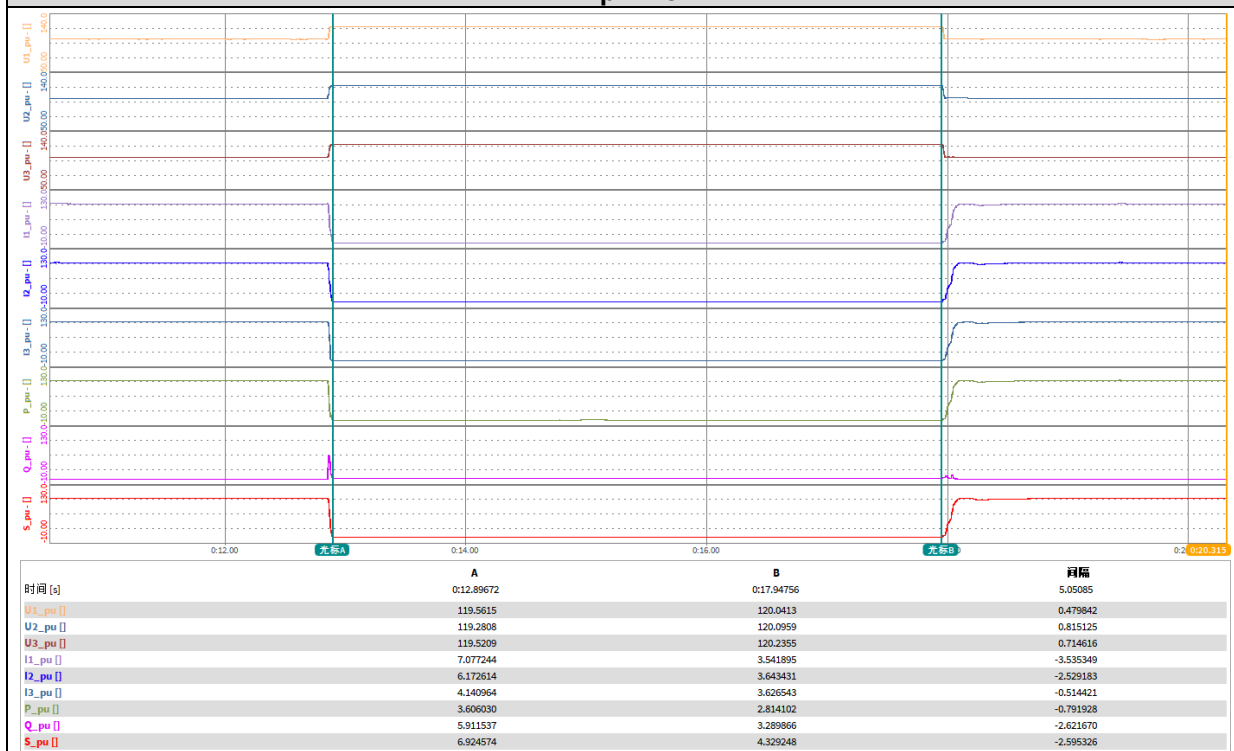
Partial load (20%Pn)

Residual voltage: 120.0%Un

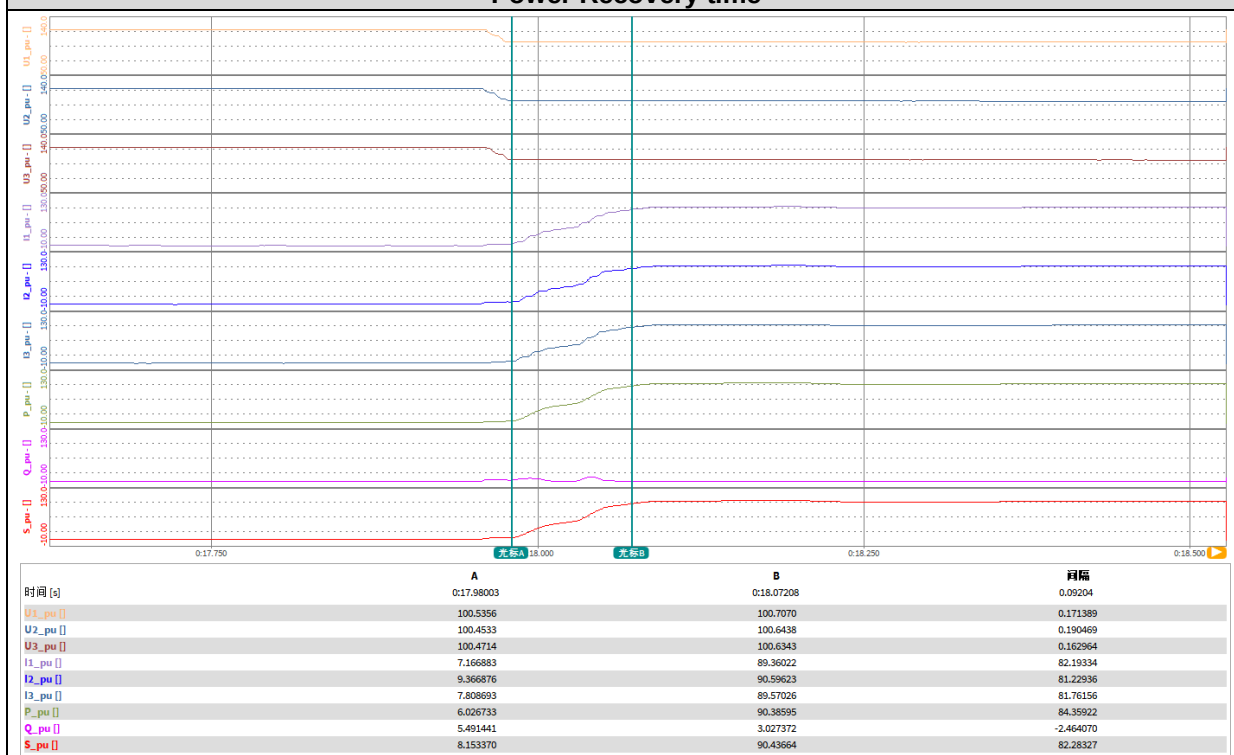
Voltage Rise time



DipTime



Power Recovery time

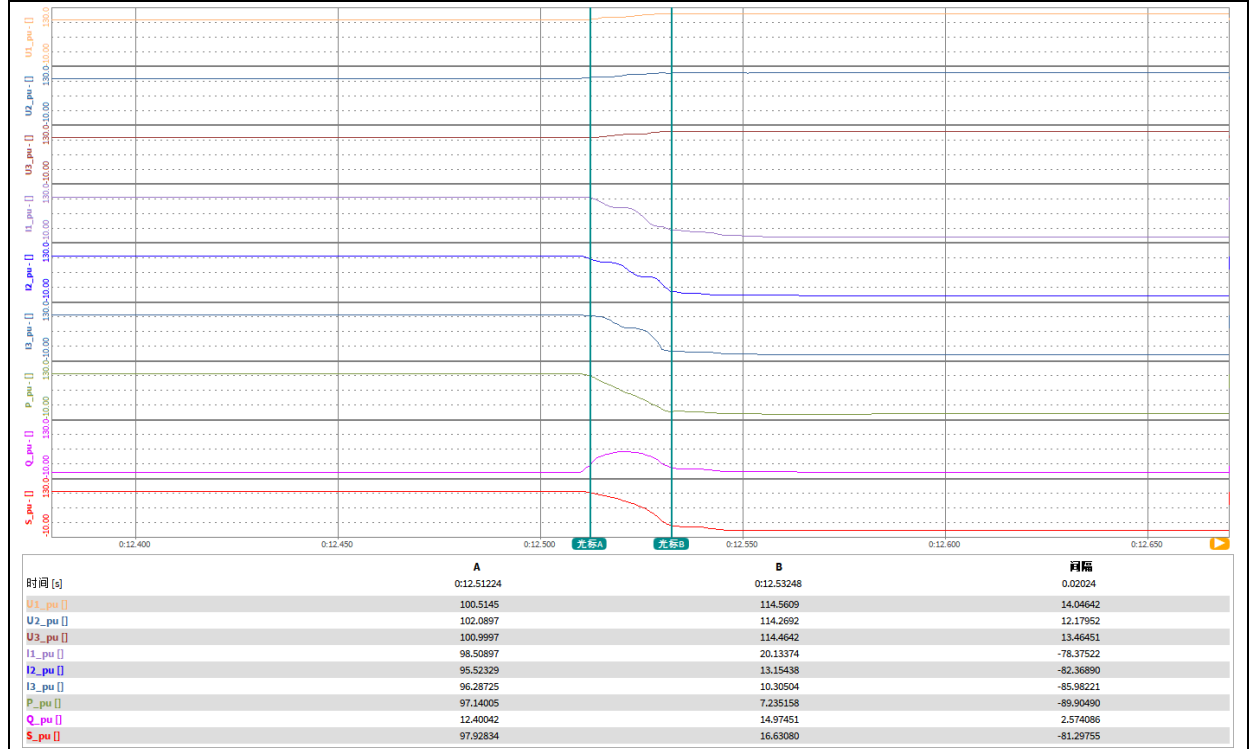


Three-Phase fault

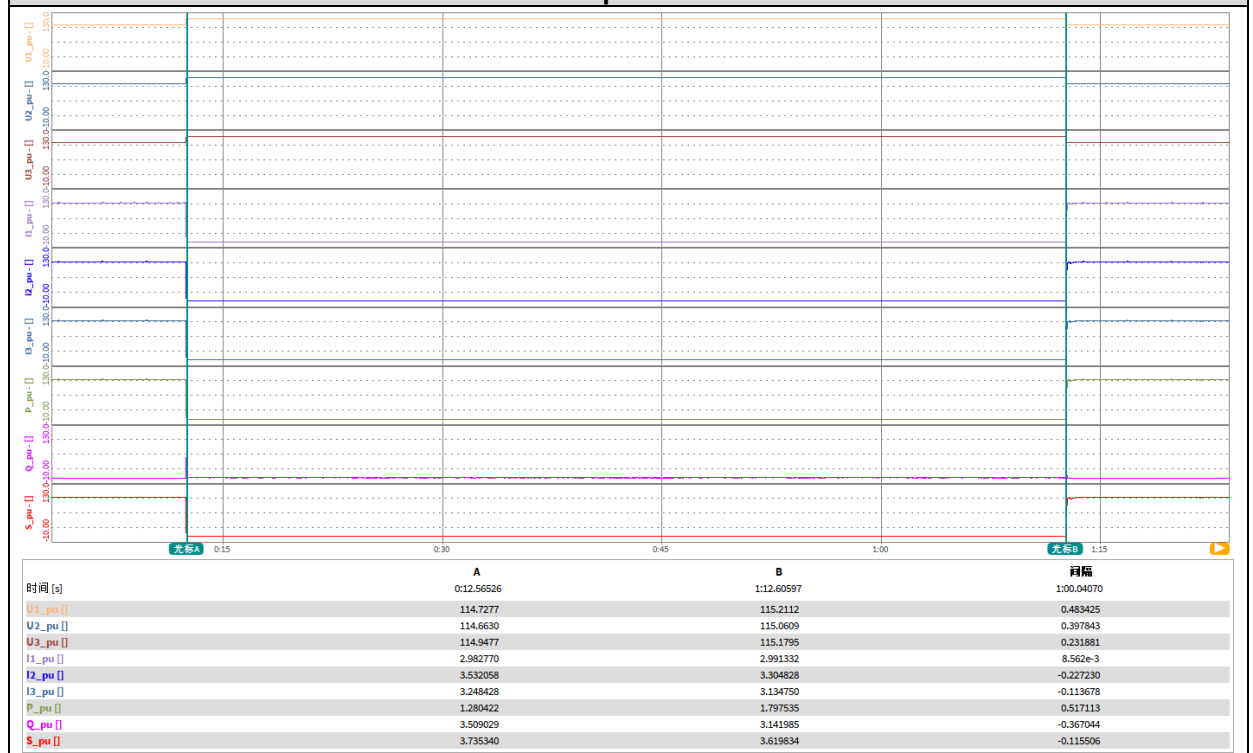
Partial load (20%Pn)

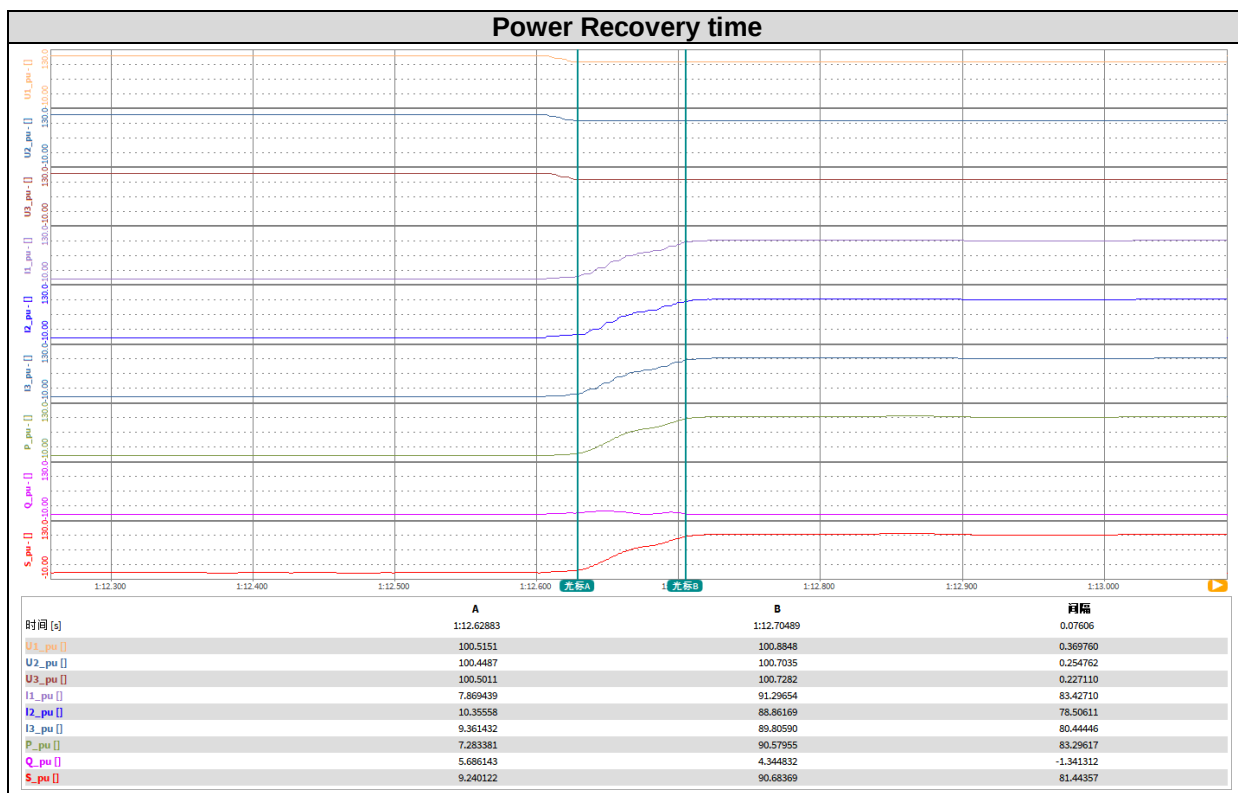
Residual voltage: 115.0%Un

Voltage Rise time



DipTime





4.3. ACTIVE RESPONSE TO FREQUENCY DEVIATION

4.3.1. Power response to overfrequency

The test has been done according to the clause 4.6.1 of the standard. The following definitions apply to the test to verify the clause:

- Test 1: P = 100 %Pn; f1 = 50.2 Hz; droop = 12 %; f-stop deactivated, with delay of 2 s (*)
- Test 2: P = 100 %; f1 = 52 Hz; droop = 2 %; function deactivated
- Test 3: P = 50 %; f1 = 51 Hz; droop = 5 %; f-stop deactivated, no delay
- Test 4: P = 100 %, f1 = 50.2 Hz; droop = 5 %; f-stop = 50.1 Hz (hysteresis), no delay

(*) The intentional delay is only active for the activation of the function, once the function is operating, the established control loop is not intentionally delayed.

Note:

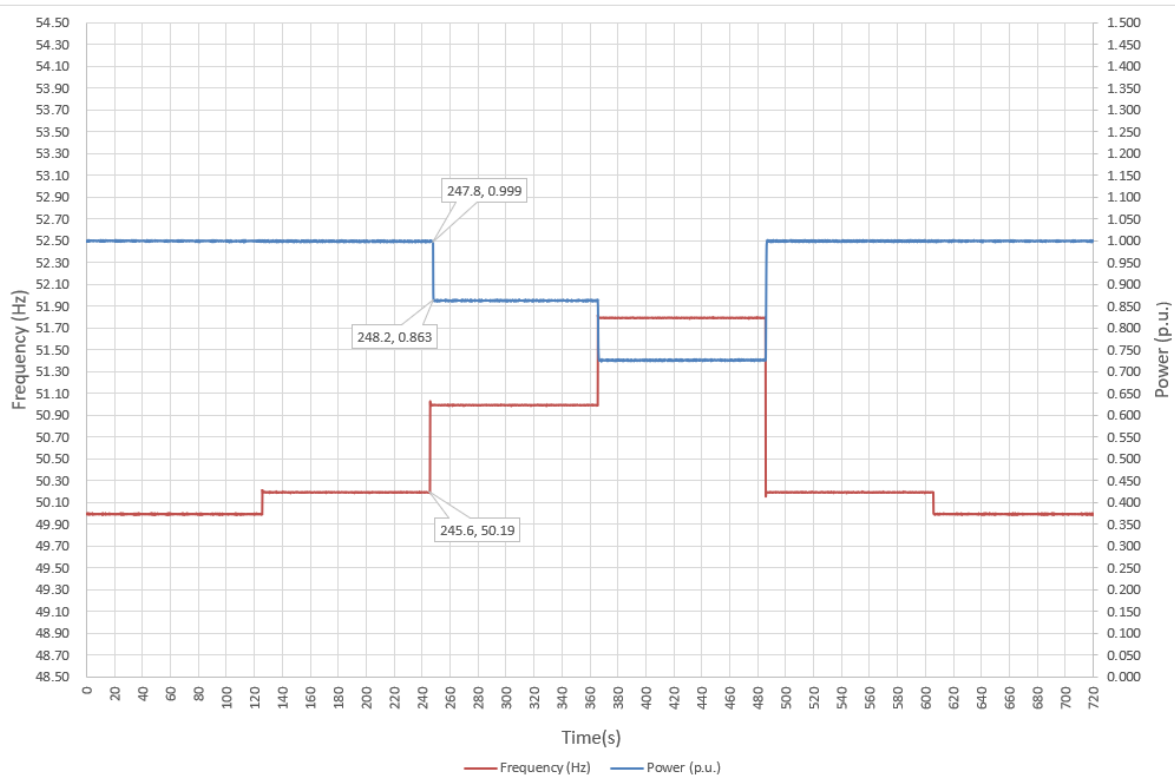
(1) The activation and deactivation of the function and its settings can be field adjustable through SunSpec Dashboard interface, under the menu of Freq_watt_param, ModEna = 0 indicating function deactivation, while ModEna = 1 indicating function activation; means to setting the parameter are protected by key of the SunSpec Dashboard.

(2) Threshold for disconnection overfrequency protection is set at 52.0 Hz at each test items.

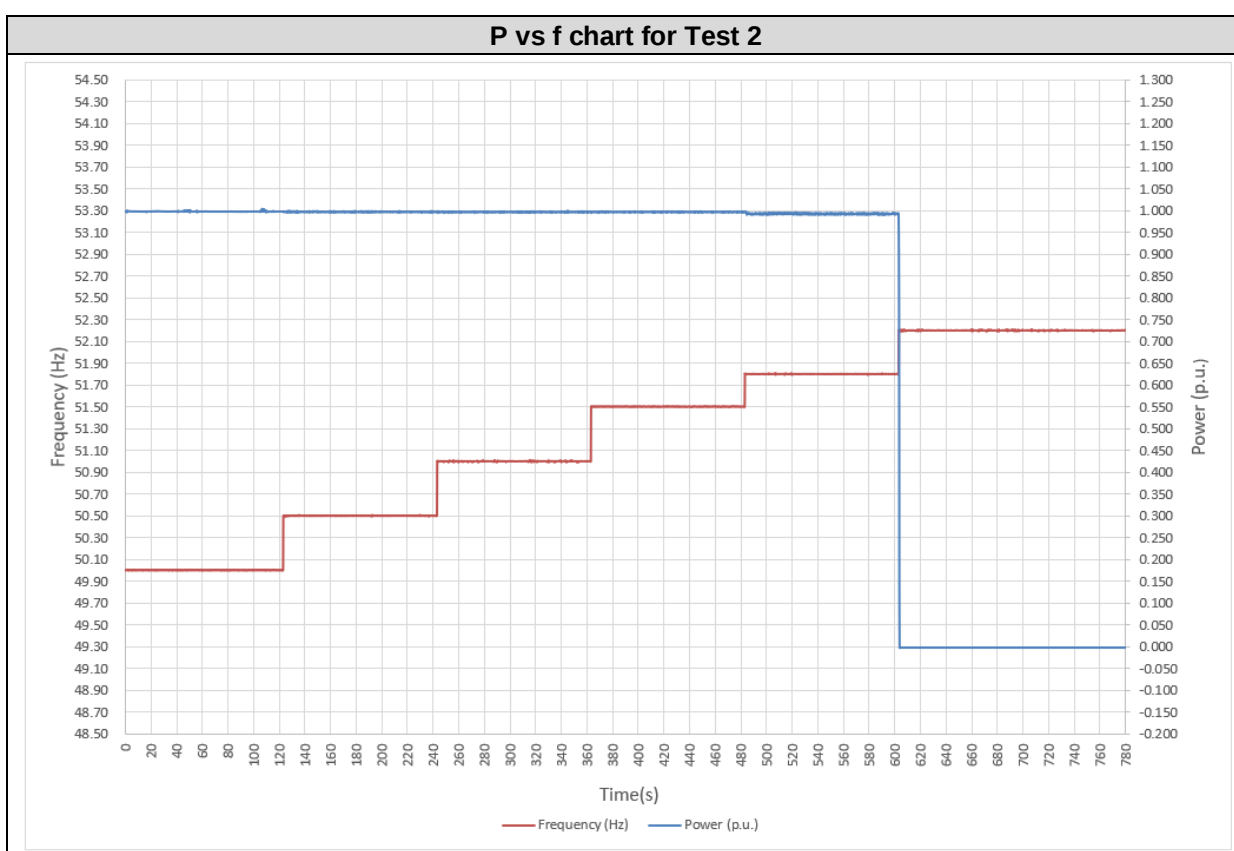
Test results are offered at the table below.

Test 1					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ±10 %)
1	50.00 ± 0.05 Hz	100.0	49.99	100.0	0.0
2	50.20 ± 0.05 Hz	100.0	50.19	99.9	-0.1
3	51.00 ± 0.05 Hz	86.7	50.99	86.3	-0.4
4	51.80 ± 0.05 Hz	73.3	51.79	72.7	-0.6
5	50.20 ± 0.05 Hz	100.0	50.20	99.8	-0.2
6	50.00 ± 0.05 Hz	100.0	49.99	100.0	0.0
Time delay setting from step 2 to step 3					
Time reference of change (s)			245.6		
End of delay (s)			247.8		
Delay time (s)			2.20		
End of change (s)			248.2		
Response time (s)			0.40		

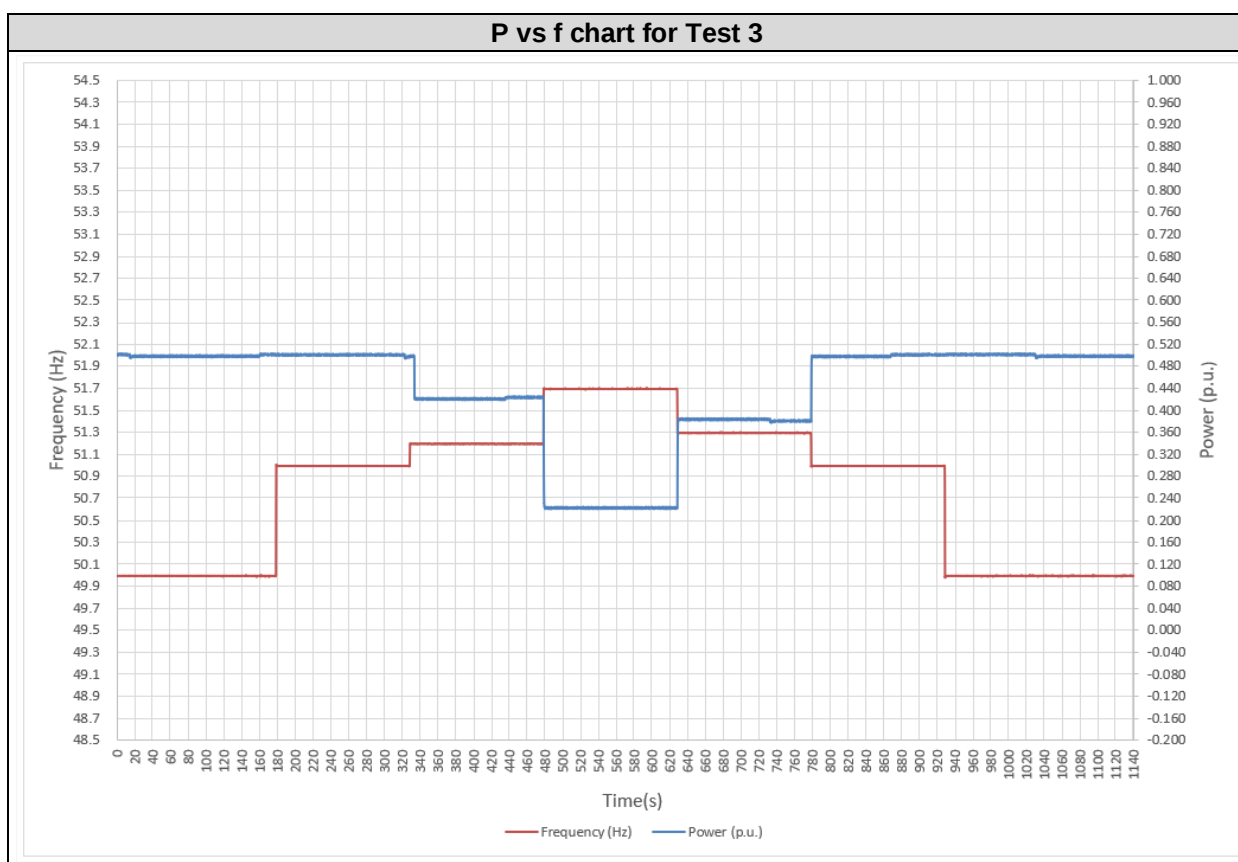
P vs f chart for Test 1



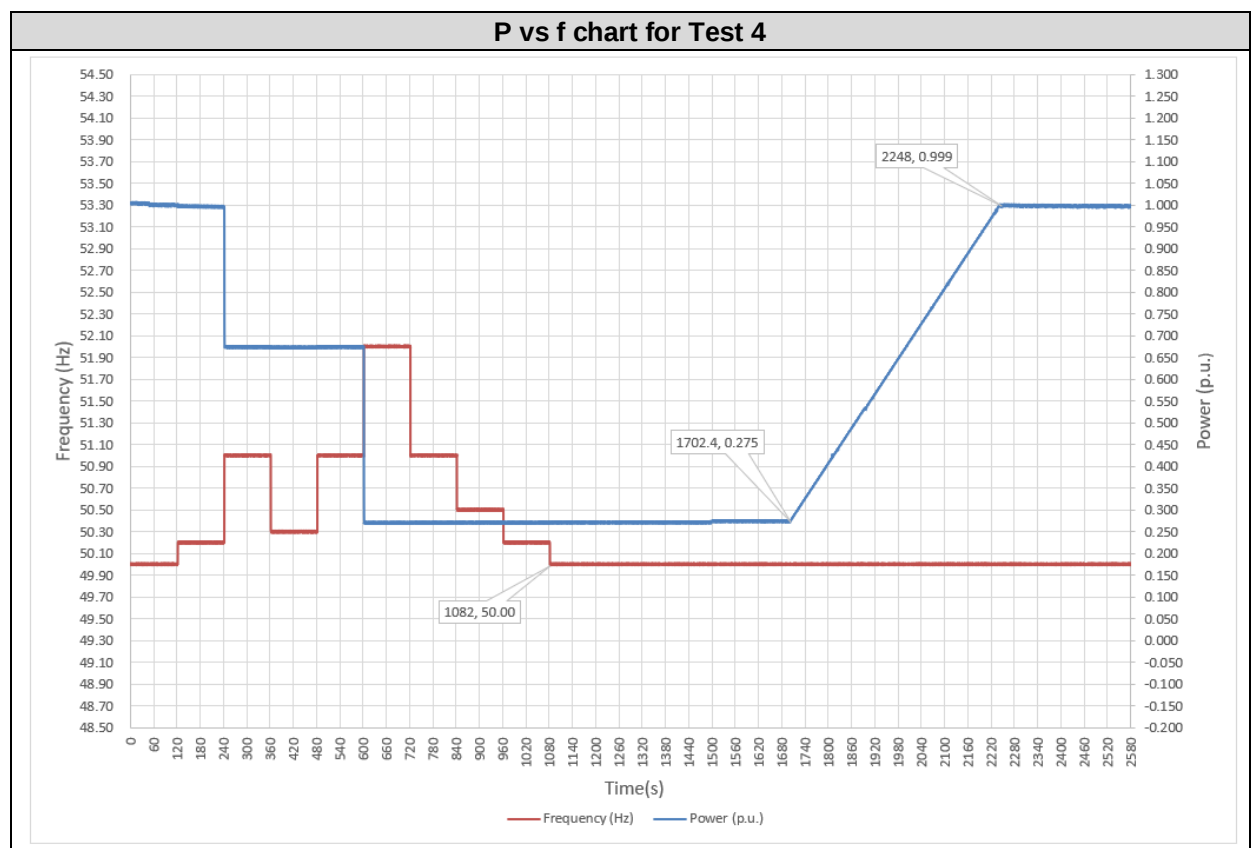
Test 2					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ±10 %)
1	50.00 ± 0.05 Hz	100.0	50.00	99.9	-0.1
2	50.50 ± 0.05 Hz	100.0	50.50	99.7	-0.3
3	51.00 ± 0.05 Hz	100.0	51.00	99.7	-0.3
4	51.50 ± 0.05 Hz	100.0	51.50	99.7	-0.3
5	51.80 ± 0.05 Hz	100.0	51.80	99.3	-0.7
6	52.20 ± 0.05 Hz	0.0	52.20	-0.2	-0.2



Test 3					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ±10 %)
1	50.00 ± 0.05 Hz	50.0	49.99	49.9	-0.1
2	51.00 ± 0.05 Hz	50.0	50.99	50.1	0.1
3	51.20 ± 0.05 Hz	42.0	51.19	42.4	0.4
4	51.70 ± 0.05 Hz	22.0	51.69	22.3	0.3
5	51.30 ± 0.05 Hz	38.0	51.29	38.2	0.2
6	51.00 ± 0.05 Hz	50.0	50.99	49.9	-0.1
7	50.00 ± 0.05 Hz	50.0	49.99	50.0	0.0



Test 4					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ± 10 %)
1	50.00 $\pm$ 0.05 Hz	100.0	50.00	100.2	0.2
2	50.20 $\pm$ 0.05 Hz	100.0	50.20	99.7	-0.3
3	51.00 $\pm$ 0.05 Hz	68.0	51.00	67.5	-0.5
4	50.30 $\pm$ 0.05 Hz	68.0	50.30	67.3	-0.7
5	51.00 $\pm$ 0.05 Hz	68.0	51.00	67.4	-0.6
6	52.00 $\pm$ 0.05 Hz	28.0	52.00	27.1	-0.9
7	51.00 $\pm$ 0.05 Hz	28.0	51.00	27.1	-0.9
8	50.50 $\pm$ 0.05 Hz	28.0	50.50	27.1	-0.9
9	50.20 $\pm$ 0.05 Hz	28.0	50.20	27.1	-0.9
10	50.00 $\pm$ 0.05 Hz	100.0	50.00	99.8	-0.2
Reconnection Time(s)		620.4		Power ramp gradient (%Pn/min)	8.0



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4.3.2. Power response to underfrequency

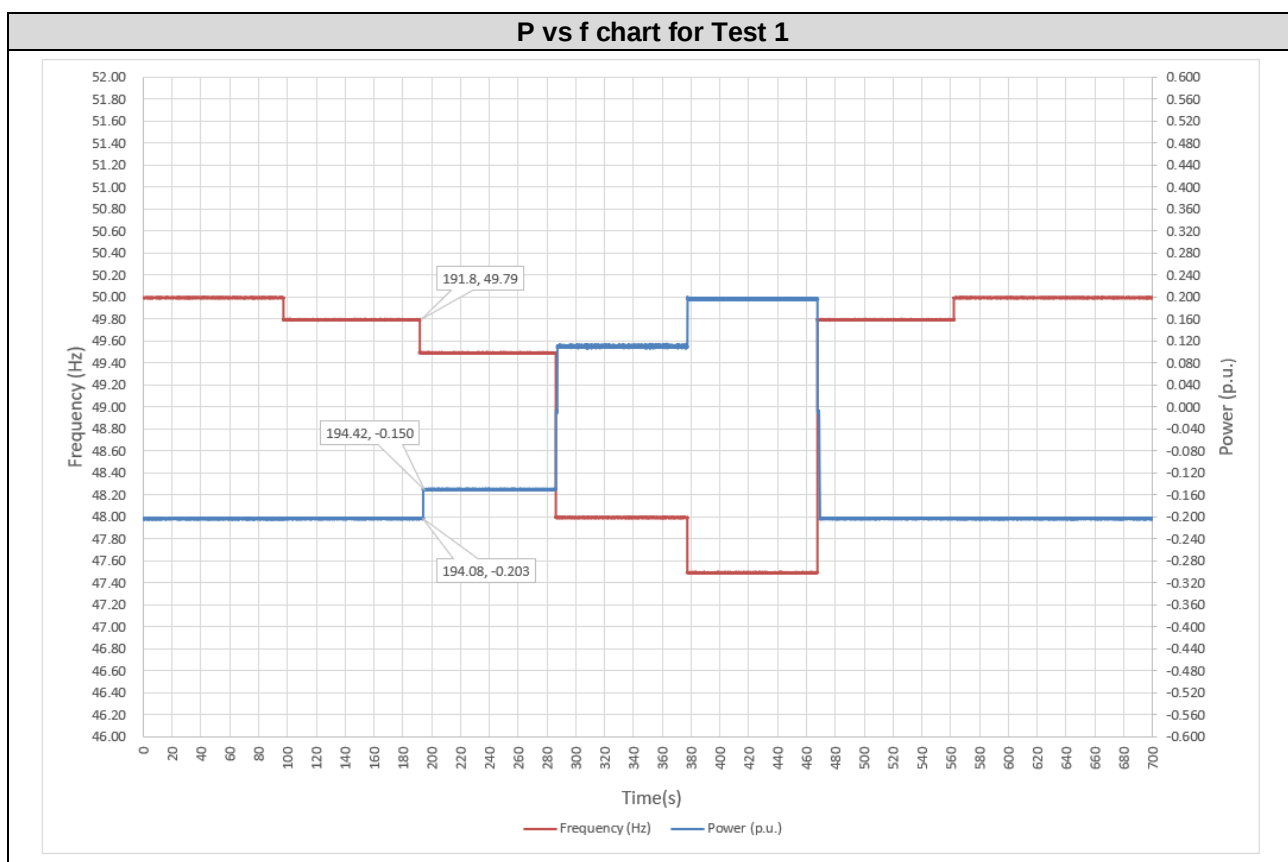
The test has been done according to the clause 4.6.2 of the standard. This test has been performed to show the capability of the inverter, although it is only mandatory for Energy Storage Systems according to the standard. The following definitions apply to the test to verify the clause:

- Test 1: $P_M = -20\%P_n$; $f_1 = 49.8\text{ Hz}$; droop = 12 %; f-stop deactivated, with delay of 2 s (*)
- Test 2: $P_M = -20\%P_n$; $f_1 = 48\text{ Hz}$; droop = 2 %; f-stop deactivated, no delay
- Test 3: $P_M = 50\%P_n$; $f_1 = 49.8\text{ Hz}$; droop = 5 %; f-stop deactivated, no delay

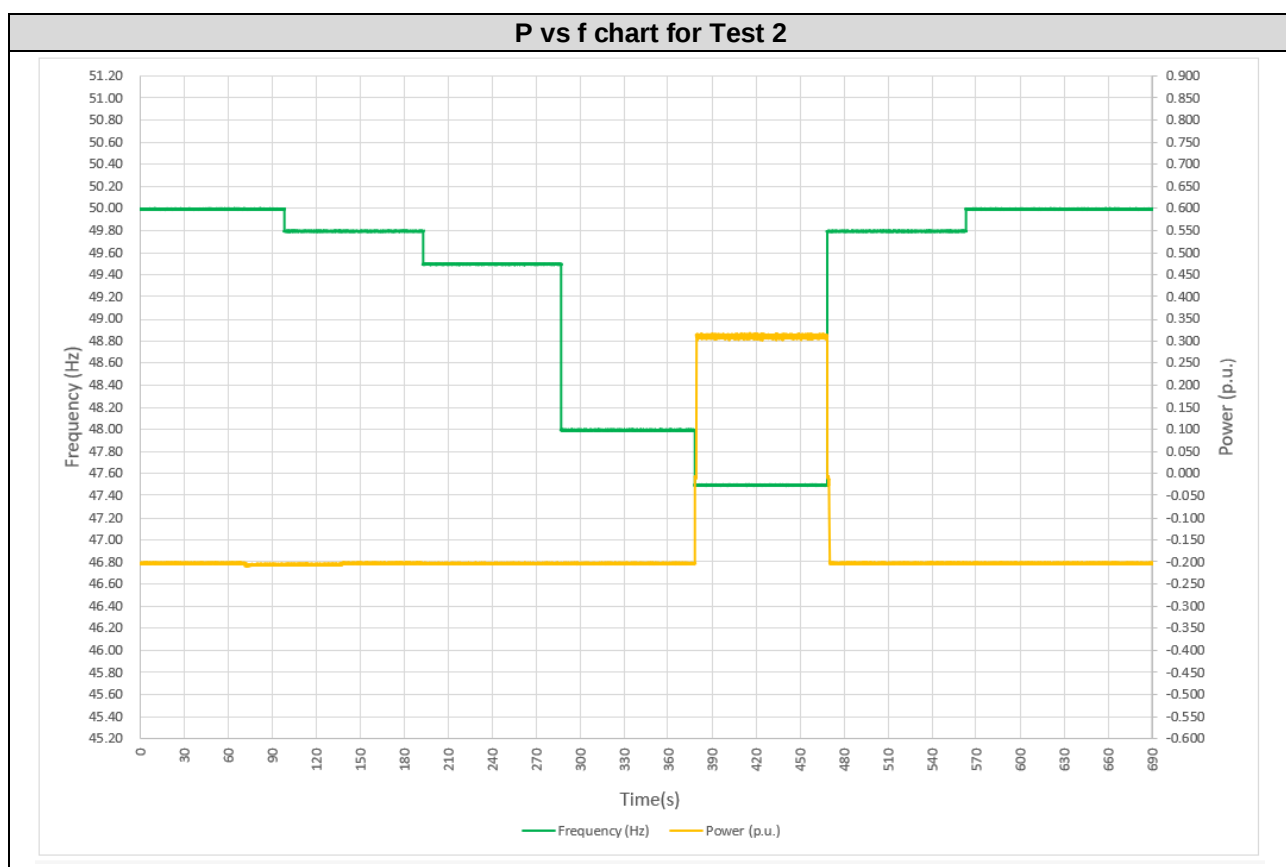
(*) The intentional delay is only active for the activation of the function, once the function is operating, the established control loop is not intentionally delayed.

Test results are offered at the table below.

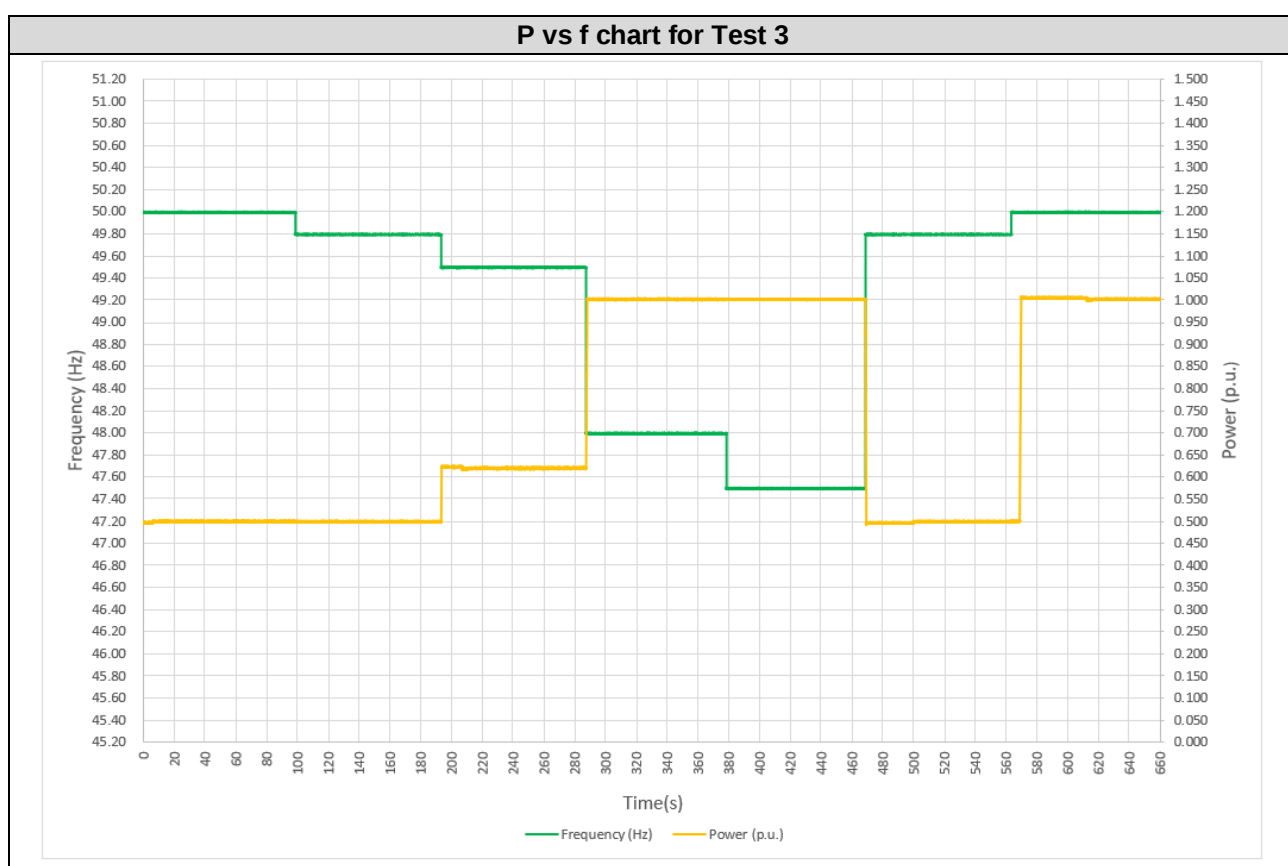
Test 1					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ±10 %)
1	50.00 ± 0.05 Hz	-20.0	49.99	-20.3	-0.3
2	49.80 ± 0.05 Hz	-20.0	49.79	-20.3	-0.3
3	49.50 ± 0.05 Hz	-15.0	49.49	-15.1	-0.1
4	48.00 ± 0.05 Hz	10.0	48.00	10.8	0.8
5	47.50 ± 0.05 Hz	18.3	47.50	19.6	1.3
6	49.80 ± 0.05 Hz	-20.0	49.79	-19.9	0.1
7	50.00 ± 0.05 Hz	-20.0	49.99	-20.3	-0.3
Time delay setting from step 2 to step 3					
Time reference of change (s)			191.80		
End of delay (s)			193.80		
Delay time (s)			2.28		
End of change (s)			193.94		
Response time (s)			0.34		



Test 2					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ±10 %)
1	50.00 ± 0.05 Hz	-20.0	49.99	-20.4	-0.4
2	49.80 ± 0.05 Hz	-20.0	49.79	-20.4	-0.4
3	49.50 ± 0.05 Hz	-20.0	49.49	-20.3	-0.3
4	48.00 ± 0.05 Hz	-20.0	47.99	-20.3	-0.3
5	47.50 ± 0.05 Hz	30.0	47.49	30.7	0.7
6	49.80 ± 0.05 Hz	-20.0	49.79	-20.0	0.0
7	50.00 ± 0.05 Hz	-20.0	49.99	-20.3	-0.3



Test 3					
Step	f (Hz)	P desired (%Pn)	Frequency meas. (Hz)	P meas. (%Pn)	P deviation (%Pn) (within ±10 %)
1	50.00 ± 0.05 Hz	50.0	49.99	49.9	-0.1
2	49.80 ± 0.05 Hz	50.0	49.79	49.9	-0.1
3	49.50 ± 0.05 Hz	62.0	49.49	62.0	0.0
4	48.00 ± 0.05 Hz	100.0	47.99	100.0	0.0
5	47.50 ± 0.05 Hz	100.0	47.49	100.1	0.1
6	49.80 ± 0.05 Hz	50.0	49.79	49.9	-0.1
7	50.00 ± 0.05 Hz	100.0	49.99	97.6	-2.4



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4.4. POWER RESPONSE TO VOLTAGE CHANGES

The generating unit shall be capable of operating in the control modes specified below within the limits specified in 4.7.2.2. The control modes are exclusive; only one mode may be active at a time.

- Q setpoint mode
- Q (U)
- Cos ϕ setpoint mode
- Cos ϕ (P)

4.4.1. Setpoint control modes

The test has been done according to the clause 4.7.2.3.2 of the standard. The following definitions apply to the test to verify the clause:

- Test 1: Q Zero ($Q = 0\%S_n$)
- Test 2: Rectangular Curve ($Q = \pm 48.4\%S_n$)
- Test 3: Triangular Curve ($PF = \pm 0.8$)
- Test 4: Reactive power capability at active power PD in the voltage range ($0.85U_n - 1.1U_n$)

4.4.1.1. Test 1: Q Zero (Q = 0 %Sn)

This test verifies the capability of the inverter to provide a fixed value of reactive power. In addition, it is verified the Q control mode.

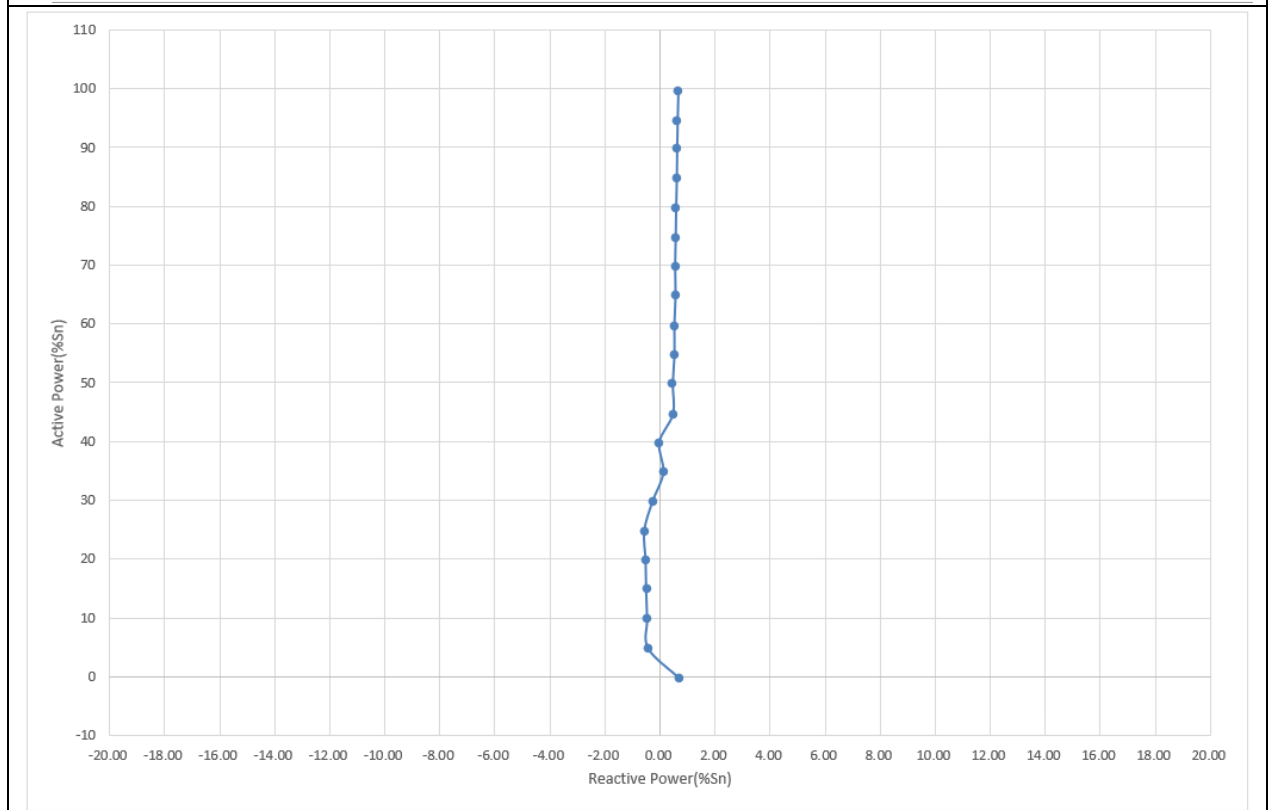
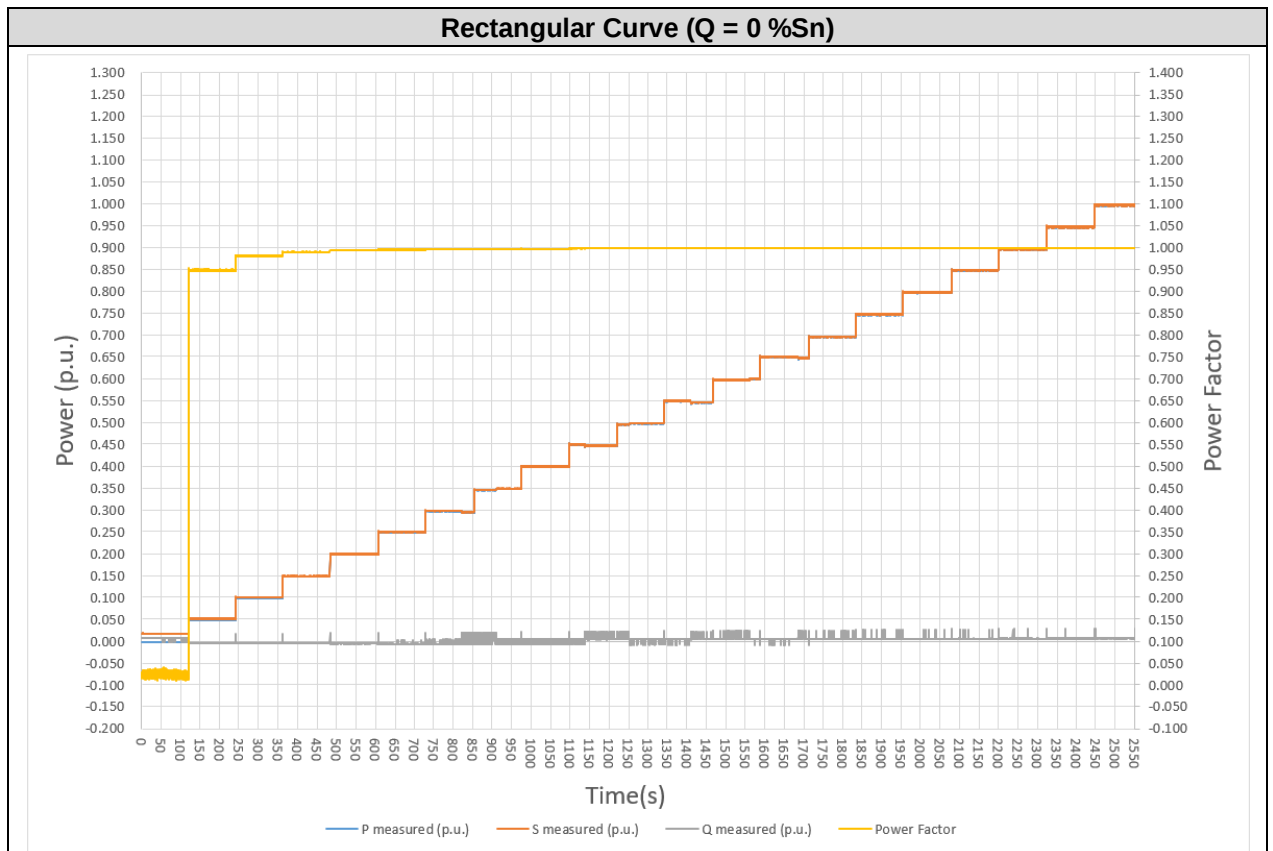
When the measurement is equal to or greater than 10% Sn, the allowable tolerance of reactive power measurement should be within $\pm 2\%$ Sn.

Test results are offered at tables below.

Rectangular Curve (Q = 0 %Sn)						
P desired (%Sn)	Power DC (kW)	P measured (%Sn)	Q desired (%Sn)	Q measured (%Sn)	Q deviation (%Sn)	Power Factor (cos ϕ)
0(*)	0.4	-0.3	--	0.7	--	0.025
5(*)	3.1	4.9	--	-0.4	--	0.947
10(*)	5.7	9.8	0.0	-0.5	-0.5	0.982
15	8.2	14.8	0.0	-0.5	-0.5	0.991
20	10.8	19.9	0.0	-0.5	-0.5	0.994
25	13.4	24.8	0.0	-0.6	-0.6	0.996
30	16.0	29.8	0.0	-0.3	-0.3	0.997
35	18.6	34.7	0.0	0.1	0.1	0.998
40	21.2	39.9	0.0	0.0	0.0	0.998
45	23.9	44.7	0.0	0.5	0.5	0.998
50	26.5	49.9	0.0	0.5	0.5	0.999
55	29.1	54.8	0.0	0.5	0.5	0.999
60	31.8	59.7	0.0	0.5	0.5	0.999
65	34.4	64.9	0.0	0.5	0.5	0.999
70	37.1	69.7	0.0	0.5	0.5	0.999
75	39.8	74.7	0.0	0.6	0.6	0.999
80	42.5	79.7	0.0	0.6	0.6	0.999
85	45.1	84.7	0.0	0.6	0.6	0.999
90	47.8	89.7	0.0	0.6	0.6	0.999
95	50.5	94.7	0.0	0.6	0.6	0.999
100	53.2	99.7	0.0	0.7	0.7	0.999

(*) No tolerance of reactive power was defined when active power level below 10%Sn

Test results are represented at diagrams below.



4.4.1.2. Test 2: Rectangular Curve ($Q = \pm 48.4 \% S_n$)

This test verifies the capability of the inverter to provide a fixed value of reactive power. In addition, it is verified the Q control mode.

When the measurement is equal to or greater than 10% S_n , the allowable tolerance of reactive power measurement should be within $\pm 2\% S_n$.

Test results are offered at tables below.

Rectangular Curve ($Q = 48.4 \% S_n$ / Inductive)						
P desired (% S_n)	Power DC (kW)	P measured (% S_n)	Q desired (% S_n)	Q measured (% S_n)	Q deviation (% S_n)	Power Factor ($\cos \phi$)
0(*)	1.1	-0.5	--	49.2	--	-0.009
5(*)	4.2	5.6	--	49.1	--	0.113
10(*)	6.2	9.5	--	49.1	--	0.189
15	9.3	15.5	48.4	49.1	0.6	0.301
20	11.3	19.8	48.4	49.0	0.6	0.373
25	14.4	25.6	48.4	49.0	0.5	0.461
30	16.4	29.6	48.4	48.9	0.5	0.516
35	19.5	35.9	48.4	48.9	0.4	0.590
40	21.6	39.7	48.4	48.8	0.4	0.629
45	24.7	45.7	48.4	48.8	0.4	0.682
50	26.8	49.8	48.4	48.8	0.4	0.712
55	30.0	55.8	48.4	48.8	0.3	0.751
60	32.1	59.8	48.4	48.7	0.3	0.774
65	35.2	65.8	48.4	48.7	0.2	0.803
70	37.3	69.9	48.4	48.6	0.2	0.819
75	40.5	75.9	48.4	48.6	0.2	0.841
80	42.6	79.9	48.4	48.6	0.2	0.853
85	45.8	85.9	48.4	48.5	0.1	0.870
90	48.0	89.9	48.4	48.5	0.1	0.879
95	51.2	95.9	48.4	48.4	0.0	0.892
100	53.3	99.8	48.4	48.4	0.0	0.899

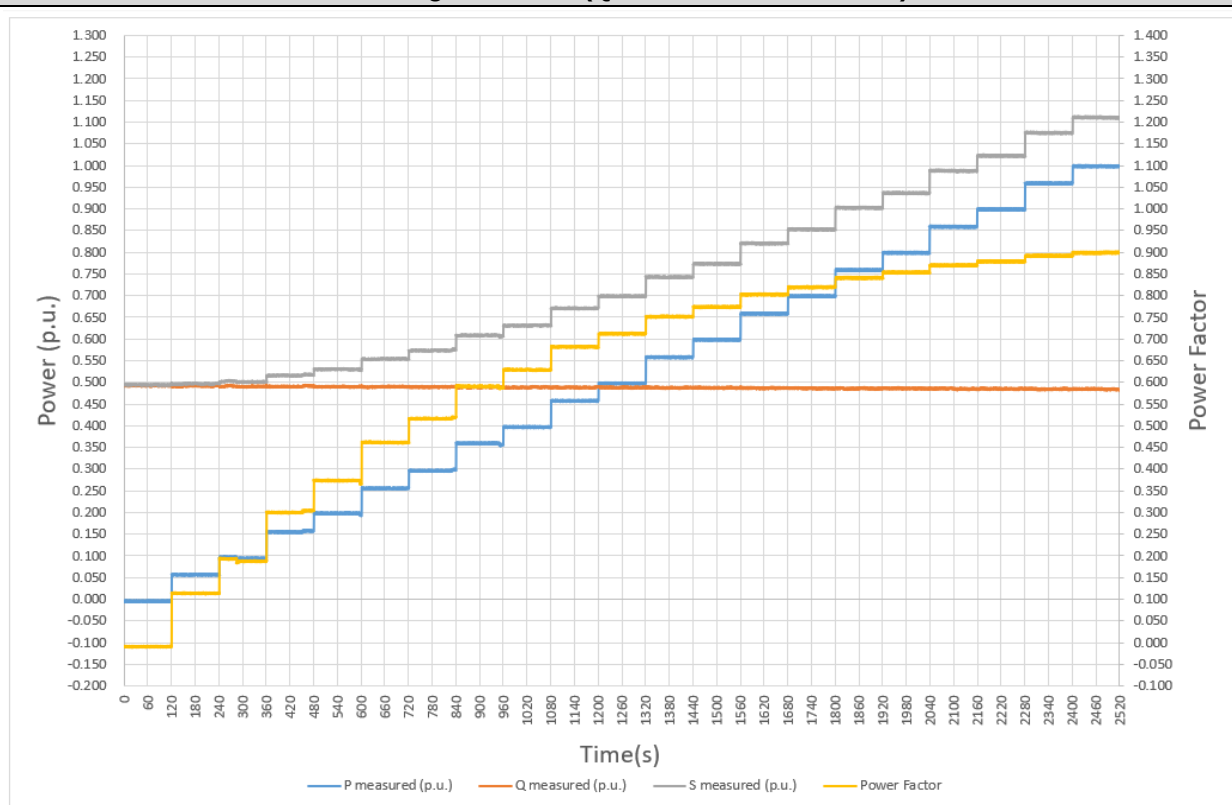
(*) No tolerance of reactive power was defined when active power level below 10% S_n

Rectangular Curve (Q = 48.4 %Sn / Capacitive)						
P desired (%Sn)	Power DC (kW)	P measured (%Sn)	Q desired (%Sn)	Q measured (%Sn)	Q deviation (%Sn)	Power Factor (cos φ)
0(*)	0.9	-1.9	--	-49.3	--	-0.039
5(*)	3.9	4.3	--	-49.2	--	0.086
10(*)	7.0	10.5	--	-49.3	--	0.209
15	9.0	14.4	-48.4	-49.4	-1.0	0.279
20	12.1	20.4	-48.4	-49.5	-1.0	0.380
25	14.1	24.7	-48.4	-49.5	-1.1	0.446
30	17.2	30.5	-48.4	-49.5	-1.1	0.524
35	19.3	34.5	-48.4	-49.5	-1.1	0.571
40	22.4	40.5	-48.4	-49.6	-1.1	0.632
45	24.4	44.6	-48.4	-49.5	-1.1	0.668
50	27.6	50.6	-48.4	-49.6	-1.1	0.713
55	29.7	54.6	-48.4	-49.6	-1.1	0.740
60	32.8	60.7	-48.4	-49.6	-1.1	0.773
65	34.9	64.7	-48.4	-49.6	-1.2	0.793
70	38.1	70.7	-48.4	-49.6	-1.2	0.817
75	40.2	74.7	-48.4	-49.6	-1.2	0.832
80	43.4	80.7	-48.4	-49.7	-1.2	0.851
85	45.5	84.7	-48.4	-49.7	-1.3	0.862
90	48.7	90.7	-48.4	-49.7	-1.3	0.876
95	50.9	94.7	-48.4	-49.7	-1.3	0.885
100	54.1	100.7	-48.4	-49.8	-1.3	0.896

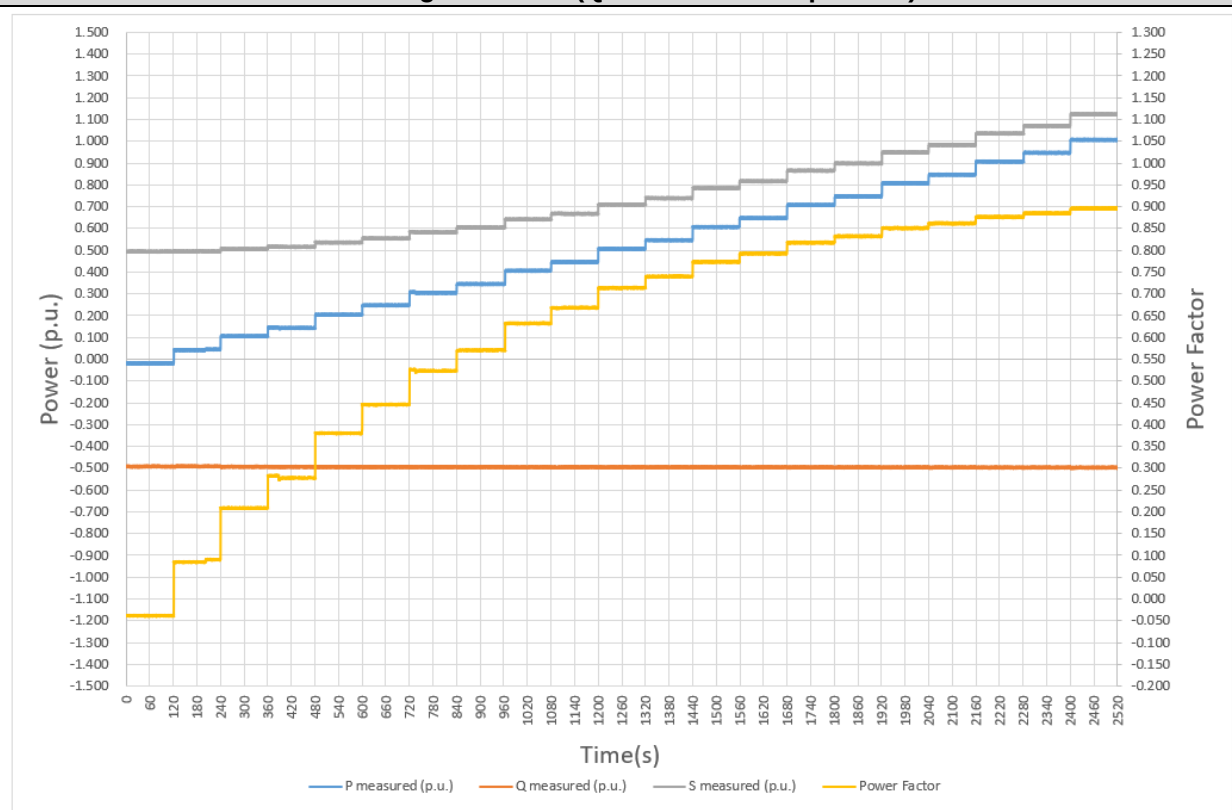
(*) No tolerance of reactive power was defined when active power level below 10%Sn

Test results are represented at diagrams below.

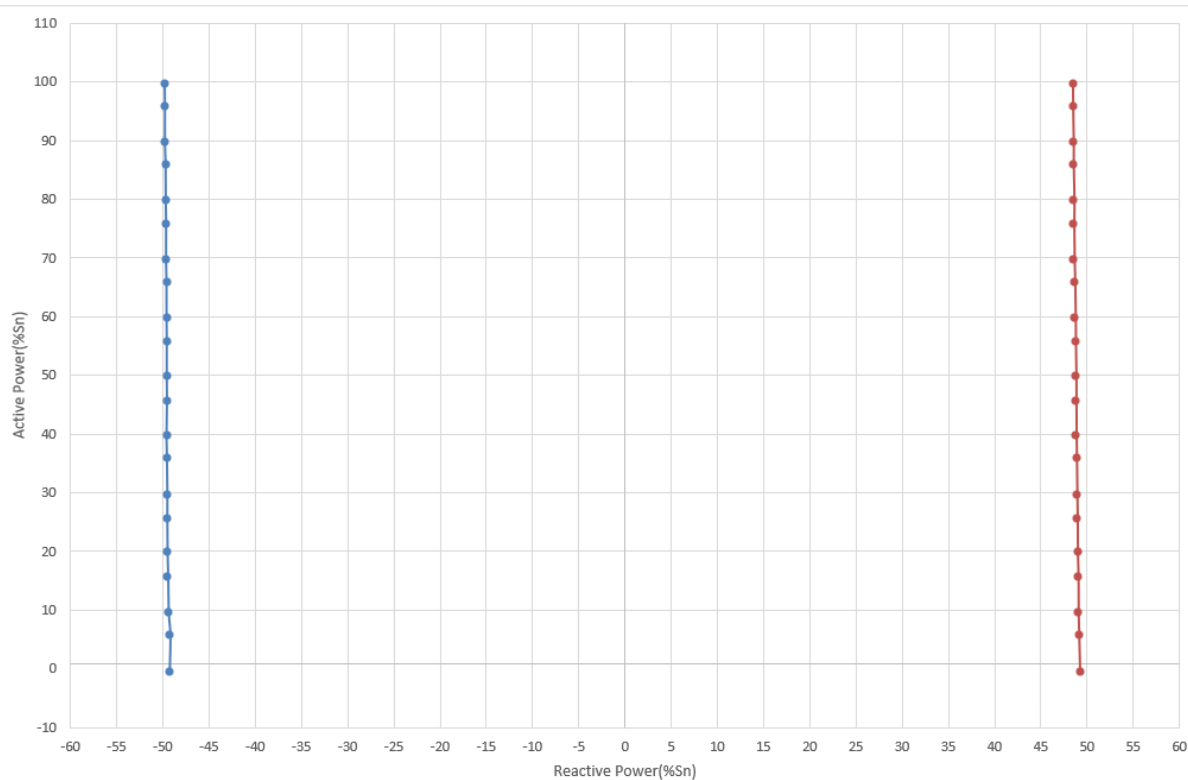
Rectangular Curve (Q = 48.4 %Sn / Inductive)



Rectangular Curve (Q = 48.4 %Sn / Capacitive)



Rectangular Curve (Capacitive vs Inductive)



4.4.1.3. Test 3: Triangular Curve (PF = ± 0.8)

This test verifies the capability of the inverter to provide a fixed value of power factor. In addition, it is verified the PF control mode.

When the measurement is equal to or greater than 10% S_n , the allowable tolerance of reactive power measurement should be within $\pm 2\%$ S_n .

Test results are offered at the tables below.

Triangular Curve (PF = 0.8 / inductive)						
P desired (% S_n)	Power DC (kW)	P measured (% S_n)	Q measured (% S_n)	Power factor desired (cos ϕ)	Power factor measured (cos ϕ)	Power factor deviation (cos ϕ)
0(*)	0.2	-1.1	-0.1	--	-0.505	--
5(*)	3.4	5.1	3.0	--	0.794	--
10(*)	5.5	9.1	5.9	--	0.802	--
15	8.6	15.2	10.4	0.800	0.805	0.005
20	10.7	19.3	13.5	0.800	0.805	0.005
25	13.8	25.4	18.1	0.800	0.806	0.006
30	15.9	29.7	21.4	0.800	0.805	0.005
35	19.1	35.8	25.9	0.800	0.806	0.006
40	21.3	39.7	28.9	0.800	0.805	0.005
45	24.5	45.7	33.4	0.800	0.804	0.004
50	26.6	49.7	36.4	0.800	0.804	0.004
55	29.8	55.8	40.9	0.800	0.804	0.004
60	32.0	59.8	43.9	0.800	0.804	0.004
65	35.2	65.9	48.4	0.800	0.804	0.004
70	37.4	69.9	51.4	0.800	0.804	0.004
75	40.7	75.9	55.9	0.800	0.804	0.004
80	42.9	80.0	58.9	0.800	0.804	0.004
85	46.2	86.0	63.3	0.800	0.804	0.004
90	48.4	90.0	66.3	0.800	0.804	0.004
95 (**)	49.4	91.7	67.0	0.800	0.807	0.007
100 (**)	49.3	91.6	66.9	0.800	0.807	0.007

(*) No tolerance of Power Factor was defined when active power level below 10% S_n

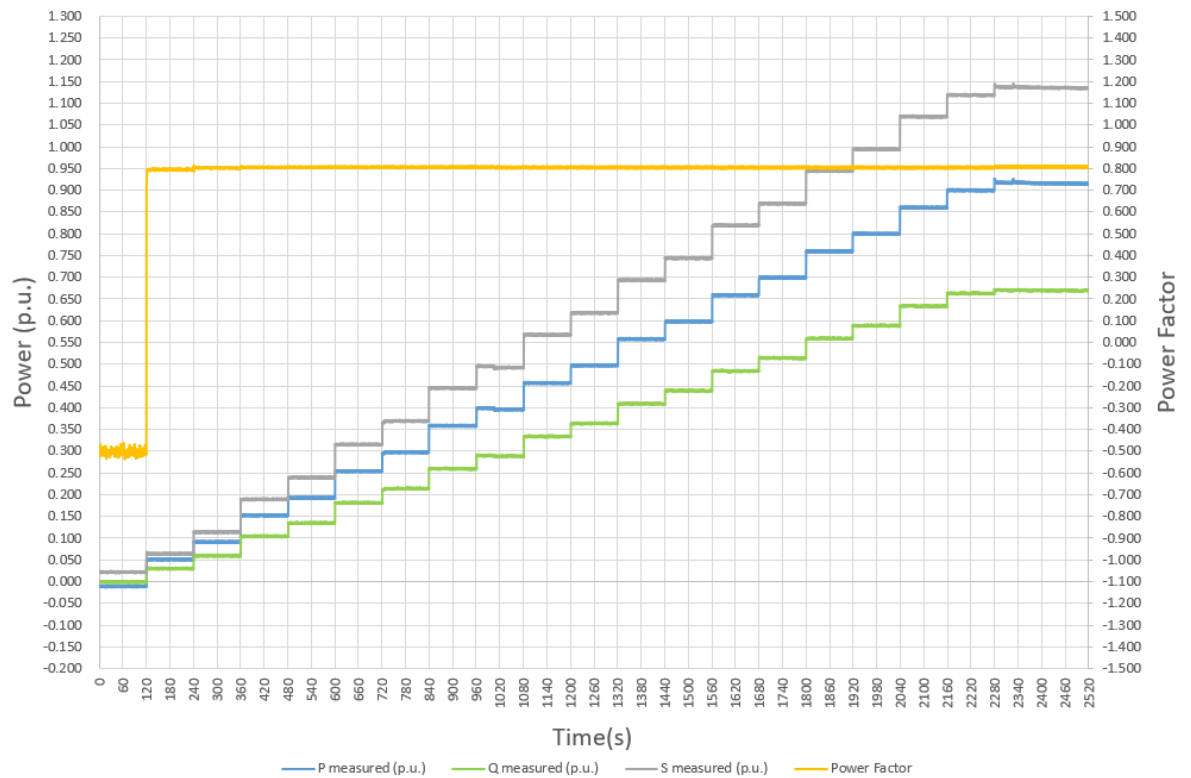
(**) The desired value of active power has not been reached due to power factor is 0.8 of the inverter.

Triangular Curve (PF = 0.8 / Capacitive)						
P desired (%Sn)	Power DC (kW)	P measured (%Sn)	Q measured (%Sn)	Power factor desired (cos ϕ)	Power factor measured (cos ϕ)	Power factor deviation (cos ϕ)
0(*)	-0.4	-0.8	3.3	--	-0.199	--
5(*)	2.6	5.3	-3.2	--	0.793	--
10(*)	5.6	11.2	-7.9	--	0.794	--
15	7.6	15.2	-11.0	0.800	0.793	-0.007
20	10.6	21.1	-15.8	0.800	0.793	-0.007
25	12.5	24.9	-18.7	0.800	0.793	-0.007
30	15.6	31.1	-23.5	0.800	0.793	-0.007
35	17.5	35.0	-26.6	0.800	0.792	-0.008
40	20.4	40.9	-31.2	0.800	0.792	-0.008
45	22.5	45.1	-34.4	0.800	0.793	-0.007
50	25.4	50.8	-39.0	0.800	0.791	-0.009
55	27.4	54.7	-42.1	0.800	0.791	-0.009
60	30.3	60.7	-46.8	0.800	0.791	-0.009
65	32.3	64.7	-49.9	0.800	0.791	-0.009
70	35.3	70.6	-54.5	0.800	0.790	-0.010
75	37.3	74.6	-57.6	0.800	0.791	-0.009
80	40.3	80.5	-62.3	0.800	0.790	-0.010
85	42.2	84.4	-65.3	0.800	0.790	-0.010
90	45.2	90.3	-69.9	0.800	0.790	-0.010
95	47.1	94.3	-72.9	0.800	0.790	-0.010
100	49.2	98.5	-74.1	0.800	0.798	-0.002

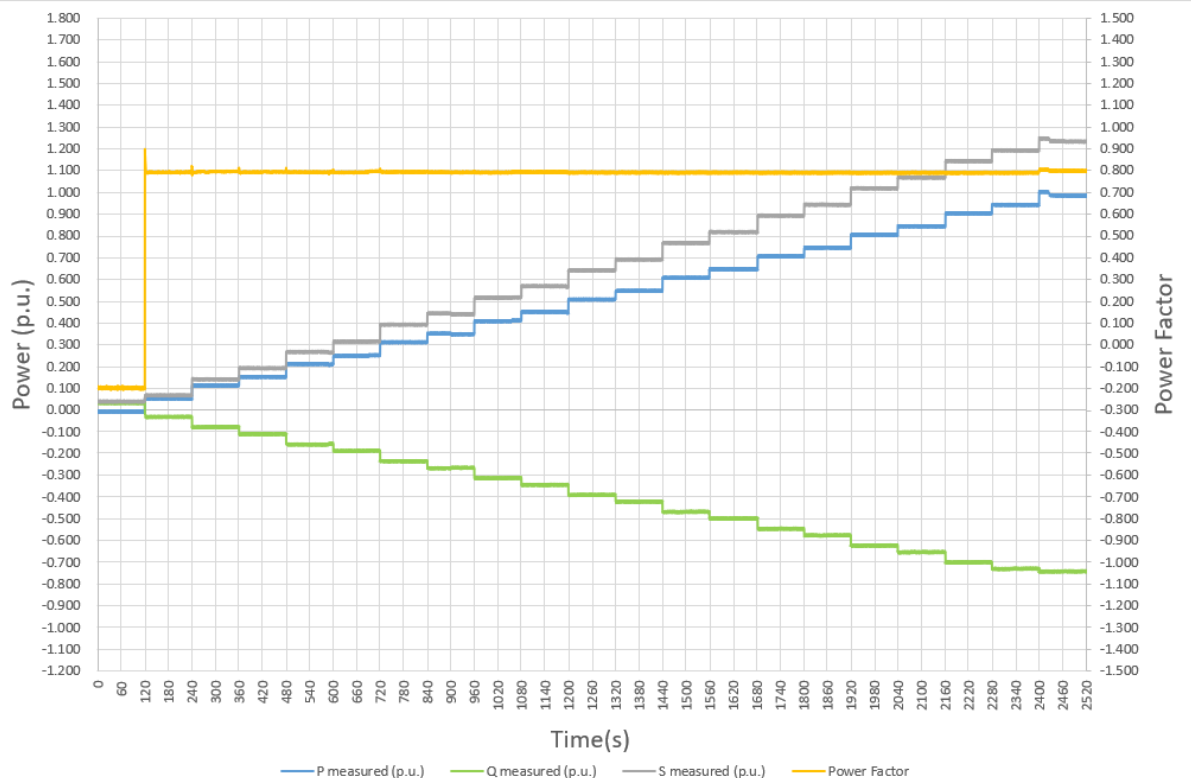
(*) No tolerance of Power Factor was defined when active power level below 10%Sn

Test results are represented at the diagrams below.

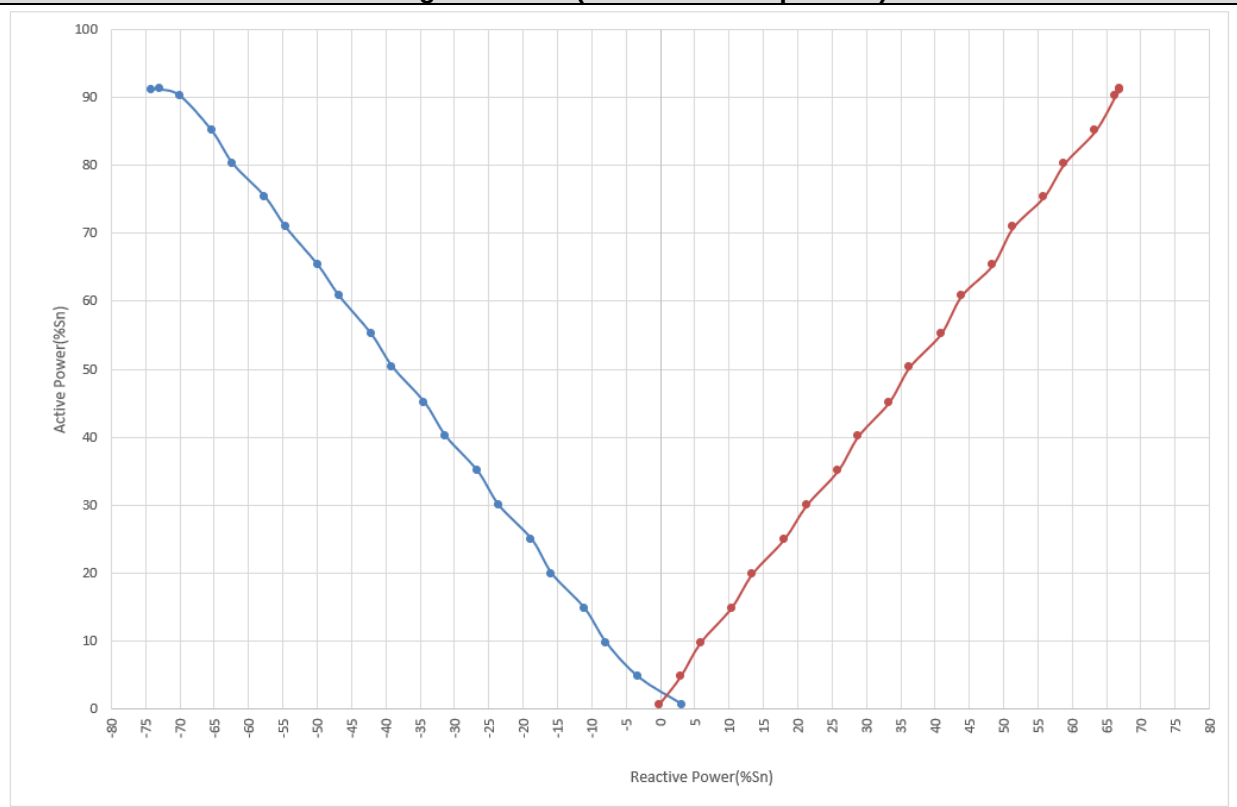
Triangular Curve (PF = 0.8 / Inductive)



Triangular Curve (PF = 0.8 / Capacitive)



Triangular Curve (Inductive vs Capacitive)



4.4.1.4. Test 4: Reactive power capability at active power P_D in the voltage range (0.85Un~1.1Un)

This test verifies the capability of the inverter to provide reactive power capability at active power P_D in the voltage range, as the Figure 13 of standard:

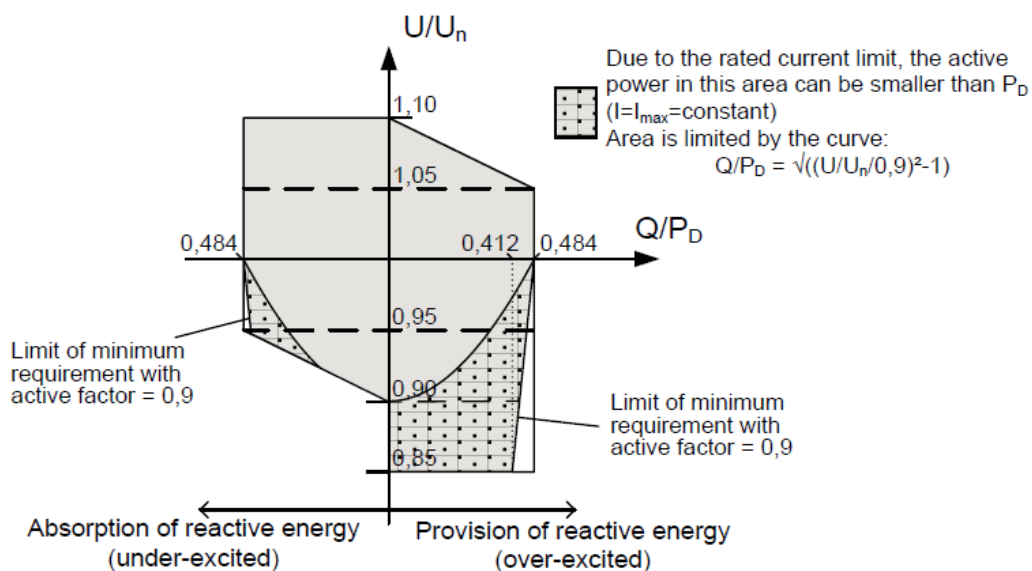


Figure 13 — Reactive power capability at active power P_D in the voltage range (positive sequence component of the fundamental)

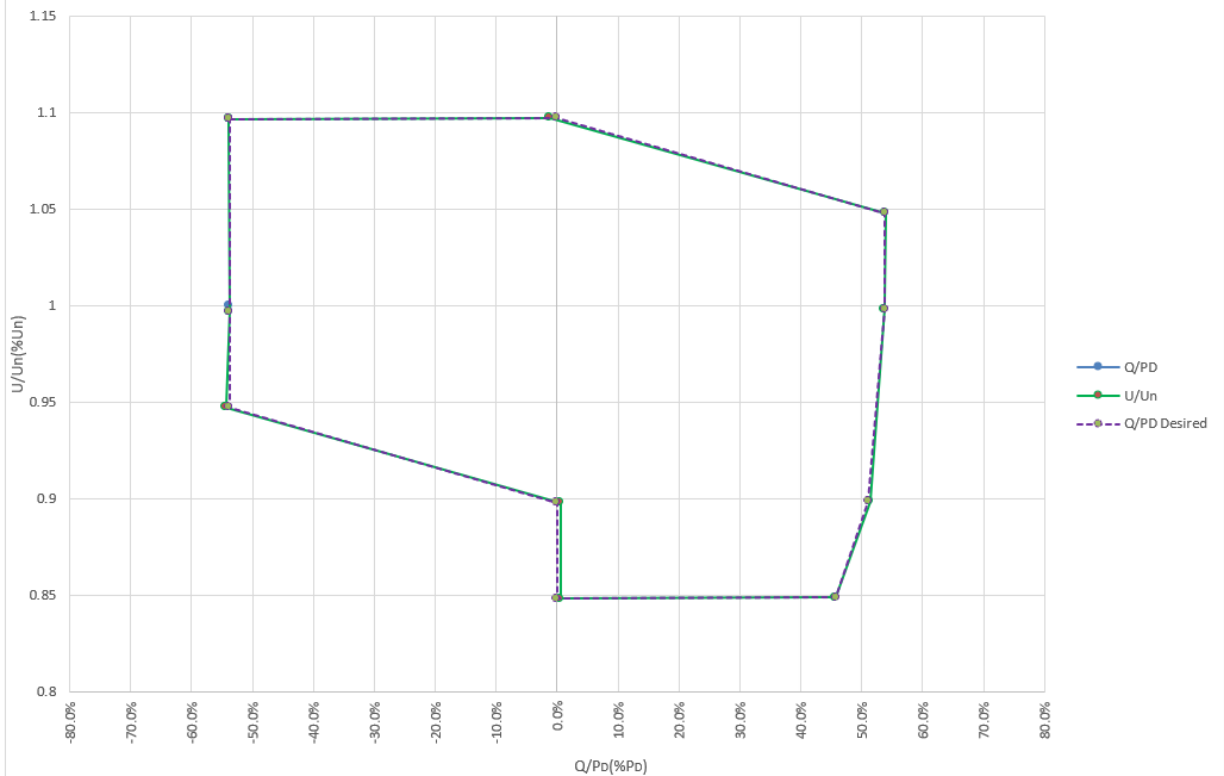
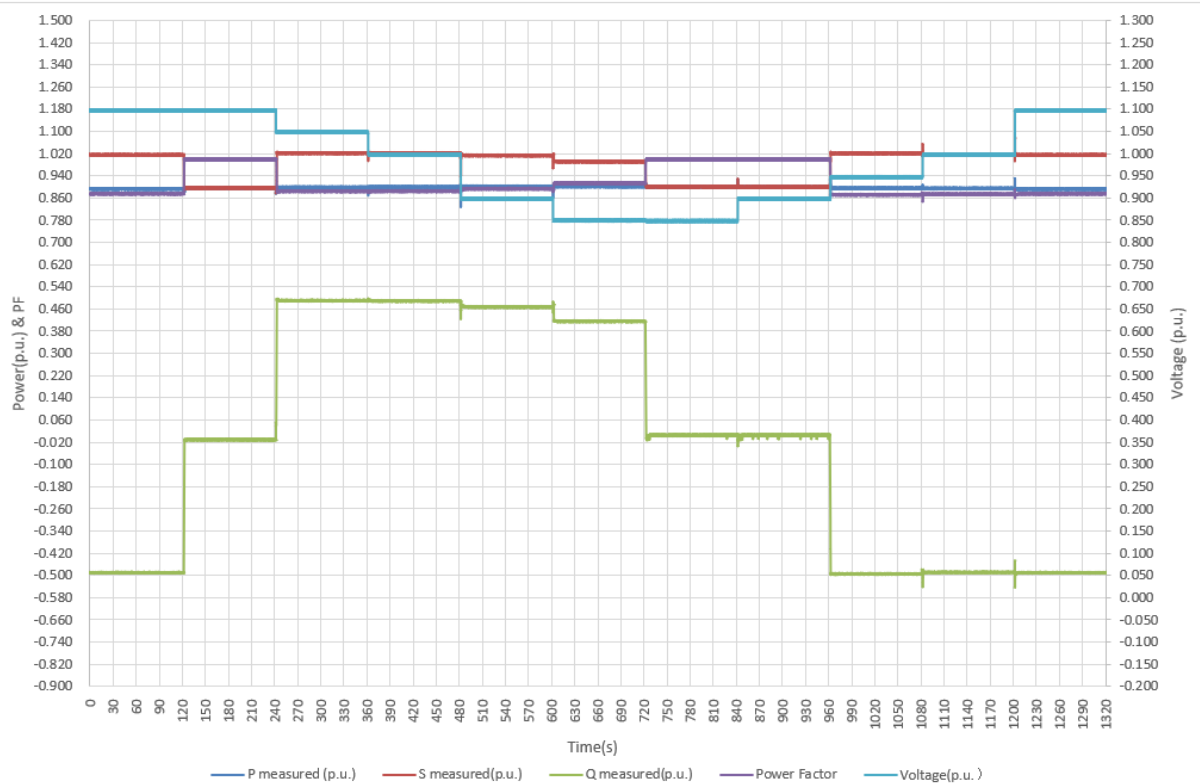
Allowed tolerance for reactive power measurements is to be considered inside $\pm 2\% S_n$ ($\pm 2.2\% P_D$)

Test results are offered at the tables below.

Reactive power capability at active power P_D in the voltage range								
Step	Voltage desired (p.u.)	Voltage meas. (p.u.)	P desired (% S_n)	P meas. (% S_n)	Q meas. (% P_D)	Q desired (% P_D)	Q deviation (% P_D)	Power Factor measured ($\cos \varphi$)
1	1.100	1.097	90.0	89.2	-54.4	-53.8	-0.6	0.875
2	1.100	1.097	90.0	89.5	-1.2	0.0	-1.2	0.999
3	1.050	1.048	90.0	89.8	54.5	53.8	0.7	0.884
4	1.000	0.998	90.0	102.1	54.3	53.8	0.5	0.885
5	0.900	0.899	90.0	101.3	51.9	51.1	0.8	0.893
6	0.850	0.849	90.0	99.1	46.2	45.8	0.4	0.913
7	0.850	0.848	90.0	90.0	0.5	0.0	0.5	0.999
8	0.900	0.898	90.0	90.0	0.5	0.0	0.5	0.999
9	0.950	0.947	90.0	102.1	-54.9	-53.8	-1.2	0.871
10	1.000	0.997	90.0	101.7	-54.3	-53.8	-0.5	0.874
11	1.100	1.097	90.0	101.5	-54.5	-53.8	-0.7	0.875

Note: $P_D = 0.9 P_n$ as declared by manufacturer, while voltage operating range is at $0.85U_n \sim 1.1U_n$.

Reactive power capability at active power PD in the voltage range (0.85Un~1.00Un)



4.4.2. Voltage related control mode

4.4.2.1. Voltage related control mode Q(U)

The test has been done according to the clause 4.7.2.3.3 of the standard.

Note: The activation and deactivation of the function and its settings can be field adjustable through SunSpec Dashboard interface, under the menu of Volt_var, ModEna = 0 indicating function deactivation, while ModEna = 1 indicating function activation; means to setting the parameter are protected by key of the SunSpec Dashboard.

Setting the characteristic as following to prove configurability of the inverter:

- U1 = 0.93, Q_{max}
- U2 = 0.96, Q = 0.1 p.u.
- U3 = 1.04, Q = - 0.1 p.u.
- U4 = 1.07, -Q_{max}

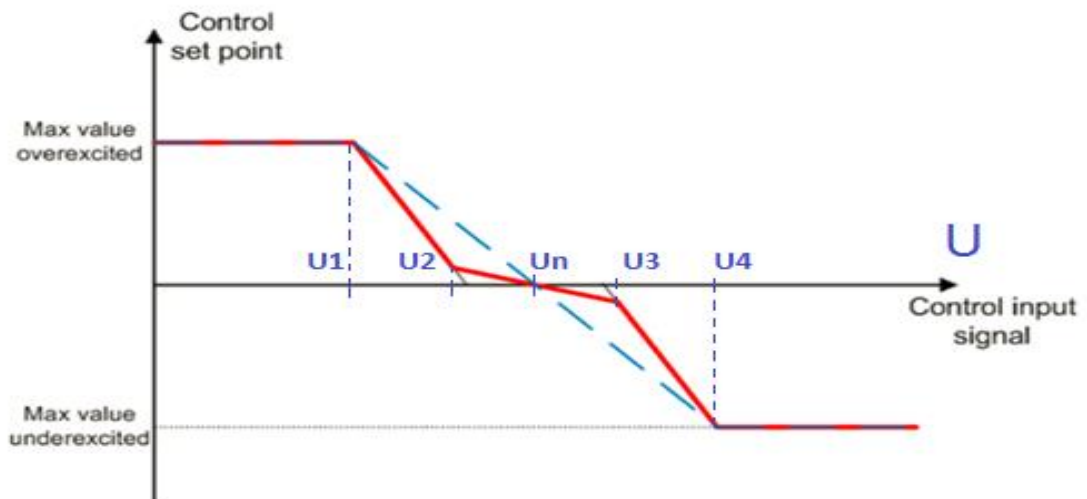


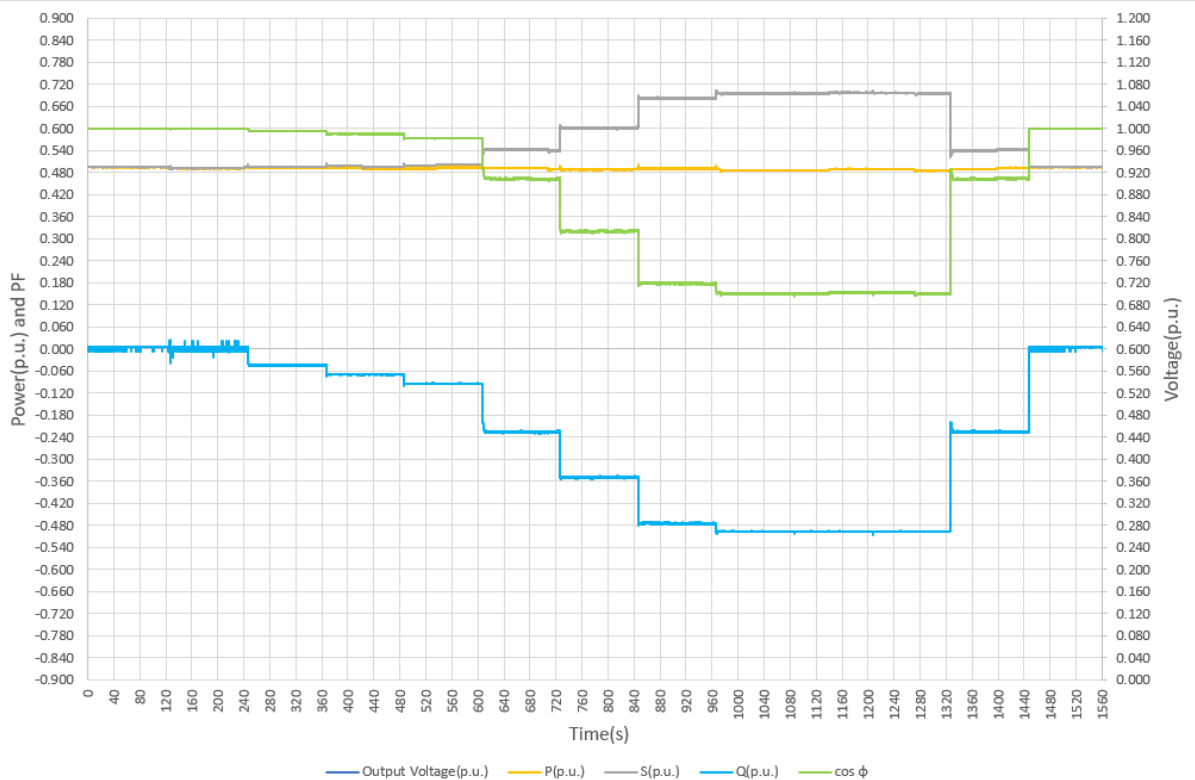
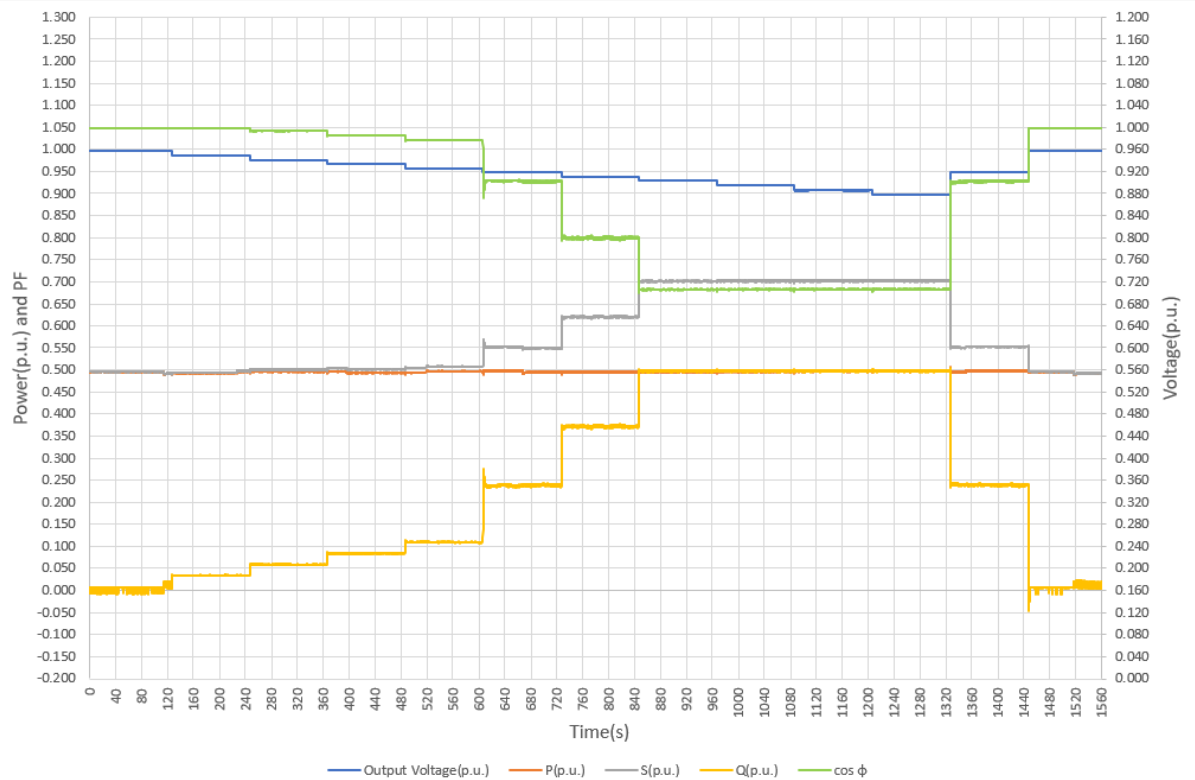
Figure 16 — Example characteristics for Q respectively $\cos \varphi$ control mode

Test results are offered at the tables below.

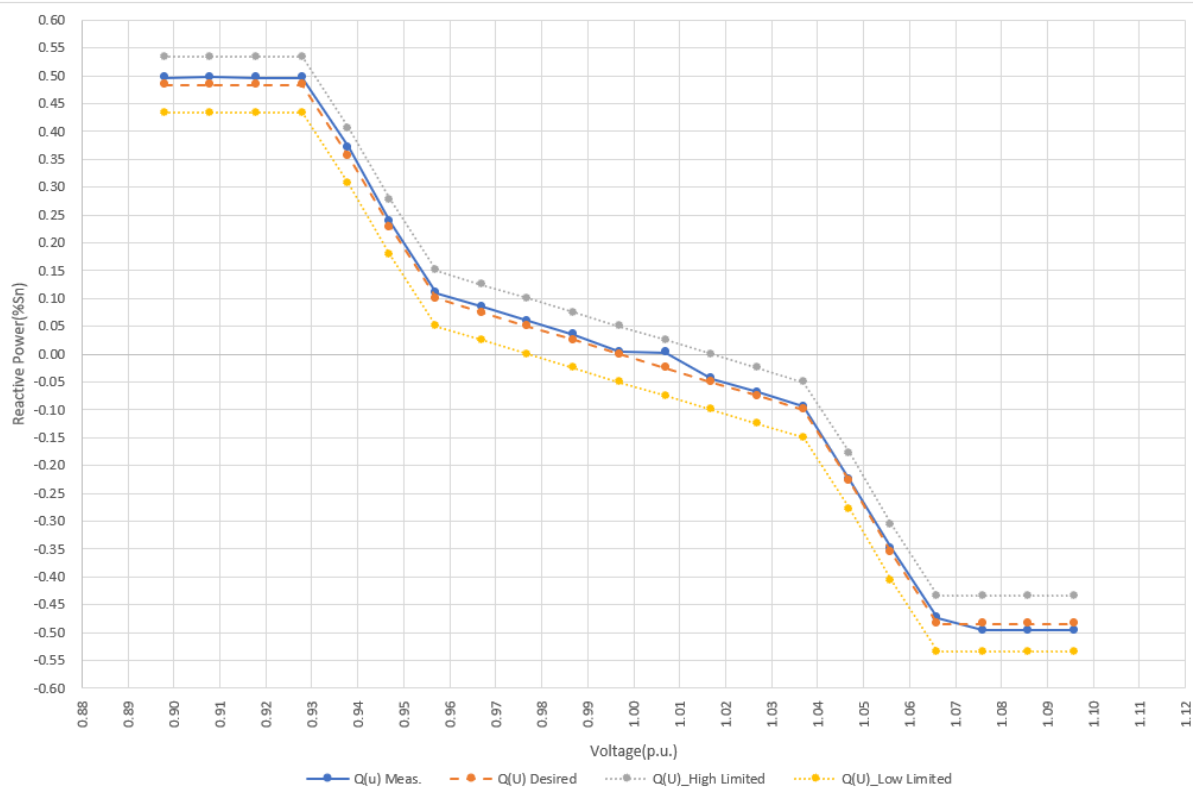
P/Pn setpoint (%)	U setpoint	P measured (p.u.)	V measured (p.u.)	Q measured (p.u.)	Q desired (p.u.)	ΔQ ($< \pm 0.05$ p.u.)	Power Factor measured ($\cos \varphi$)
50	1.000 Un	0.495	0.997	0.004	0.000	0.004	0.999
50	0.990 Un	0.493	0.987	0.035	0.025	0.010	0.997
50	0.980 Un	0.496	0.977	0.060	0.050	0.010	0.993
50	0.970 Un	0.494	0.967	0.085	0.075	0.010	0.986
50	0.960 Un	0.497	0.957	0.110	0.100	0.010	0.976
50	0.950 Un	0.496	0.947	0.238	0.228	0.010	0.902
50	0.940 Un	0.495	0.938	0.372	0.356	0.016	0.799
50	0.930 Un	0.495	0.928	0.497	0.484	0.013	0.706
50	0.920 Un	0.496	0.918	0.497	0.484	0.013	0.706
50	0.910 Un	0.496	0.908	0.498	0.484	0.014	0.706
50	0.900 Un	0.496	0.898	0.497	0.484	0.013	0.706
50	0.950 Un	0.498	0.948	0.239	0.228	0.011	0.901
50	1.000 Un	0.494	0.997	0.006	0.000	0.006	0.999

P/Pn setpoint (%)	U setpoint	P measured (p.u.)	V measured (p.u.)	Q measured (p.u.)	Q desired (p.u.)	ΔQ ($< \pm 0.05$ p.u.)	Power Factor measured ($\cos \varphi$)
50	1.000 Un	0.495	0.997	0.005	0.000	0.005	0.999
50	1.010 Un	0.492	1.007	0.003	-0.025	0.028	0.999
50	1.020 Un	0.494	1.017	-0.044	-0.050	0.006	0.996
50	1.030 Un	0.492	1.027	-0.069	-0.075	0.006	0.990
50	1.040 Un	0.493	1.037	-0.094	-0.100	0.006	0.982
50	1.050 Un	0.493	1.047	-0.226	-0.228	0.002	0.909
50	1.060 Un	0.489	1.056	-0.349	-0.356	0.007	0.814
50	1.070 Un	0.491	1.066	-0.474	-0.484	0.010	0.719
50	1.080 Un	0.487	1.076	-0.496	-0.484	-0.012	0.700
50	1.090 Un	0.489	1.086	-0.496	-0.484	-0.012	0.702
50	1.100 Un	0.488	1.096	-0.496	-0.484	-0.012	0.701
50	1.050 Un	0.491	1.047	-0.225	-0.228	0.003	0.909
50	1.000 Un	0.495	0.997	0.005	0.000	0.005	0.999

Voltage related control mode - Q(U)



Voltage vs Reactive Power



4.4.2.2 Voltage related control mode Q(U) with lock-in/lock-out function

The test has been done according to the clause 4.7.2.3.3 of the standard.

Two active power levels shall be configurable both at least in the range of 0 % to 100 % of PD. The lock-in value turns the Q(U) mode on, the lock-out value turns Q(U) off. If lock-in is larger than lock-out a hysteresis is given. See also Figure 14 in the standard.

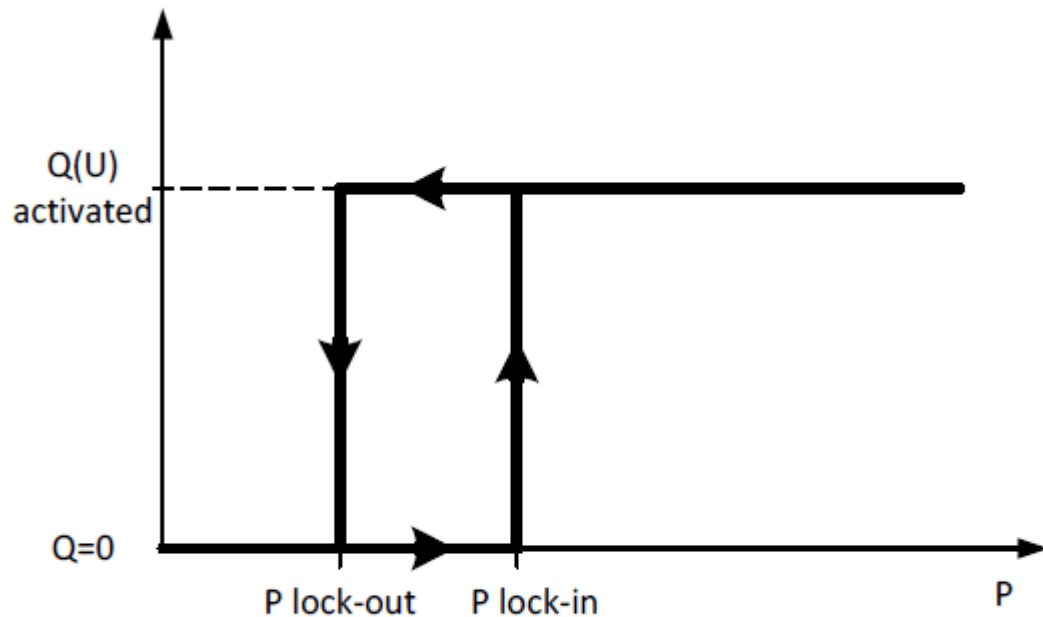


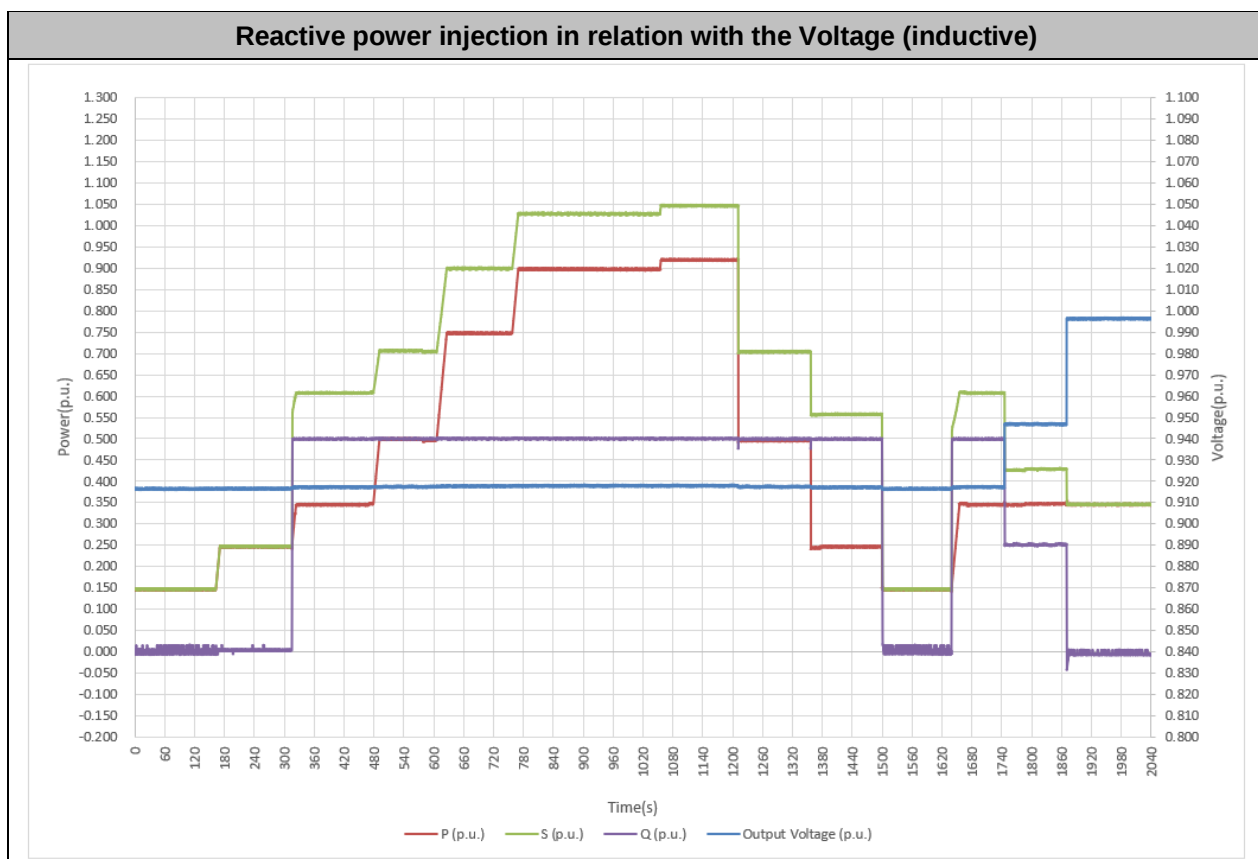
Figure 14 – Example of lock-in and lock-out values for Q(U) mode

Setting the characteristic as following to prove configurability of the inverter:

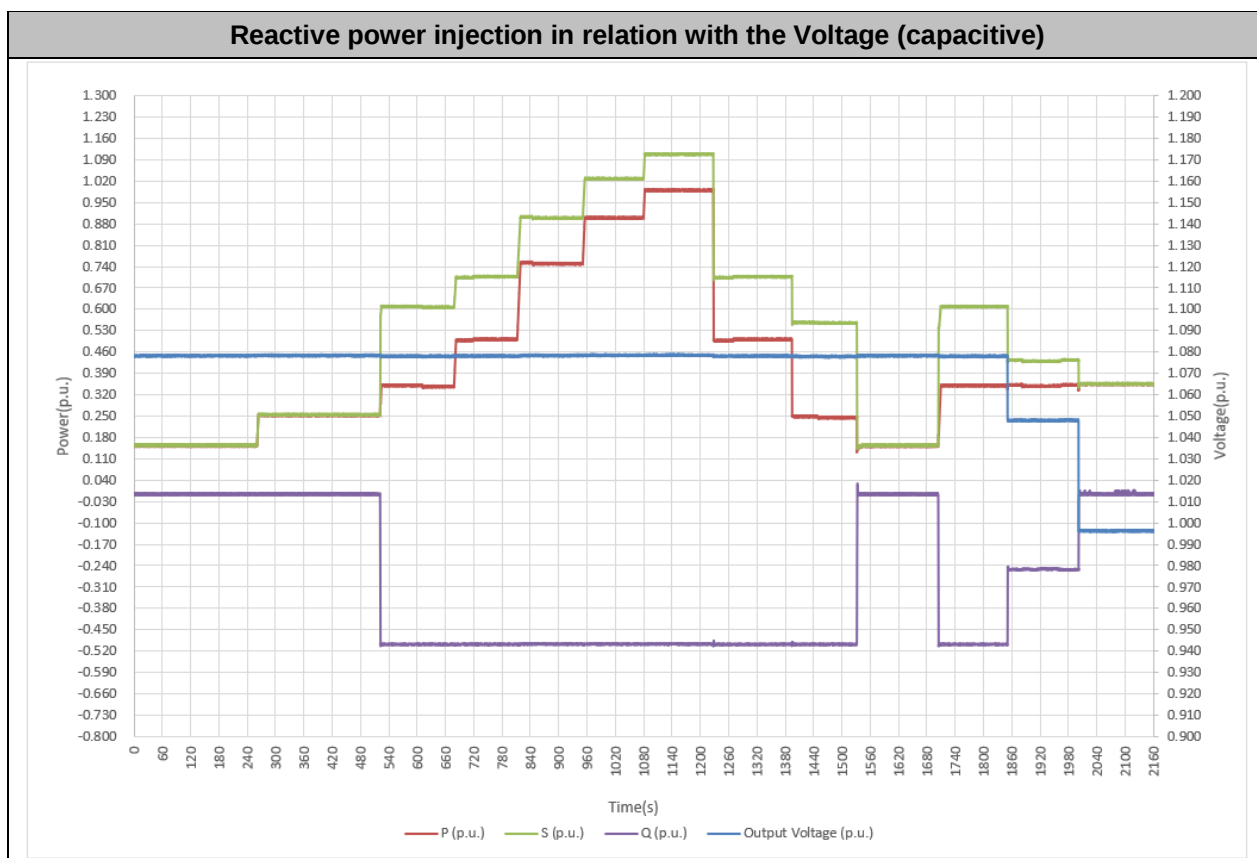
- $U1 = 0.93$, Q_{max}
 - $U2 = 0.96$, $Q = 0.1$ p.u.
 - $U3 = 1.04$, $Q = -0.1$ p.u.
 - $U4 = 1.07$, $-Q_{max}$
- P lock-in = 30 %, P lock-out = 20 %

Test results are offered at the tables below.

Reactive power injection in relation with the Voltage (inductive)						
P/Pn setpoint (%Pn)	U setpoint (p.u.)	P meas. (p.u.)	V meas. (p.u.)	Q meas. (p.u.)	Q desired (p.u.)	ΔQ (< ± 0.05 p.u.)
<20	0.920	0.146	0.916	0.002	0.000	0.002
25	0.920	0.247	0.917	0.004	0.000	0.004
35	0.920	0.345	0.917	0.500	0.484	0.015
50	0.920	0.499	0.917	0.500	0.484	0.016
75	0.920	0.748	0.918	0.500	0.484	0.016
90	0.920	0.898	0.918	0.500	0.484	0.016
100	0.920	0.919	0.918	0.500	0.484	0.016
50	0.920	0.496	0.917	0.500	0.484	0.016
25	0.920	0.247	0.917	0.500	0.484	0.015
<20	0.920	0.146	0.917	0.002	0.000	0.002
35	0.920	0.345	0.917	0.500	0.484	0.015
35	0.950	0.347	0.947	0.251	0.242	0.010
35	1.000	0.345	0.996	-0.005	0.000	-0.005



Reactive power injection in relation with the Voltage (capacitive)						
P/Pn setpoint (%Pn)	U setpoint (p.u.)	P meas. (p.u.)	V meas. (p.u.)	Q meas. (p.u.)	Q desired (p.u.)	ΔQ (< ± 0.05 p.u.)
<20	1.080	0.153	1.078	-0.006	0.000	-0.006
25	1.080	0.253	1.078	-0.006	0.000	-0.006
35	1.080	0.347	1.078	-0.498	-0.484	-0.014
50	1.080	0.502	1.078	-0.498	-0.484	-0.014
75	1.080	0.749	1.078	-0.497	-0.484	-0.013
90	1.080	0.900	1.079	-0.497	-0.484	-0.013
100	1.080	0.990	1.079	-0.497	-0.484	-0.013
50	1.080	0.501	1.078	-0.498	-0.484	-0.014
25	1.080	0.246	1.078	-0.498	-0.484	-0.014
<20	1.080	0.152	1.078	-0.006	0.000	-0.006
35	1.080	0.350	1.078	-0.498	-0.484	-0.014
35	1.050	0.350	1.048	-0.252	-0.242	-0.010
35	1.000	0.355	0.996	-0.005	0.000	-0.005



4.4.2.3 Static accuracy

The test has been done according to the clause 4.7.2.3.3 of the standard.

The dynamic accuracy shall be in accordance with Figure 15 in the standard with a maximum tolerance of $\pm 5\% P_D$ plus a time delay of up to 3 seconds deviating from an ideal first order filter response.

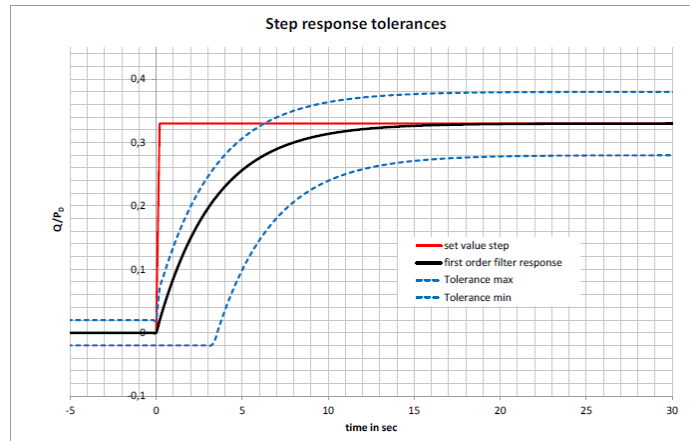
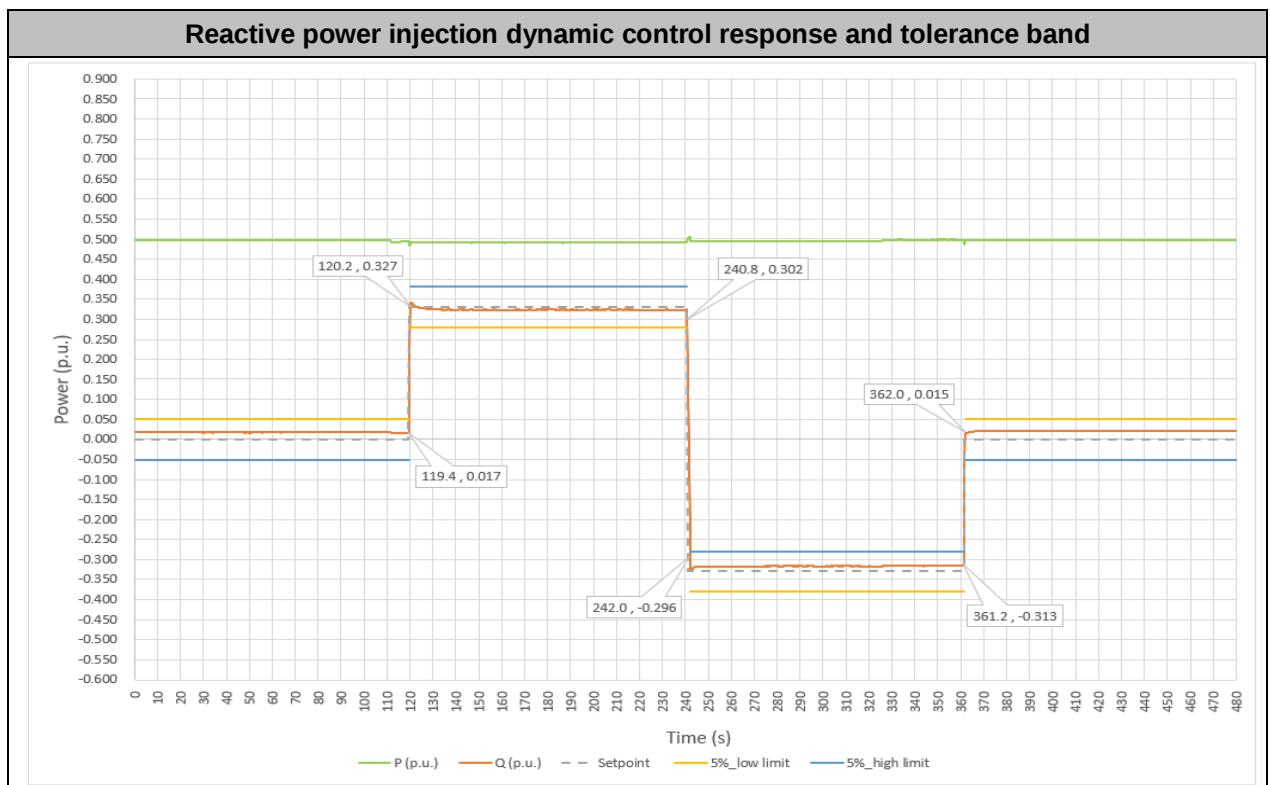


Figure 15 — Example of dynamic control response and tolerance band for a step from $Q=0$ to $Q=33\%P_D$ with $\tau=3.33s$

Test results are offered at the tables below.

%P _n	Steps	Time measured (s)
50	$Q = 0 \rightarrow Q = 33\%P_n$ (Inductive)	$t = 0.8$
	$Q = 33\%P_n$ (Inductive) $\rightarrow Q = 33\%P_n$ (Capacitive)	$t = 1.2$
	$Q = 33\%P_n$ (Capacitive) $\rightarrow Q = 0$	$t = 0.8$

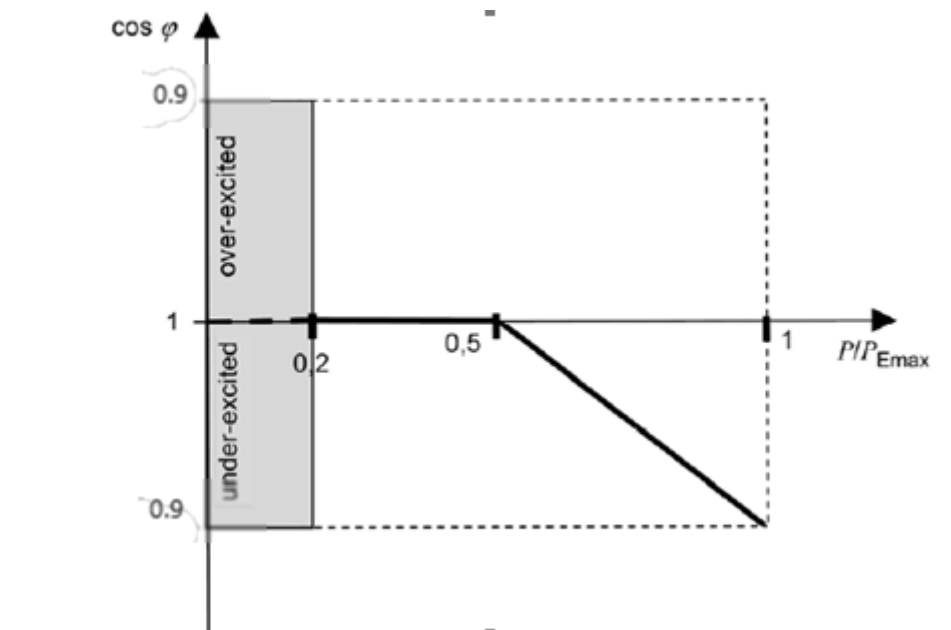


4.4.2.4 Power related control mode

The test has been done according to the clause 4.7.2.3.4 of the standard.

The power related control mode $\cos \phi$ (P) controls the $\cos \phi$ of the output as a function of the active power output.

For power related control modes, a characteristic defined by the manufacturer as follows:

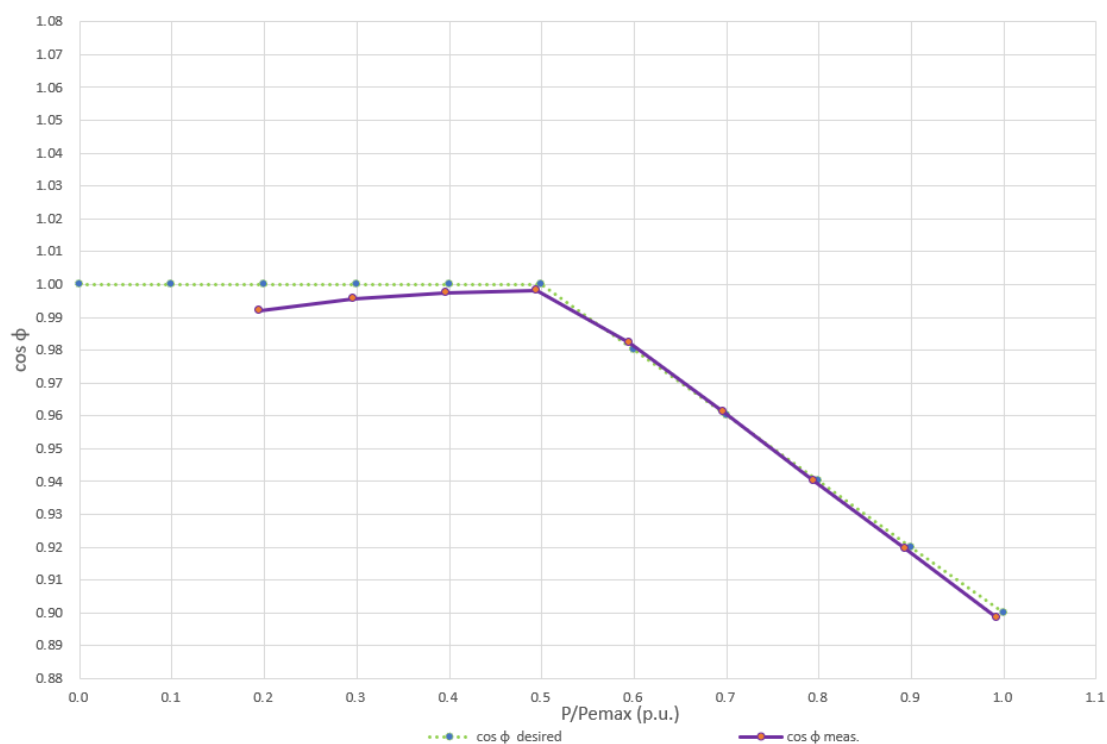
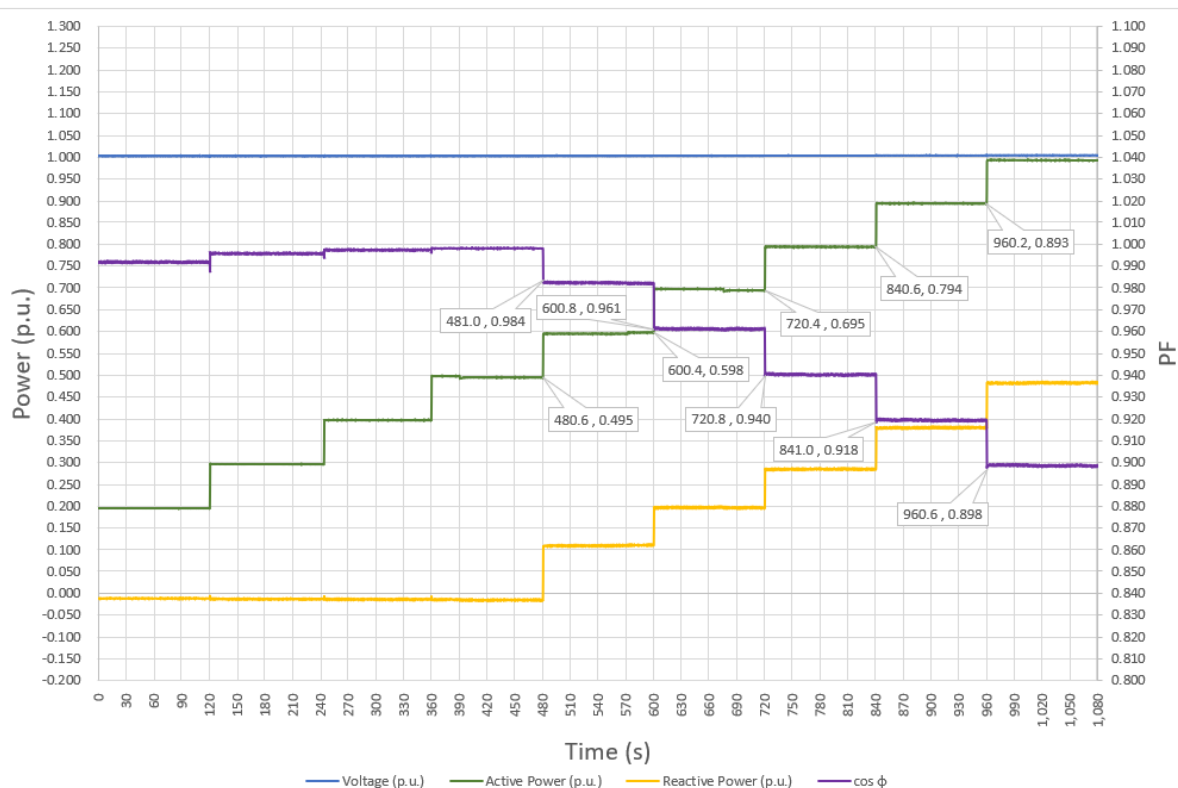


Resulting from a change in active power output a new $\cos \phi$ set point is defined according to the set characteristic. The response to a new $\cos \phi$ set value shall be as fast as technically feasible to allow the change in reactive power to be in synchrony with the change in active power. The new reactive power set value shall be reached at the latest within 10 s after the end value of the active power is reached. The static accuracy of each $\cos \phi$ set point shall be according to 4.7.2.2.

The results are offered in the table below (Note: 10 %P_n has not measured in the following test):

Setting $\cos \phi$ (P) with the standard characteristic curve (20 %P _n to 100 %P _n)						
Active Power setting (%P _{E\max})	Active Power measured (p.u.)	Reactive Power measured (p.u.)	$\cos \phi$ measured	Desired $\cos \phi$	$\Delta \cos \phi$ (<0.01)	Transient period (<10s)
20	0.195	-0.012	0.992	1.000	-0.008	
30	0.296	-0.013	0.996	1.000	-0.004	--
40	0.397	-0.014	0.997	1.000	-0.003	--
50	0.495	-0.015	0.998	1.000	-0.002	--
60	0.595	0.109	0.982	0.980	0.002	0.4
70	0.696	0.197	0.961	0.960	0.001	0.4
80	0.794	0.285	0.940	0.940	0.000	0.4
90	0.894	0.380	0.919	0.920	-0.001	0.4
100	0.993	0.483	0.899	0.900	-0.001	0.6

Setting $\cos \phi(P)$ with standard characteristic curve (20 %P_n to 100 %P_n)

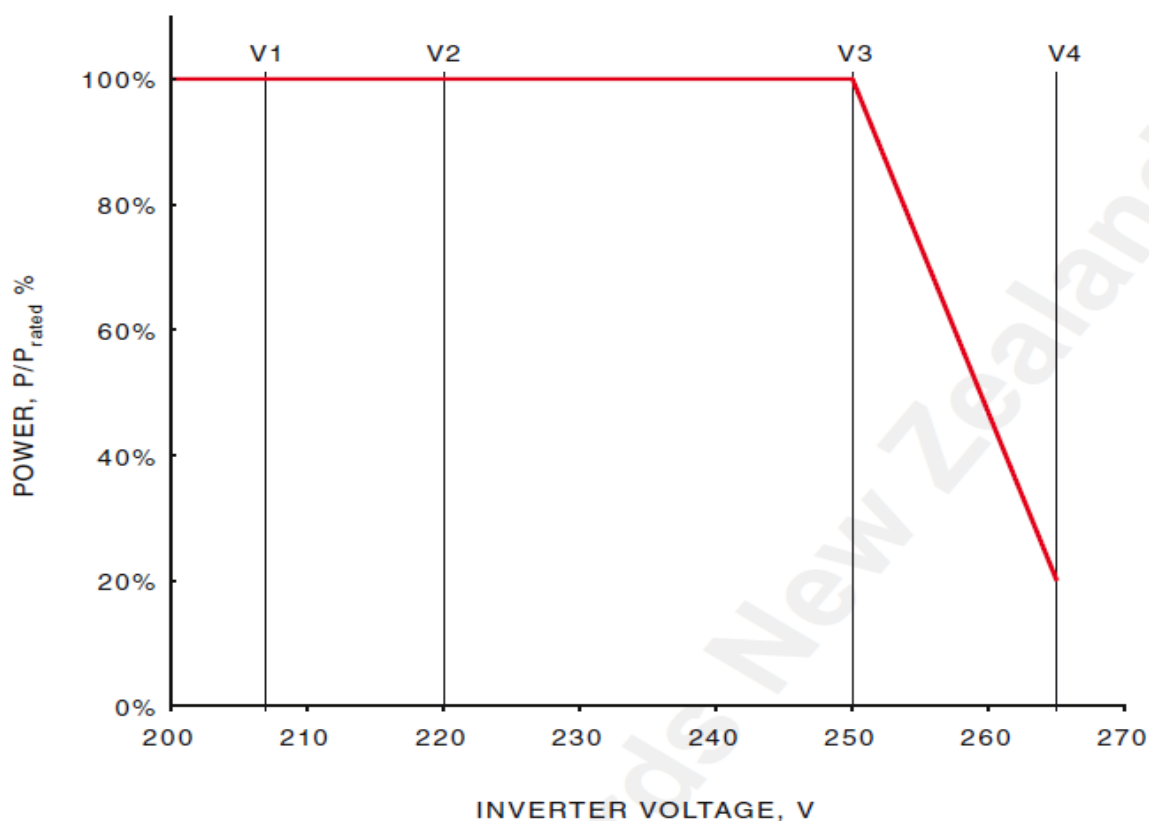


4.4.3. Voltage related active power reduction (Volt-Watt)

The test has been done according to the clause 4.7.3 of the standard.

The final implemented logic can be chosen by the manufacturer. Nevertheless, this logic shall not cause steps or oscillations in the output power. The power reduction caused by such a function may not be faster than an equivalent of a time constant $\tau = 3 \text{ s}$ ($= 33 \text{ \%}/\text{s}$ at a 100 % change).

The following parameters have been set by the manufacturer for this test:



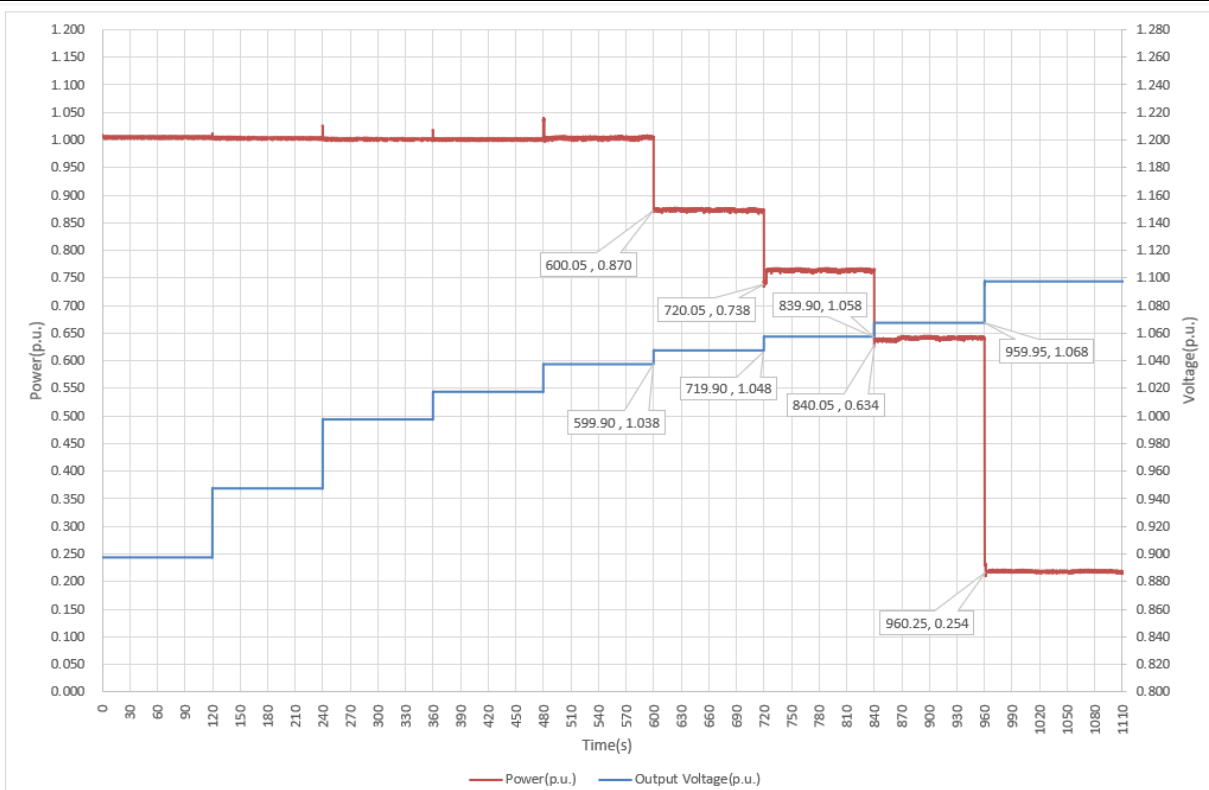
Test 1 and Test 2 setpoint as following:

Reference	Test 1 Set points		Test 2 Set points	
	Volt. (%Un)	Power (%Pn)	Volt. (%Un)	Power (%Pn)
V1	90.0%	100%	90.0%	100%
V2	95.6%	100%	95.6%	100%
V3	104.0%	100%	108.7%	100%
V4	110.0%	20%	115.2%	20%

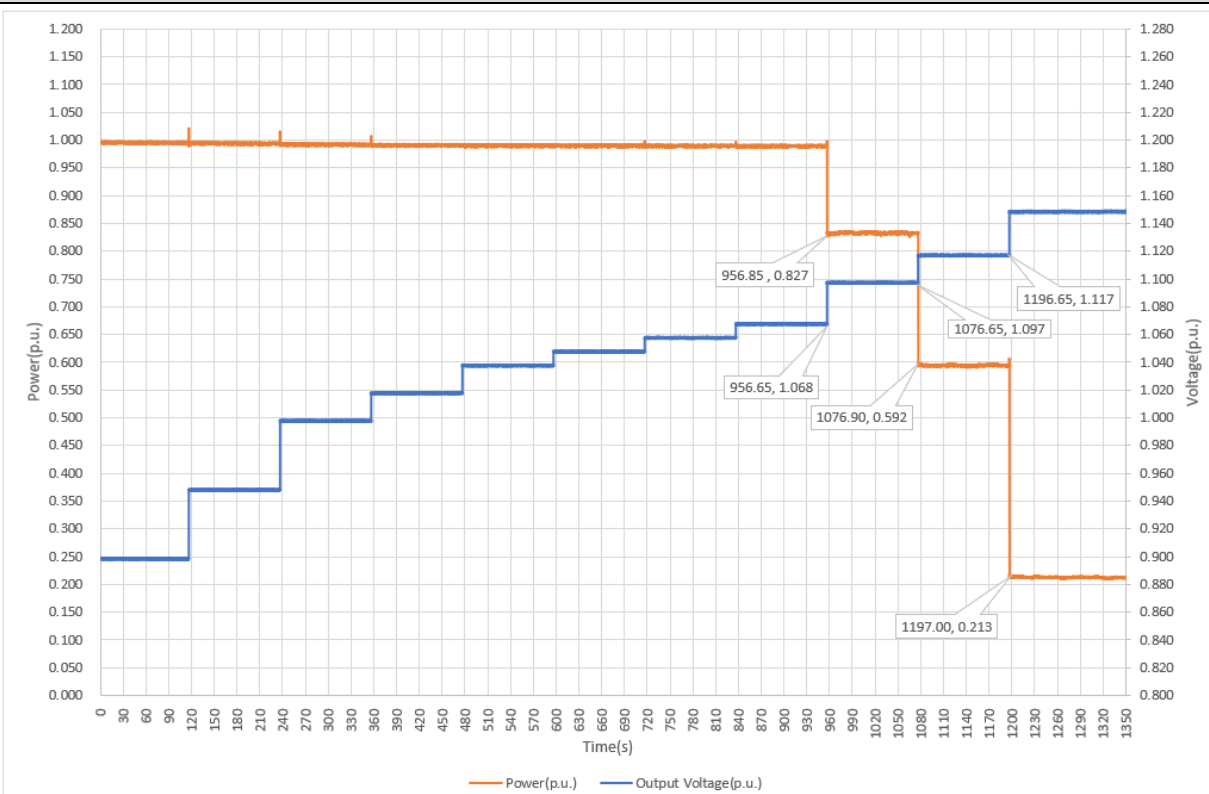
Test results are offered at tables below.

Volt-Watt TEST 1					
V setting (p.u.)	V meas. (p.u.)	P desired (p.u.)	P meas. (p.u.)	P deviation (p.u.)	Response time (s)
0.900	0.898	1.000	1.006	1.000	--
0.950	0.947	1.000	1.004	1.000	--
1.000	0.997	1.000	1.002	1.000	--
1.020	1.018	1.000	1.002	1.000	--
1.040	1.038	1.000	1.004	1.000	--
1.050	1.047	0.867	0.873	0.867	0.15
1.060	1.057	0.771	0.764	0.771	0.15
1.070	1.068	0.657	0.642	0.657	0.15
1.100	1.098	0.200	0.218	0.200	0.15
Volt-Watt TEST 2					
V setting (p.u.)	V meas. (p.u.)	P desired (p.u.)	P meas. (p.u.)	P deviation (p.u.)	Response time (s)
0.900	0.898	1.000	0.996	1.000	--
0.950	0.948	1.000	0.995	1.000	--
1.000	0.998	1.000	0.993	1.000	--
1.020	1.018	1.000	0.992	1.000	--
1.040	1.038	1.000	0.991	1.000	--
1.050	1.048	1.000	0.991	1.000	--
1.060	1.058	1.000	0.990	1.000	--
1.070	1.068	1.000	0.990	1.000	--
1.100	1.097	0.840	0.834	0.840	0.20
1.120	1.117	0.594	0.595	0.594	0.25
1.152	1.148	0.200	0.213	0.200	0.35

Volt-Watt Test 1



Vatt-Watt Test 2



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4.4.4. Short circuit current requirements on generating plants

The following clauses describe the required short circuit current contribution for generating plants taking into account the connection technology of the generating modules.

Generating modules classified as type B modules according to COMMISSION REGULATION 2016/631 shall comply with the requirements of 4.7.4.2 and 4.7.4.3. Generating modules classified as type A according to COMMISSION REGULATION 2016/631 should comply with these requirements. The actual behaviour of type A modules shall be specified in the connection agreement.

4.4.4.1 Generating plant with non-synchronous generating technology

4.4.4.1.1 Voltage support during faults and voltage steps

The requirements are stated in clause 4.7.4.2.1 of the standard.

The EUT is classified as Type B. This is no voltage support during faults and voltage steps.

4.4.4.1.2 Zero current mode for converter connected generating technology

The requirements are stated in clause 4.7.4.2.2 of the standard.

The EUT is classified as Type B. Refer to Section 4.2.2 and 4.2.3 of this report. During UVRT and OVRT, the EUT is always work at zero current mode.

4.4.4.1.3 Induction generator based units

The requirements are stated in clause 4.7.4.2.3 of the standard.

The EUT is no voltage support during faults and voltage steps. This clause is not applicable.

4.4.4.2 Generating plant with synchronous generating technology - Synchronous generator based units

The requirements are stated in clause 4.7.4.3 of the standard.

The EUT is with non-synchronous generating technology. This clause is not applicable.

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4.5. EMC AND POWER QUALITY

As required in clause 4.8 of the standard, all electric and electronic equipment to be installed under the scope of this standard shall be in compliance with relative standards for Electromagnetic Compatibility.

The compliances with these requirements are stated in the following test reports:

Test Report no. ES170725041E-1, issued by EMTEK (SHENZHEN) CO., LTD on March 10th, 2020.

Note: Aside of EMC evidences of compliances, the harmonic and flicker content has been measured just to provide further information of the tested unit, and the results are stated in the following items 4.5.1 and 4.5.2 of the report.

4.5.1. Harmonic emissions

The test has been done according to the clause 4.8 of the standard EN 61000-3-12

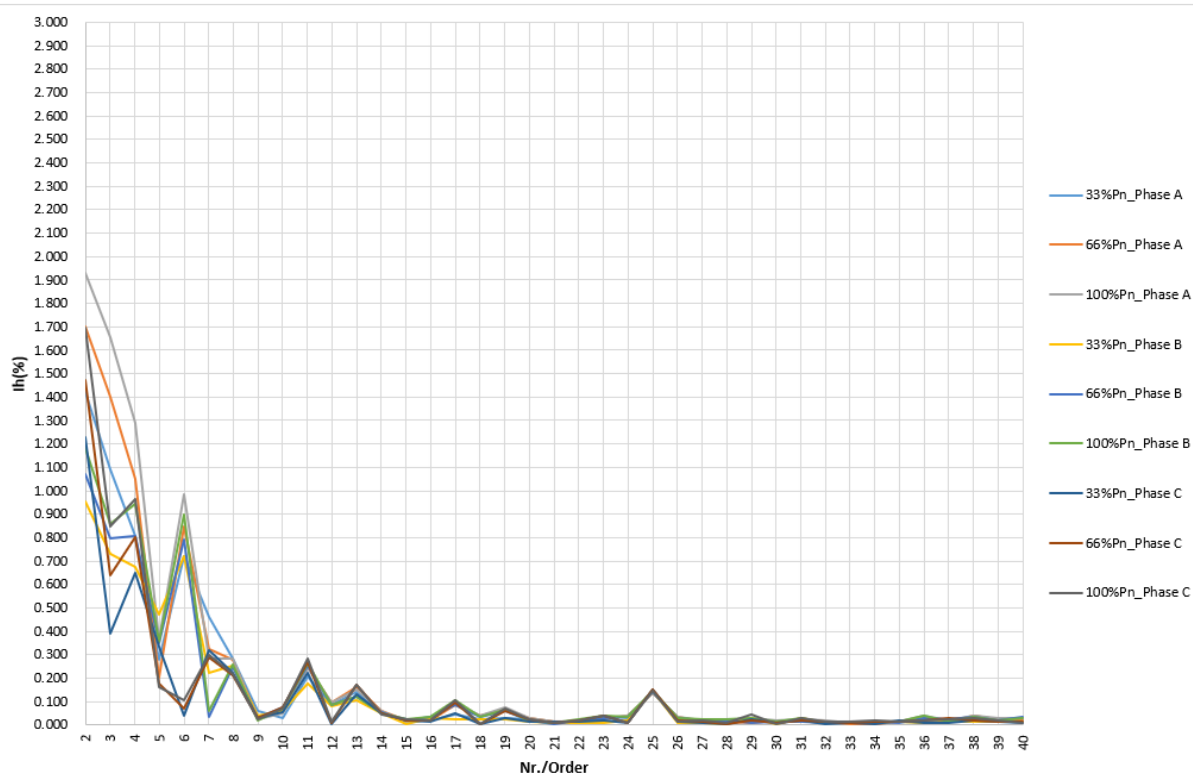
Below are the measured values of current harmonics.

Phase A				
P _{bin} (%)	33	66	100	LIMIT (%)
Nr. / Order	I _h (%)	I _h (%)	I _h (%)	
2	1.424	1.702	1.928	8.000
3	1.091	1.401	1.657	--
4	0.805	1.053	1.290	4.000
5	0.281	0.201	0.367	10.700
6	0.722	0.846	0.987	2.700
7	0.460	0.326	0.283	7.200
8	0.285	0.280	0.283	2.000
9	0.062	0.035	0.026	--
10	0.028	0.060	0.052	1.600
11	0.210	0.249	0.242	3.100
12	0.085	0.097	0.096	1.300
13	0.140	0.163	0.151	2.000
14	0.052	0.059	0.059	--
15	0.024	0.019	0.012	--
16	0.018	0.018	0.023	--
17	0.047	0.088	0.095	--
18	0.025	0.040	0.040	--
19	0.030	0.065	0.075	--
20	0.022	0.028	0.027	--
21	0.008	0.012	0.016	--
22	0.009	0.014	0.014	--
23	0.013	0.027	0.028	--
24	0.023	0.037	0.041	--
25	0.144	0.135	0.136	--
26	0.019	0.037	0.032	--
27	0.011	0.017	0.018	--
28	0.012	0.015	0.017	--
29	0.020	0.010	0.029	--
30	0.007	0.022	0.019	--
31	0.022	0.014	0.026	--
32	0.009	0.012	0.019	--
33	0.011	0.006	0.009	--
34	0.006	0.004	0.010	--
35	0.014	0.016	0.013	--
36	0.011	0.036	0.041	--
37	0.017	0.013	0.020	--
38	0.014	0.039	0.041	--
39	0.021	0.024	0.029	--
40	0.021	0.025	0.028	--
THD (%)	2.206	2.656	3.089	13.000
PWHD (%)	0.883	1.036	1.105	22.000

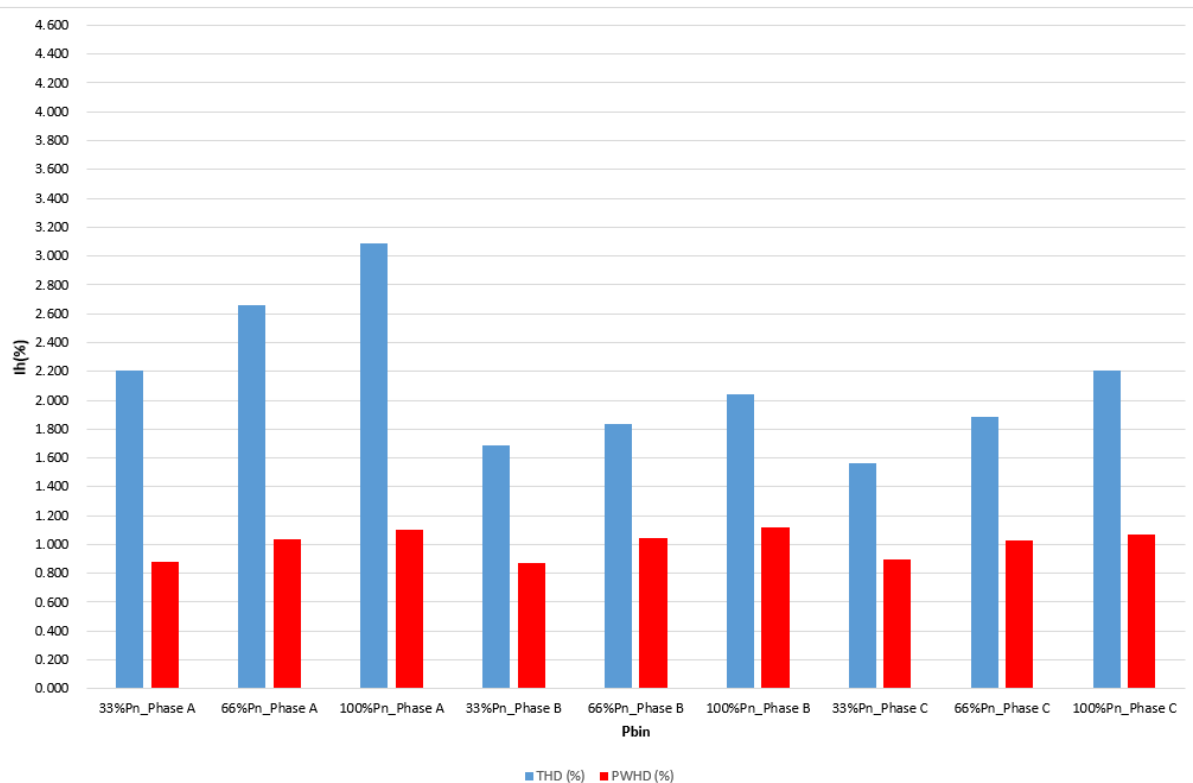
Phase B				
P _{bin} (%)	33	66	100	LIMIT (%)
Nr. / Order	I _h (%)	I _h (%)	I _h (%)	
2	0.953	1.072	1.181	8.000
3	0.729	0.798	0.856	--
4	0.675	0.805	0.945	4.000
5	0.473	0.348	0.357	10.700
6	0.722	0.791	0.897	2.700
7	0.221	0.037	0.060	7.200
8	0.252	0.246	0.259	2.000
9	0.040	0.027	0.022	--
10	0.060	0.075	0.073	1.600
11	0.178	0.253	0.263	3.100
12	0.082	0.087	0.083	1.300
13	0.104	0.122	0.119	2.000
14	0.052	0.055	0.049	--
15	0.004	0.018	0.024	--
16	0.028	0.035	0.033	--
17	0.026	0.088	0.105	--
18	0.027	0.036	0.034	--
19	0.025	0.058	0.066	--
20	0.016	0.021	0.021	--
21	0.008	0.005	0.011	--
22	0.012	0.021	0.023	--
23	0.011	0.032	0.037	--
24	0.019	0.033	0.033	--
25	0.147	0.143	0.145	--
26	0.009	0.028	0.029	--
27	0.010	0.022	0.025	--
28	0.006	0.017	0.024	--
29	0.029	0.011	0.029	--
30	0.004	0.016	0.015	--
31	0.030	0.015	0.032	--
32	0.012	0.008	0.007	--
33	0.009	0.010	0.011	--
34	0.007	0.012	0.013	--
35	0.013	0.011	0.013	--
36	0.007	0.030	0.038	--
37	0.015	0.025	0.013	--
38	0.015	0.033	0.036	--
39	0.015	0.020	0.021	--
40	0.016	0.033	0.028	--
THD (%)	1.685	1.839	2.043	13.000
PWHD (%)	0.873	1.048	1.115	22.000

Phase C				
P _{bin} (%)	33	66	100	LIMIT (%)
Nr. / Order	I _h (%)	I _h (%)	I _h (%)	
2	1.227	1.472	1.695	8.000
3	0.391	0.640	0.849	--
4	0.648	0.803	0.964	4.000
5	0.335	0.175	0.159	10.700
6	0.042	0.072	0.104	2.700
7	0.322	0.291	0.297	7.200
8	0.221	0.213	0.217	2.000
9	0.029	0.029	0.025	--
10	0.054	0.075	0.071	1.600
11	0.221	0.270	0.282	3.100
12	0.005	0.011	0.014	1.300
13	0.129	0.172	0.173	2.000
14	0.048	0.049	0.046	--
15	0.021	0.019	0.023	--
16	0.014	0.022	0.021	--
17	0.052	0.097	0.107	--
18	0.003	0.005	0.006	--
19	0.030	0.060	0.069	--
20	0.017	0.025	0.025	--
21	0.014	0.011	0.010	--
22	0.014	0.021	0.020	--
23	0.018	0.038	0.040	--
24	0.008	0.011	0.010	--
25	0.153	0.151	0.145	--
26	0.012	0.019	0.015	--
27	0.007	0.013	0.019	--
28	0.006	0.007	0.010	--
29	0.026	0.020	0.045	--
30	0.009	0.007	0.006	--
31	0.019	0.018	0.028	--
32	0.003	0.017	0.013	--
33	0.008	0.009	0.013	--
34	0.004	0.016	0.021	--
35	0.021	0.017	0.015	--
36	0.010	0.021	0.018	--
37	0.009	0.028	0.027	--
38	0.018	0.020	0.028	--
39	0.013	0.015	0.019	--
40	0.008	0.016	0.011	--
THD (%)	1.565	1.882	2.205	13.000
PWHD (%)	0.898	1.025	1.066	22.000

Current Harmonics



THD and PWhD



4.5.2. Flicker and voltage fluctuations

The test has been done according to the clause 4.8 of the standard.

The measurements of voltage fluctuations have been measured at 33%, 66% and 100% of the nominal power value of the inverter according to the standard IEC 61000-3-11:2017.

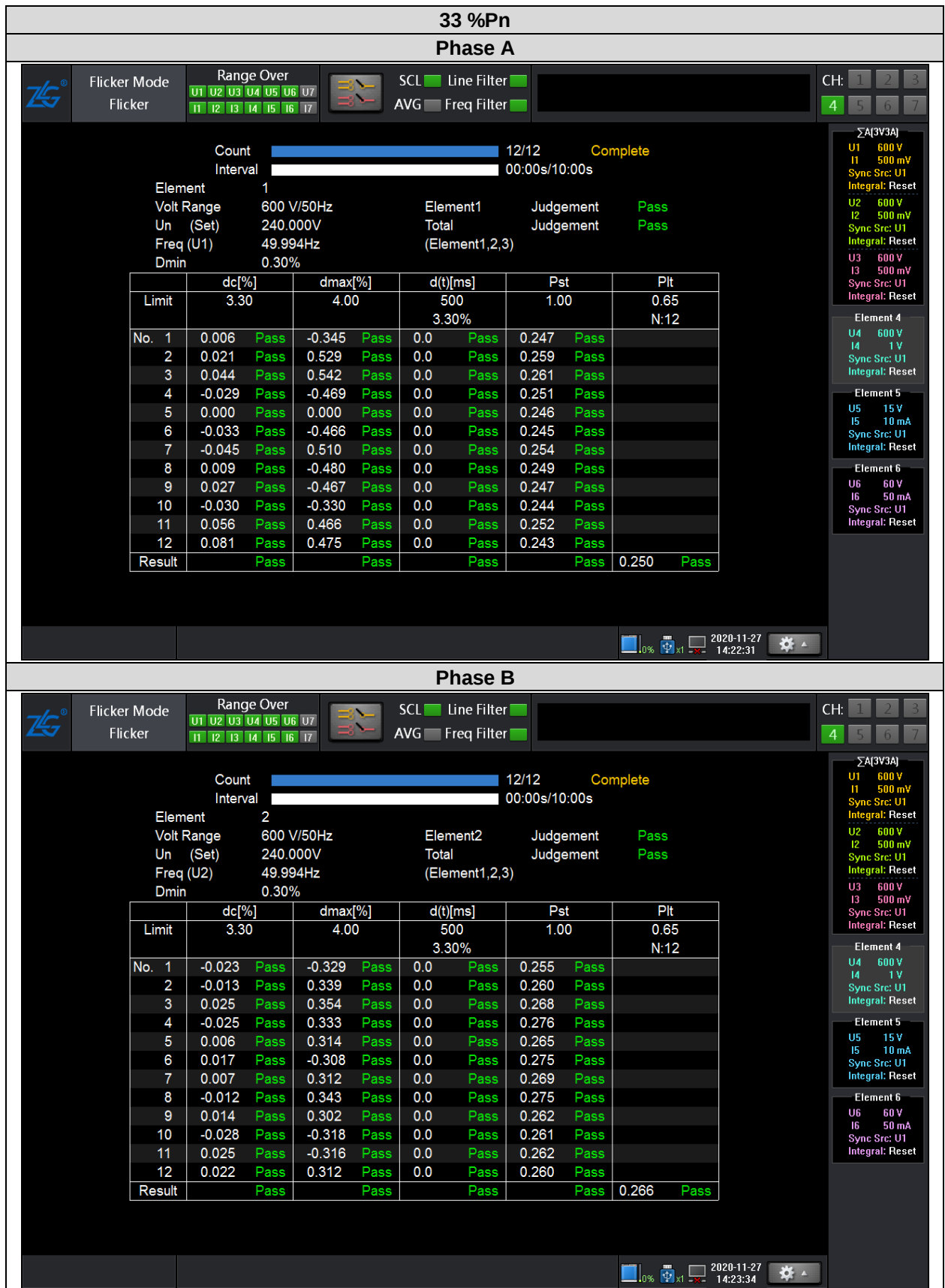
The flicker test result as following:

33 %Pn				
Pbin (%)	Limit	Phase A	Phase B	Phase C
PST	≤ 1.0	0.261	0.275	0.264
PLT	≤ 0.65	0.250	0.266	0.235
dc	$\leq 3.30 \%$	0.081 %	0.028 %	0.051%
dmax	4 %	0.542 %	0.354 %	0.472 %

66 %Pn				
Pbin (%)	Limit	Phase A	Phase B	Phase C
PST	≤ 1.0	0.461	0.460	0.226
PLT	≤ 0.65	0.284	0.286	0.204
dc	$\leq 3.30 \%$	0.090 %	0.054 %	0.067 %
dmax	4 %	0.592 %	0.360 %	0.486 %

100%Pn				
Pbin (%)	Limit	Phase A	Phase B	Phase C
PST	≤ 1.0	0.249	0.261	0.231
PLT	≤ 0.65	0.241	0.248	0.219
dc	$\leq 3.30 \%$	0.040 %	0.045 %	0.047 %
dmax	4 %	0.449 %	0.347 %	0.412 %

As it can be seen in the next screenshots, this test has 12 steps. The values took of Pst, Plt, dc and dmax are the most unfavorable of the 12 steps.



Phase C


 Flicker Mode
 Flicker

Range Over
 U1 U2 U3 U4 U5 U6 U7
 I1 I2 I3 I4 I5 I6 I7

SCL ☒ Line Filter ☒
 AVG ☒ Freq Filter ☒

CH: 1 2 3
4 5 6 7

Count 12/12 Complete
 Interval 00:00s/10:00s

Element 3
 Volt Range 600 V/50Hz
 Un (Set) 240.000V
 Freq (U3) 49.994Hz
 Dmin 0.30%

Element3 Judgement Pass
 Total Judgement Pass
 (Element1,2,3)

	dc[%]	dmax[%]	d(t)[ms]	Pst	Plt
Limit	3.30	4.00	500 3.30%	1.00	0.65 N:12
No. 1	0.000 Pass	-0.439 Pass	0.0 Pass	0.216 Pass	
2	-0.004 Pass	-0.439 Pass	0.0 Pass	0.264 Pass	
3	-0.002 Pass	-0.422 Pass	0.0 Pass	0.235 Pass	
4	-0.006 Pass	-0.411 Pass	0.0 Pass	0.216 Pass	
5	0.035 Pass	-0.414 Pass	0.0 Pass	0.217 Pass	
6	0.044 Pass	-0.472 Pass	0.0 Pass	0.242 Pass	
7	0.030 Pass	-0.414 Pass	0.0 Pass	0.246 Pass	
8	0.000 Pass	0.386 Pass	0.0 Pass	0.248 Pass	
9	0.051 Pass	-0.416 Pass	0.0 Pass	0.238 Pass	
10	0.000 Pass	-0.390 Pass	0.0 Pass	0.228 Pass	
11	0.045 Pass	0.422 Pass	0.0 Pass	0.242 Pass	
12	0.047 Pass	0.390 Pass	0.0 Pass	0.222 Pass	
Result	Pass	Pass	Pass	Pass	0.235 Pass

ΣA[3V3A]
 U1 600 V
 I1 500 mA
 Sync Src: U1
 Integral: Reset
 U2 600 V
 I2 500 mA
 Sync Src: U1
 Integral: Reset
 U3 600 V
 I3 500 mA
 Sync Src: U1
 Integral: Reset
 Element 4
 U4 600 V
 I4 1 V
 Sync Src: U1
 Integral: Reset
 Element 5
 U5 15 V
 I5 10 mA
 Sync Src: U1
 Integral: Reset
 Element 6
 U6 60 V
 I6 50 mA
 Sync Src: U1
 Integral: Reset

10% x1 2020-11-27 14:24:41

66 %Pn

Phase A


 Flicker Mode
 Flicker

Range Over
 U1 U2 U3 U4 U5 U6 U7
 I1 I2 I3 I4 I5 I6 I7

SCL ☒ Line Filter ☒
 AVG ☒ Freq Filter ☒

CH: 1 2 3
4 5 6 7

Count 12/12 Complete
 Interval 00:00s/10:00s

Element 1
 Volt Range 600 V/50Hz
 Un (Set) 240.000V
 Freq (U1) 49.994Hz
 Dmin 0.30%

Element1 Judgement Pass
 Total Judgement Pass
 (Element1,2,3)

	dc[%]	dmax[%]	d(t)[ms]	Pst	Plt
Limit	3.30	4.00	500 3.30%	1.00	0.65 N:12
No. 1	-0.069 Pass	-0.523 Pass	0.0 Pass	0.461 Pass	
2	-0.079 Pass	-0.536 Pass	0.0 Pass	0.233 Pass	
3	-0.076 Pass	0.592 Pass	0.0 Pass	0.239 Pass	
4	0.000 Pass	-0.522 Pass	0.0 Pass	0.228 Pass	
5	-0.090 Pass	0.535 Pass	0.0 Pass	0.269 Pass	
6	0.017 Pass	-0.497 Pass	0.0 Pass	0.254 Pass	
7	0.000 Pass	0.492 Pass	0.0 Pass	0.276 Pass	
8	-0.038 Pass	0.521 Pass	0.0 Pass	0.249 Pass	
9	0.014 Pass	0.480 Pass	0.0 Pass	0.278 Pass	
10	-0.075 Pass	0.488 Pass	0.0 Pass	0.248 Pass	
11	-0.052 Pass	-0.506 Pass	0.0 Pass	0.249 Pass	
12	0.000 Pass	0.494 Pass	0.0 Pass	0.247 Pass	
Result	Pass	Pass	Pass	Pass	0.284 Pass

ΣA[3V3A]
 U1 600 V
 I1 500 mA
 Sync Src: U1
 Integral: Reset
 U2 600 V
 I2 500 mA
 Sync Src: U1
 Integral: Reset
 U3 600 V
 I3 500 mA
 Sync Src: U1
 Integral: Reset
 Element 4
 U4 600 V
 I4 1 V
 Sync Src: U1
 Integral: Reset
 Element 5
 U5 15 V
 I5 10 mA
 Sync Src: U1
 Integral: Reset
 Element 6
 U6 60 V
 I6 50 mA
 Sync Src: U1
 Integral: Reset

10% x1 2020-11-28 16:02:04

Phase B

 Flicker Mode
Flicker

Range Over
U1 U2 U3 U4 U5 U6 U7
I1 I2 I3 I4 I5 I6 I7

SCL ☒ Line Filter ☒
AVG ☒ Freq Filter ☒

CH: 1 2 3
4 5 6 7

Count 12/12 Complete
Interval 00:00s/10:00s

Element 2
Volt Range 600 V/50Hz
Un (Set) 240.000V
Freq (U2) 49.994Hz
Dmin 0.30%

Element2 Judgement Pass
Total Judgement Pass
(Element1,2,3)

	dc[%]	dmax[%]	d(t)[ms]	Pst	Plt
Limit	3.30	4.00	500 3.30%	1.00	0.65 N:12
No. 1	-0.034 Pass	-0.344 Pass	0.0 Pass	0.460 Pass	
2	0.011 Pass	-0.312 Pass	0.0 Pass	0.221 Pass	
3	0.028 Pass	-0.323 Pass	0.0 Pass	0.214 Pass	
4	-0.032 Pass	-0.360 Pass	0.0 Pass	0.240 Pass	
5	-0.043 Pass	-0.350 Pass	0.0 Pass	0.263 Pass	
6	-0.014 Pass	-0.321 Pass	0.0 Pass	0.266 Pass	
7	0.012 Pass	-0.330 Pass	0.0 Pass	0.282 Pass	
8	-0.054 Pass	-0.327 Pass	0.0 Pass	0.258 Pass	
9	0.021 Pass	-0.338 Pass	0.0 Pass	0.279 Pass	
10	-0.040 Pass	-0.340 Pass	0.0 Pass	0.265 Pass	
11	-0.017 Pass	-0.347 Pass	0.0 Pass	0.250 Pass	
12	-0.012 Pass	-0.317 Pass	0.0 Pass	0.257 Pass	
Result	Pass	Pass	Pass	Pass	0.286 Pass

ΣA(3V3A)
U1 600 V
I1 500 mA
Sync Src: U1
Integral: Reset
U2 600 V
I2 500 mA
Sync Src: U1
Integral: Reset
U3 600 V
I3 500 mA
Sync Src: U1
Integral: Reset
Element 4
U4 600 V
I4 1 V
Sync Src: U1
Integral: Reset
Element 5
U5 15 V
I5 10 mA
Sync Src: U1
Integral: Reset
Element 6
U6 60 V
I6 50 mA
Sync Src: U1
Integral: Reset

10% x1 2020-11-28 18:03:00

Phase C

 Flicker Mode
Flicker

Range Over
U1 U2 U3 U4 U5 U6 U7
I1 I2 I3 I4 I5 I6 I7

SCL ☒ Line Filter ☒
AVG ☒ Freq Filter ☒

CH: 1 2 3
4 5 6 7

Count 12/12 Complete
Interval 00:00s/10:00s

Element 3
Volt Range 600 V/50Hz
Un (Set) 240.000V
Freq (U3) 49.994Hz
Dmin 0.30%

Element3 Judgement Pass
Total Judgement Pass
(Element1,2,3)

	dc[%]	dmax[%]	d(t)[ms]	Pst	Plt
Limit	3.30	4.00	500 3.30%	1.00	0.65 N:12
No. 1	-0.034 Pass	-0.464 Pass	0.0 Pass	0.189 Pass	
2	-0.012 Pass	0.449 Pass	0.0 Pass	0.178 Pass	
3	-0.021 Pass	0.475 Pass	0.0 Pass	0.175 Pass	
4	-0.018 Pass	0.427 Pass	0.0 Pass	0.186 Pass	
5	0.067 Pass	-0.481 Pass	0.0 Pass	0.205 Pass	
6	-0.039 Pass	-0.469 Pass	0.0 Pass	0.211 Pass	
7	-0.056 Pass	-0.465 Pass	0.0 Pass	0.216 Pass	
8	-0.029 Pass	0.486 Pass	0.0 Pass	0.210 Pass	
9	0.020 Pass	0.465 Pass	0.0 Pass	0.219 Pass	
10	-0.032 Pass	0.438 Pass	0.0 Pass	0.208 Pass	
11	-0.025 Pass	-0.419 Pass	0.0 Pass	0.209 Pass	
12	-0.024 Pass	0.454 Pass	0.0 Pass	0.226 Pass	
Result	Pass	Pass	Pass	Pass	0.204 Pass

ΣA(3V3A)
U1 600 V
I1 500 mA
Sync Src: U1
Integral: Reset
U2 600 V
I2 500 mA
Sync Src: U1
Integral: Reset
U3 600 V
I3 500 mA
Sync Src: U1
Integral: Reset
Element 4
U4 600 V
I4 1 V
Sync Src: U1
Integral: Reset
Element 5
U5 15 V
I5 10 mA
Sync Src: U1
Integral: Reset
Element 6
U6 60 V
I6 50 mA
Sync Src: U1
Integral: Reset

10% x1 2020-11-28 18:04:41

100 %Pn

Phase A

 Flicker Mode: Flicker Range Over: U1 U2 U3 U4 U5 U6 U7 I1 I2 I3 I4 I5 I6 I7 SCL Line Filter AVG Freq Filter CH: 1 2 3 4 5 6 7

Count: 12/12 Complete
Interval: 00:00s/10:00s

Element 1
Volt Range: 600 V/50Hz
Un (Set): 240.000V
Freq (U1): 49.994Hz
Dmin: 0.30%

Element1 Judgement: Pass
Total Judgement: Pass
(Element1,2,3)

	dc[%]	dmax[%]	d(t)[ms]	Pst	Plt
Limit	3.30	4.00	500 3.30%	1.00	0.65 N:12
No. 1	0.000 Pass	0.000 Pass	0.0 Pass	0.247 Pass	
2	0.000 Pass	0.000 Pass	0.0 Pass	0.236 Pass	
3	0.000 Pass	0.000 Pass	0.0 Pass	0.237 Pass	
4	-0.032 Pass	-0.306 Pass	0.0 Pass	0.235 Pass	
5	0.000 Pass	0.000 Pass	0.0 Pass	0.219 Pass	
6	0.000 Pass	0.000 Pass	0.0 Pass	0.246 Pass	
7	0.000 Pass	0.000 Pass	0.0 Pass	0.244 Pass	
8	0.000 Pass	0.000 Pass	0.0 Pass	0.239 Pass	
9	0.000 Pass	0.000 Pass	0.0 Pass	0.249 Pass	
10	0.040 Pass	-0.449 Pass	0.0 Pass	0.241 Pass	
11	0.000 Pass	0.000 Pass	0.0 Pass	0.249 Pass	
12	0.000 Pass	0.000 Pass	0.0 Pass	0.244 Pass	
Result	Pass	Pass	Pass	Pass	0.241 Pass

ΣA(3V3A)
U1 600 V
I1 500 mA
Sync Src: U1
Integral: Reset
U2 600 V
I2 500 mA
Sync Src: U1
Integral: Reset
U3 600 V
I3 500 mA
Sync Src: U1
Integral: Reset
Element 4
U4 600 V
I4 1 V
Sync Src: U1
Integral: Reset
Element 5
U5 15 V
I5 10 mA
Sync Src: U1
Integral: Reset
Element 6
U6 60 V
I6 50 mA
Sync Src: U1
Integral: Reset

2020-11-29 11:23:50

Phase B

 Flicker Mode: Flicker Range Over: U1 U2 U3 U4 U5 U6 U7 I1 I2 I3 I4 I5 I6 I7 SCL Line Filter AVG Freq Filter CH: 1 2 3 4 5 6 7

Count: 12/12 Complete
Interval: 00:00s/10:00s

Element 2
Volt Range: 600 V/50Hz
Un (Set): 240.000V
Freq (U2): 49.994Hz
Dmin: 0.30%

Element2 Judgement: Pass
Total Judgement: Pass
(Element1,2,3)

	dc[%]	dmax[%]	d(t)[ms]	Pst	Plt
Limit	3.30	4.00	500 3.30%	1.00	0.65 N:12
No. 1	-0.024 Pass	-0.312 Pass	0.0 Pass	0.250 Pass	
2	0.045 Pass	0.347 Pass	0.0 Pass	0.242 Pass	
3	0.015 Pass	0.306 Pass	0.0 Pass	0.233 Pass	
4	-0.013 Pass	-0.302 Pass	0.0 Pass	0.250 Pass	
5	-0.021 Pass	-0.330 Pass	0.0 Pass	0.250 Pass	
6	0.022 Pass	0.314 Pass	0.0 Pass	0.252 Pass	
7	0.023 Pass	0.342 Pass	0.0 Pass	0.233 Pass	
8	-0.034 Pass	-0.318 Pass	0.0 Pass	0.243 Pass	
9	0.006 Pass	0.333 Pass	0.0 Pass	0.250 Pass	
10	0.000 Pass	0.000 Pass	0.0 Pass	0.251 Pass	
11	-0.018 Pass	-0.317 Pass	0.0 Pass	0.254 Pass	
12	0.028 Pass	0.333 Pass	0.0 Pass	0.261 Pass	
Result	Pass	Pass	Pass	Pass	0.248 Pass

ΣA(3V3A)
U1 600 V
I1 500 mA
Sync Src: U1
Integral: Reset
U2 600 V
I2 500 mA
Sync Src: U1
Integral: Reset
U3 600 V
I3 500 mA
Sync Src: U1
Integral: Reset
Element 4
U4 600 V
I4 1 V
Sync Src: U1
Integral: Reset
Element 5
U5 15 V
I5 10 mA
Sync Src: U1
Integral: Reset
Element 6
U6 60 V
I6 50 mA
Sync Src: U1
Integral: Reset

2020-11-29 11:25:20



4.6. INTERFACE PROTECTION

4.6.1. Requirements on voltage and frequency protection

The test has been done according to the clause 4.9.3 of the standard. The minimum required accuracy for protection is:

- For frequency measurement ± 0.05 Hz;
- For voltage measurement ± 1 %Un.
- The reset time shall be ≤ 50 ms.
- The interface protection relay shall not conduct continuous starting and disengaging operations of the interface protection relay. Therefore, a reasonable reset ratio shall be implemented which shall not be zero but be below 2 % of nominal value for voltage and below 0.2 Hz for frequency.

4.6.1.1 Undervoltage protection

Undervoltage protection may be implemented with two completely independent protection thresholds, each one able to be activated or not. The standard adjustment ranges are as follows.

Undervoltage threshold stage 1 [27 <]:

- Threshold (0.2 – 1) Un adjustable by steps of 0.01 Un
- Operate time (0.1 – 100) s adjustable in steps of 0.1 s

Undervoltage threshold stage 2 [27 <<]:

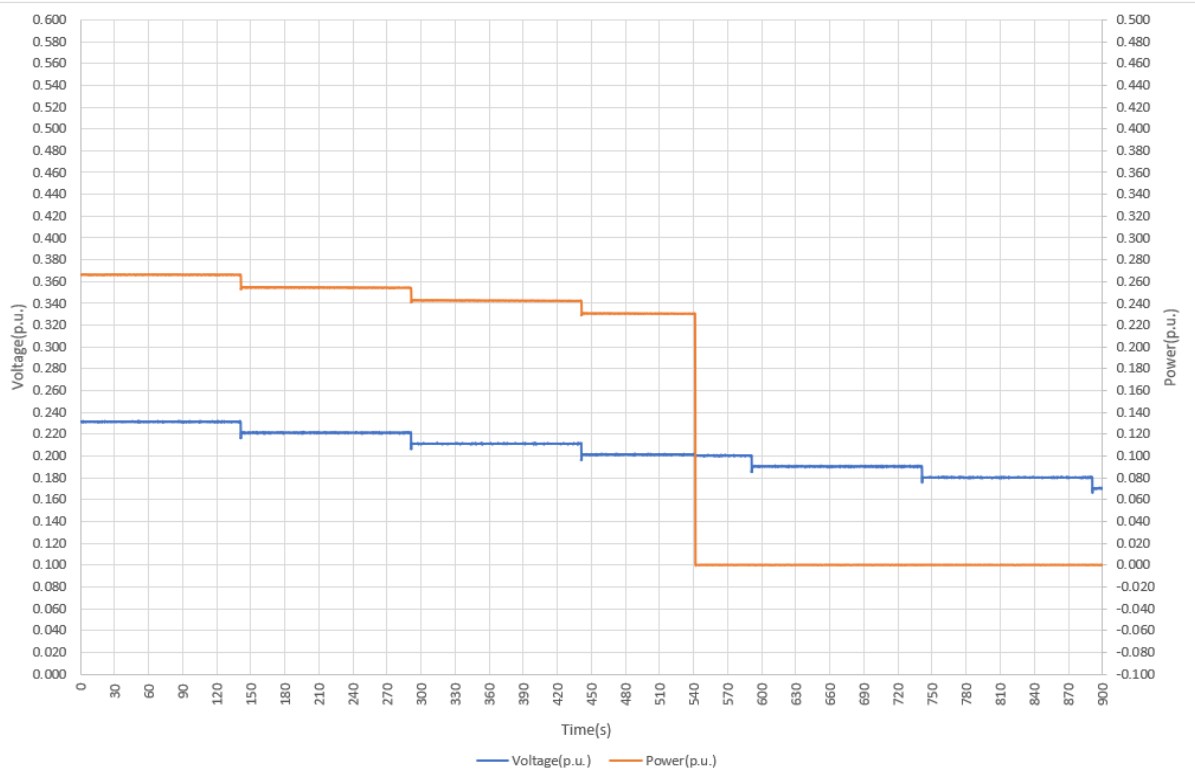
- Threshold (0.2 – 1) Un adjustable by steps of 0.01 Un
- Operate time (0.1 – 5) s adjustable in steps of 0.05 s

The undervoltage threshold stage 2 is not applicable for micro-generating plants.

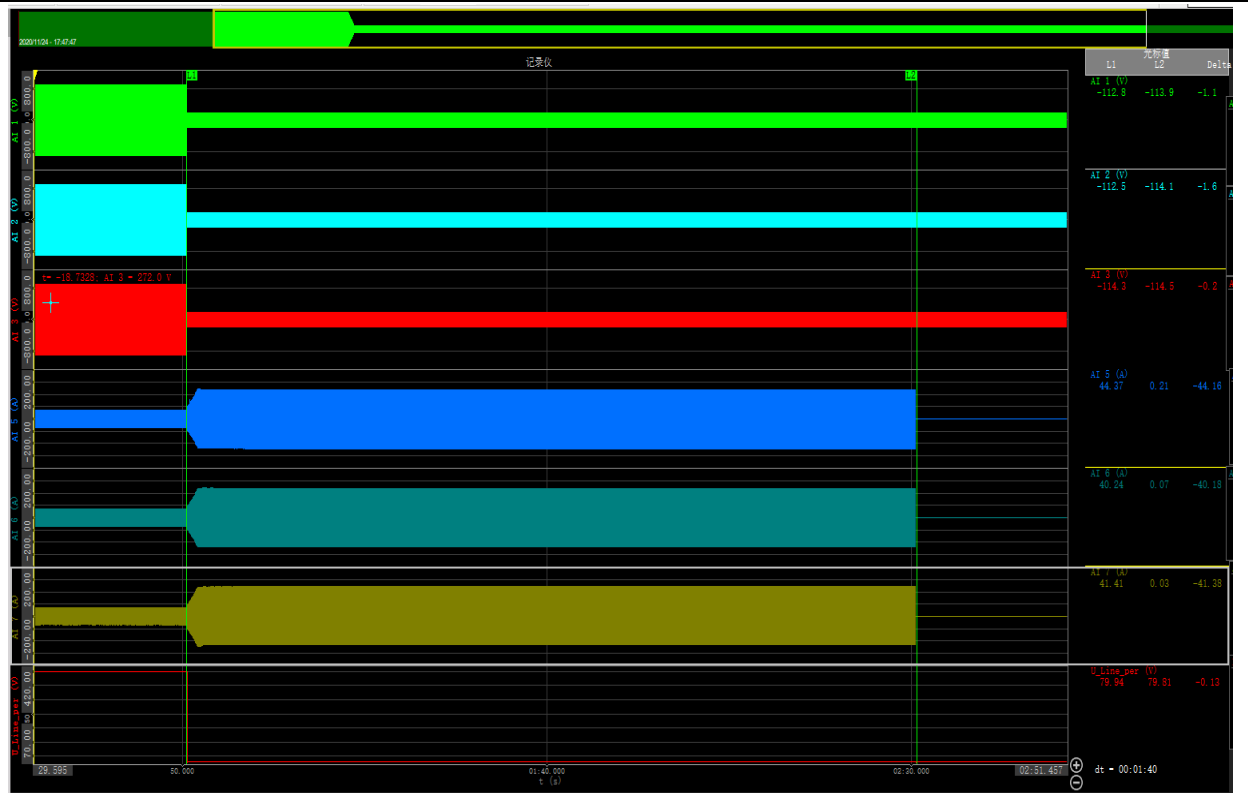
The following definitions apply to the test to verify the clause:

Undervoltage	Test No.	Voltage setting (p.u.)	Voltage meas. (p.u.)	Voltage deviation (p.u.)	Trip time setting (s)	Trip time meas. (s)	Trip time deviation (s)
Stage 1 [27 <]	1	0.200	0.201	0.001	100.000	100.000	0.000
	2	1.000	0.998	-0.002	0.100	0.128	0.028
Stage 2 [27 <<]	3	0.200	0.201	0.001	5.000	5.016	0.016
	4	1.000	0.997	-0.003	0.100	0.127	0.027

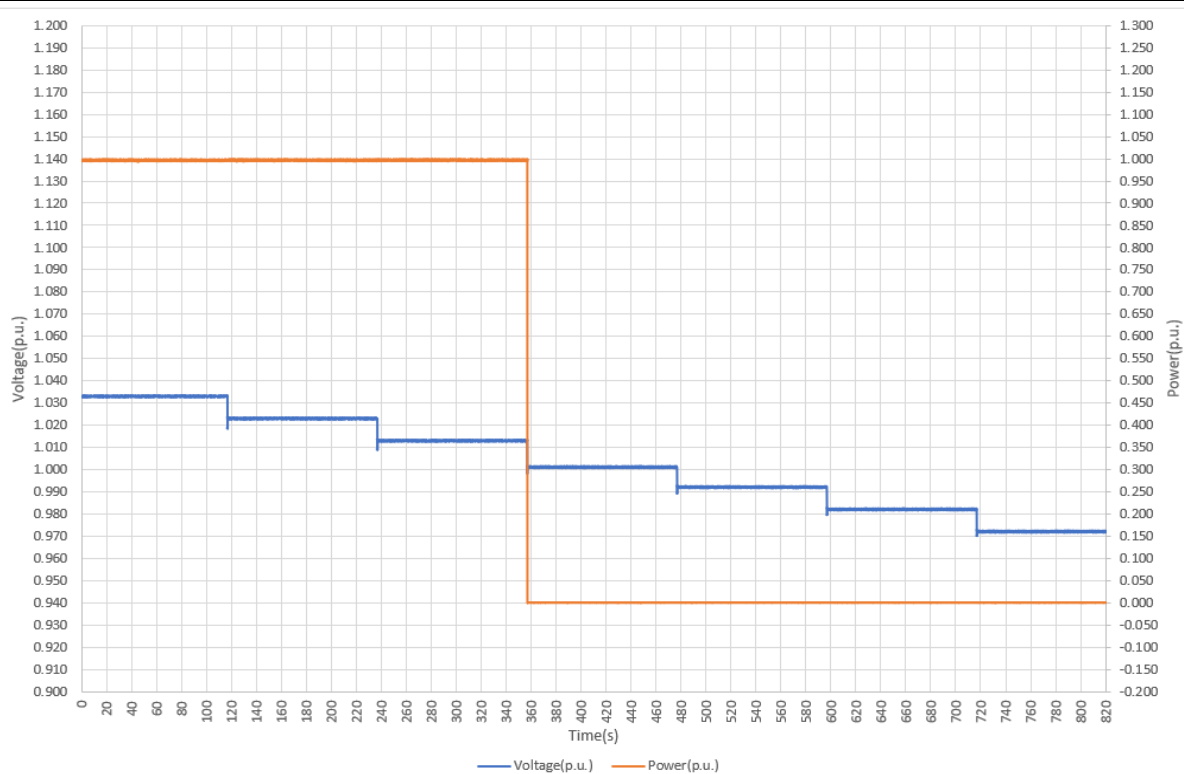
Undervoltage - Test 1: Trip value



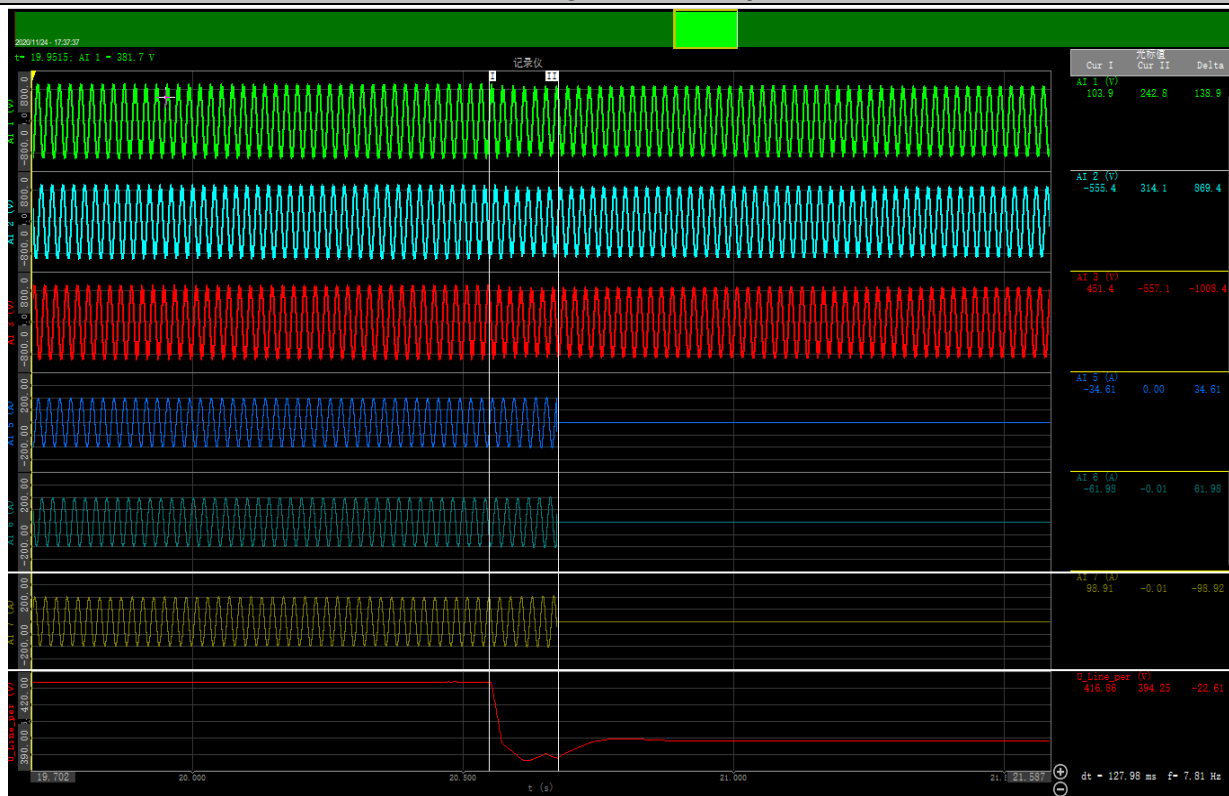
Undervoltage - Test 1: Trip time



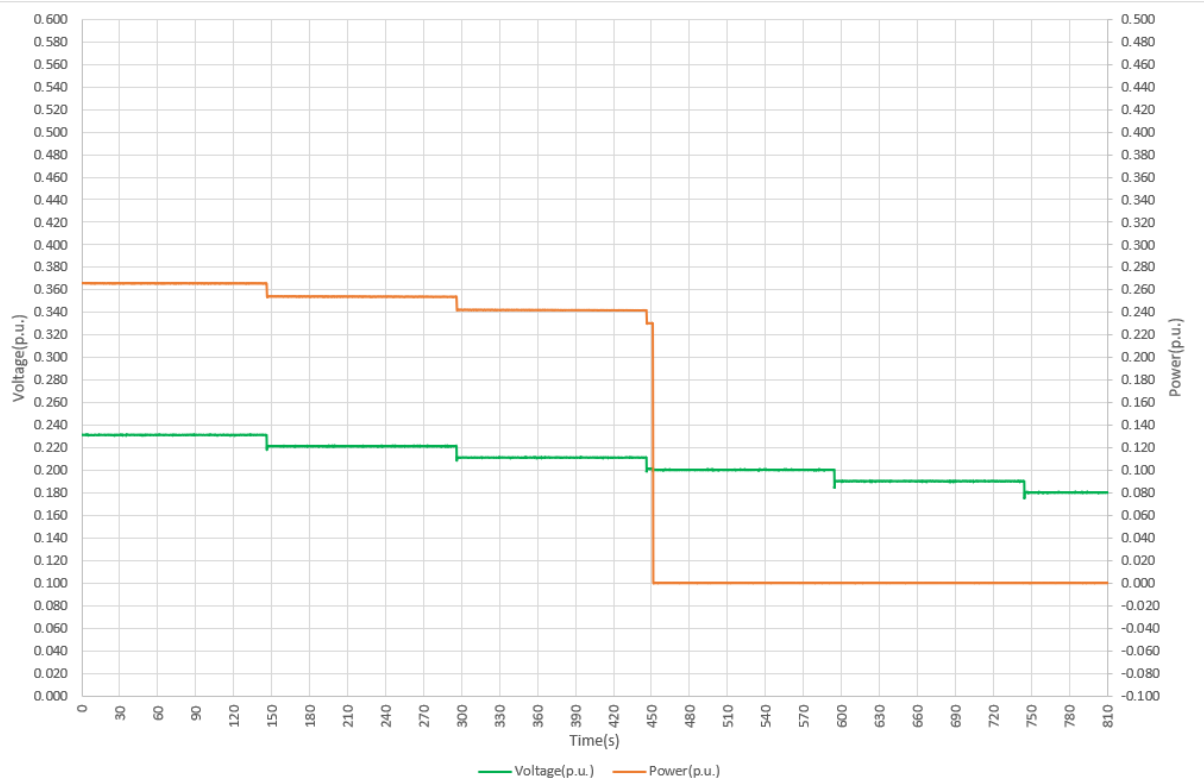
Under voltage - Test 2: Trip value



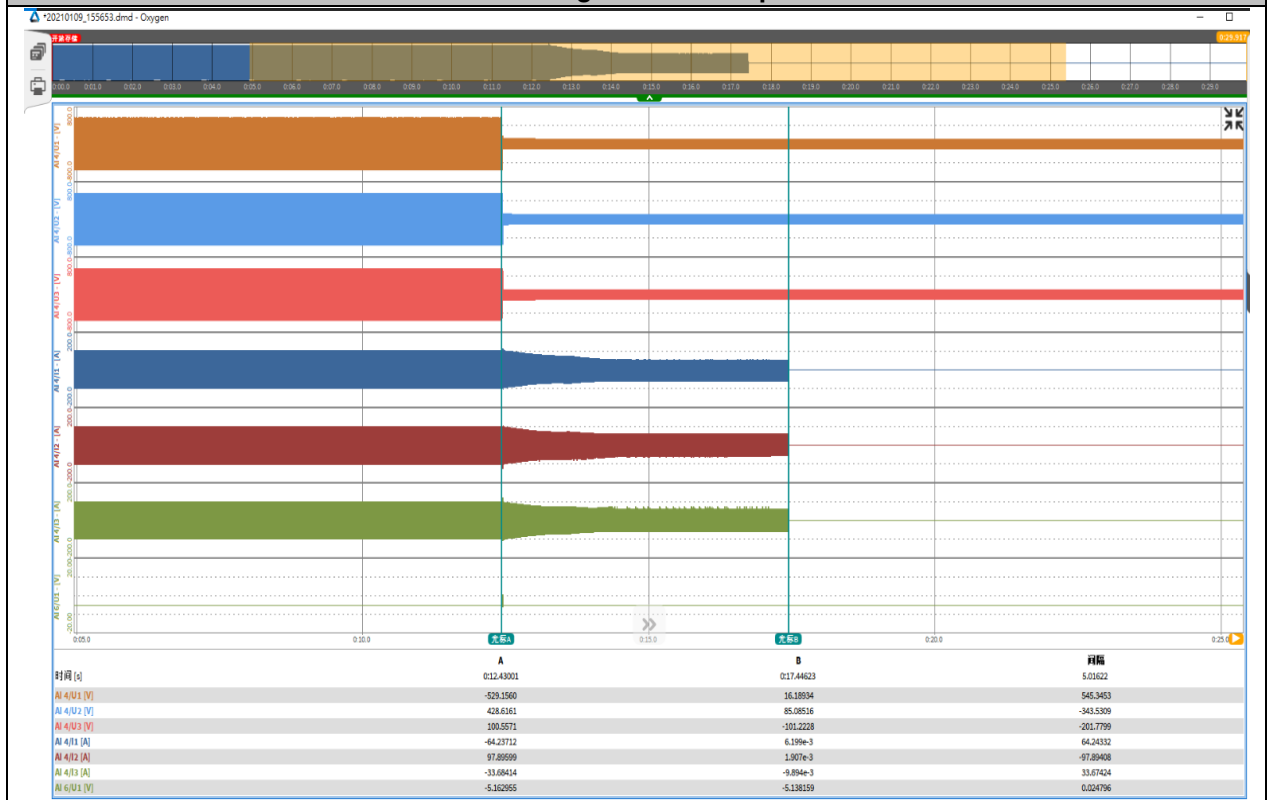
Under voltage - Test 2: Trip time



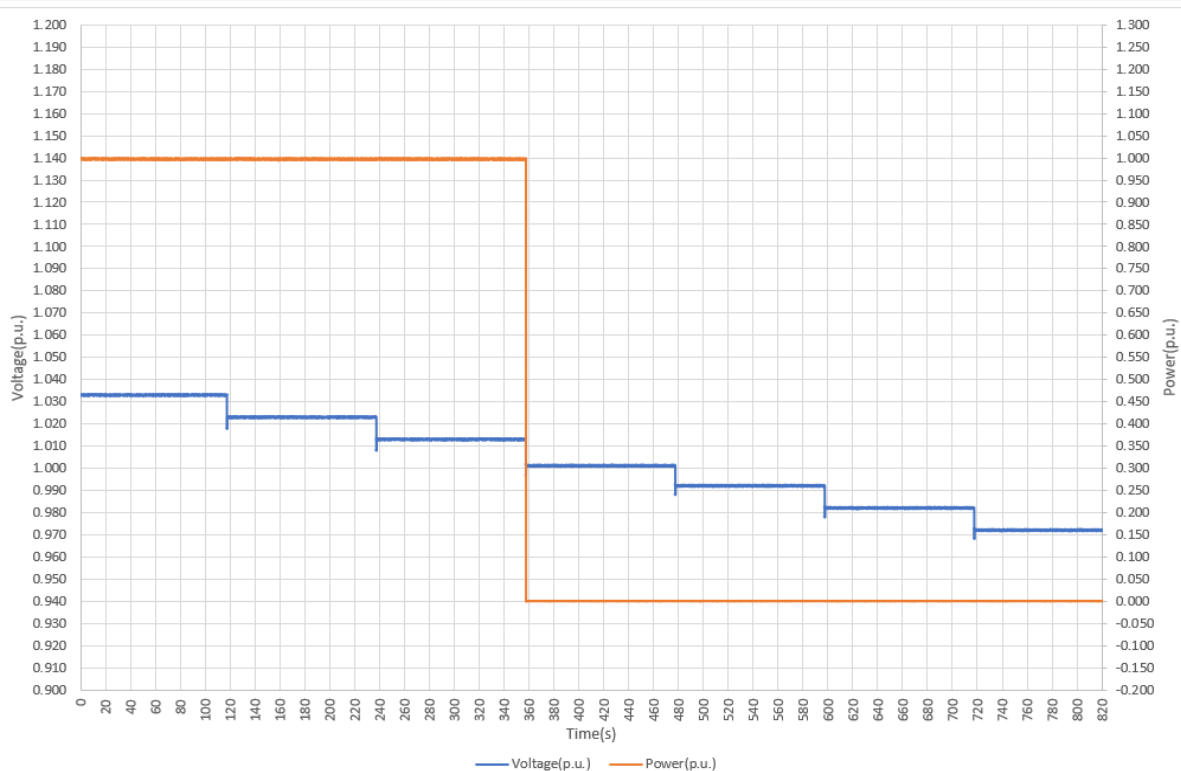
Undervoltage - Test 3: Trip value



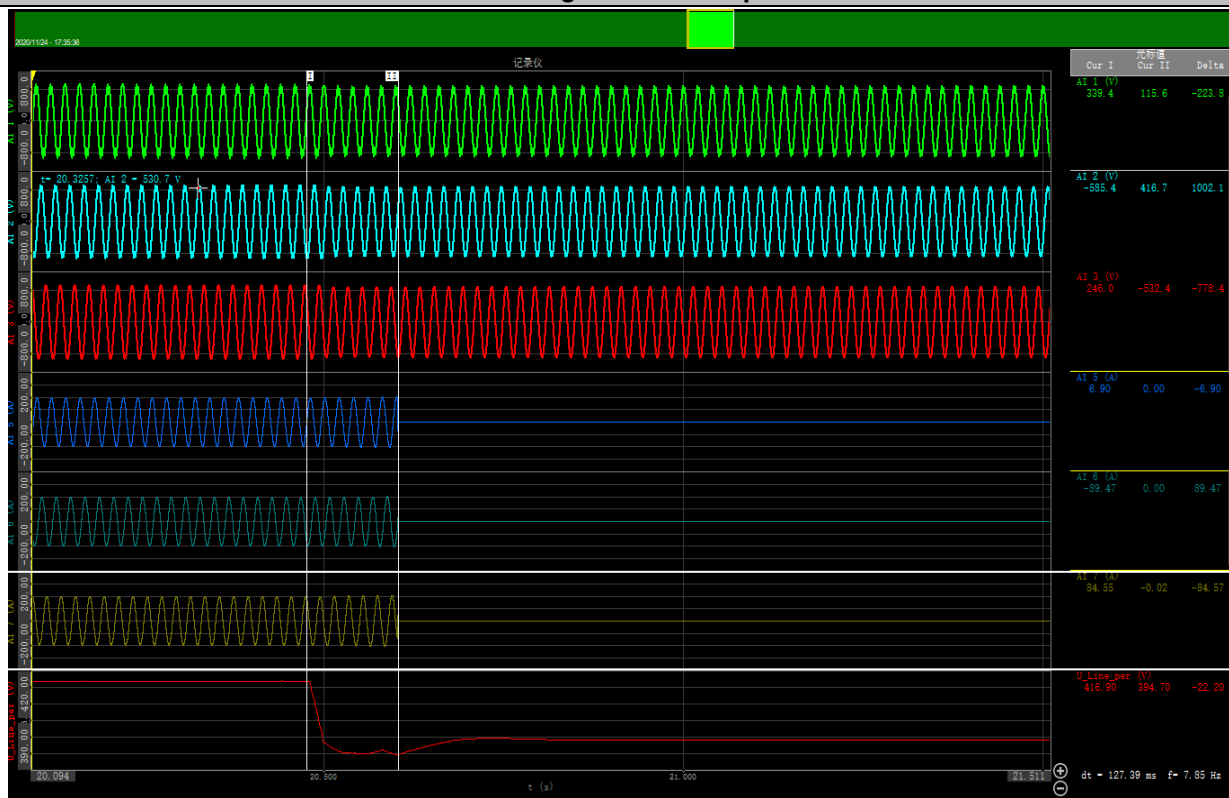
Undervoltage - Test 3: Trip time



Undervoltage - Test 4: Trip value



Undervoltage - Test 4: Trip time



4.6.1.2 Overvoltage protection

Overvoltage protection may be implemented with two completely independent protection thresholds, each one able to be activated or not. The standard adjustment ranges are as follows.

Overvoltage threshold stage 1 [59 >]:

- Threshold (1.0 – 1.2) U_n adjustable by steps of 0.01 U_n
- Operate time (0.1 – 100) s adjustable in steps of 0.1 s

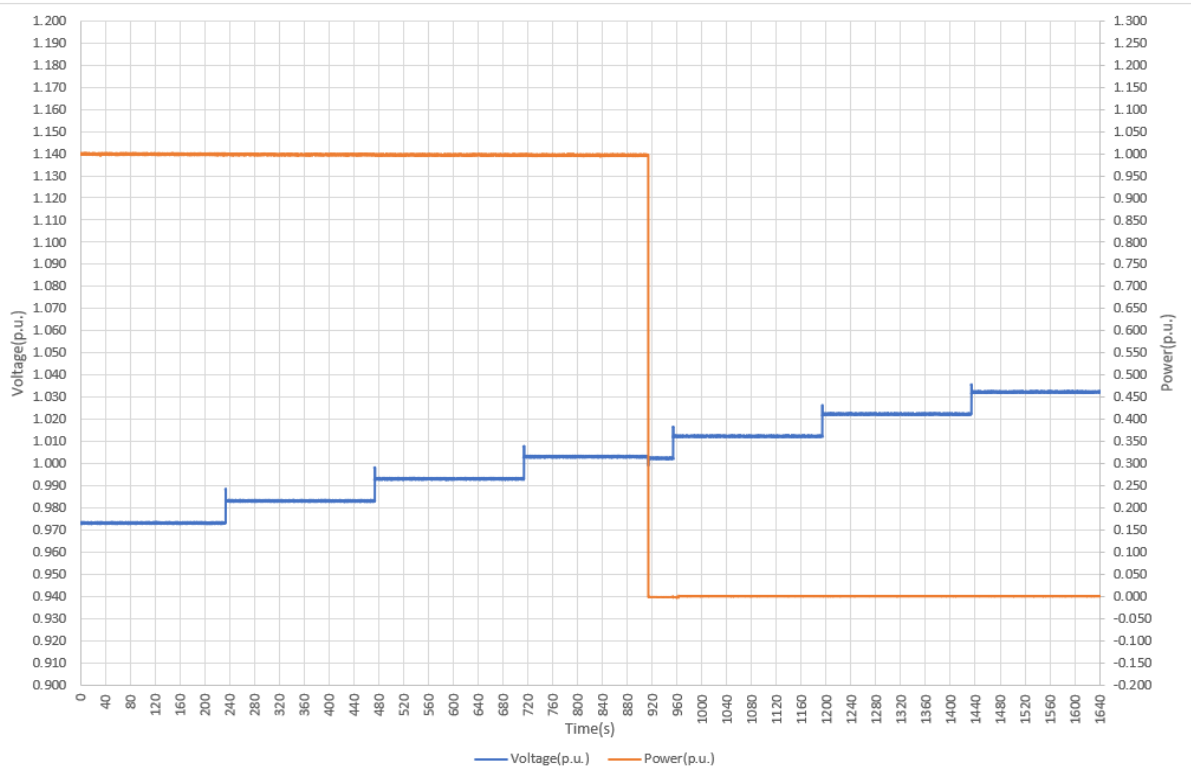
Overvoltage threshold stage 2 [59 > >]:

- Threshold (1.0 – 1.30) U_n adjustable by steps of 0.01 U_n
- Operate time (0.1 – 5) s adjustable in steps of 0.05 s

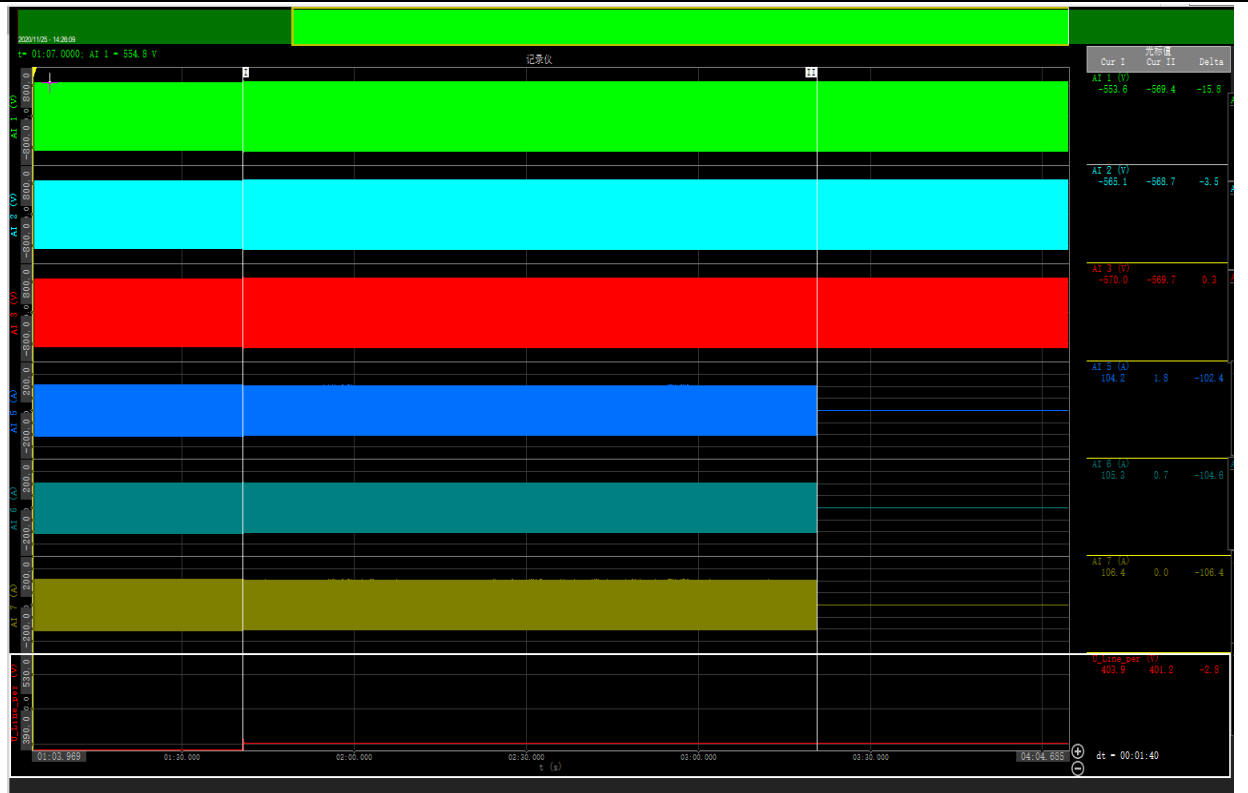
The following definitions apply to the test to verify the clause:

Overvoltage	Test No.	Voltage setting (p.u.)	Voltage meas. (p.u.)	Voltage deviation (p.u.)	Trip time setting (s)	Trip time meas. (s)	Trip time deviation (s)
Stage 1 [59 >]:	1	1.000	1.003	0.003	100.000	100.000	0.000
	2	1.200	1.205	0.005	0.100	0.125	0.025
Stage 2 [59 > >]:	3	1.000	1.006	0.006	5.000	5.037	0.037
	4	1.300	1.305	0.005	0.100	0.127	0.027

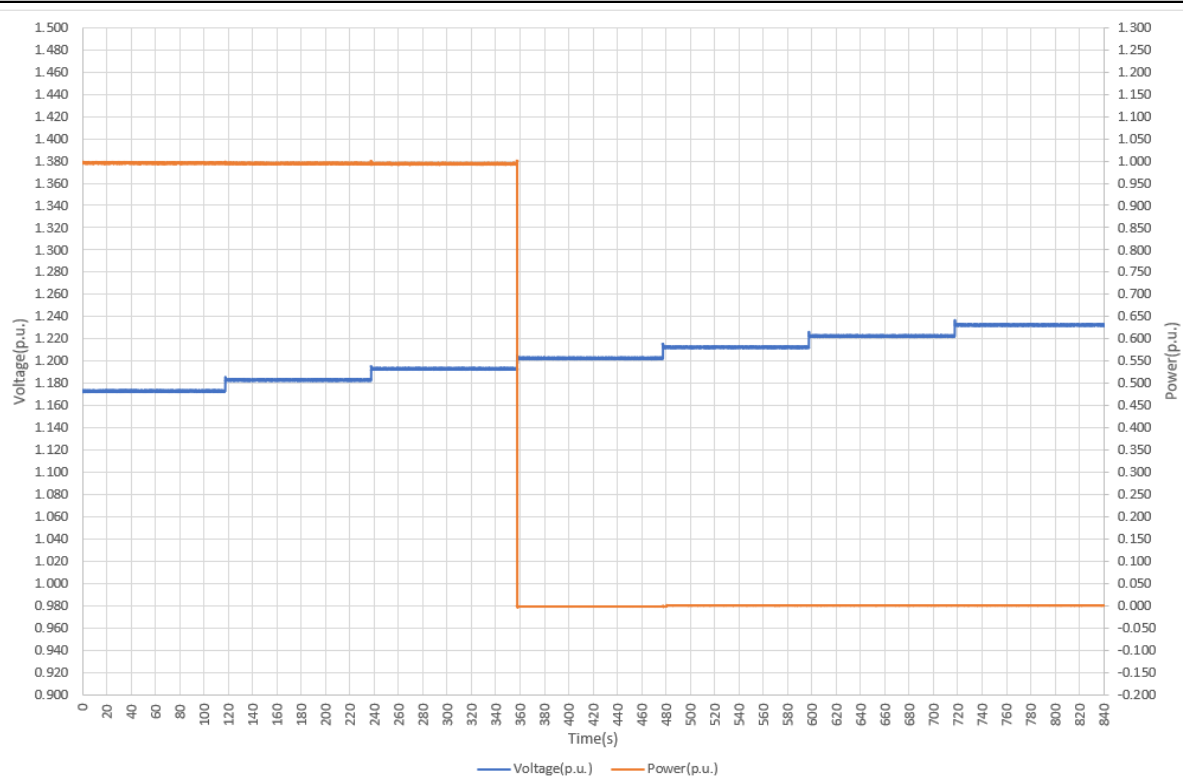
Overvoltage - Test 1: Trip value



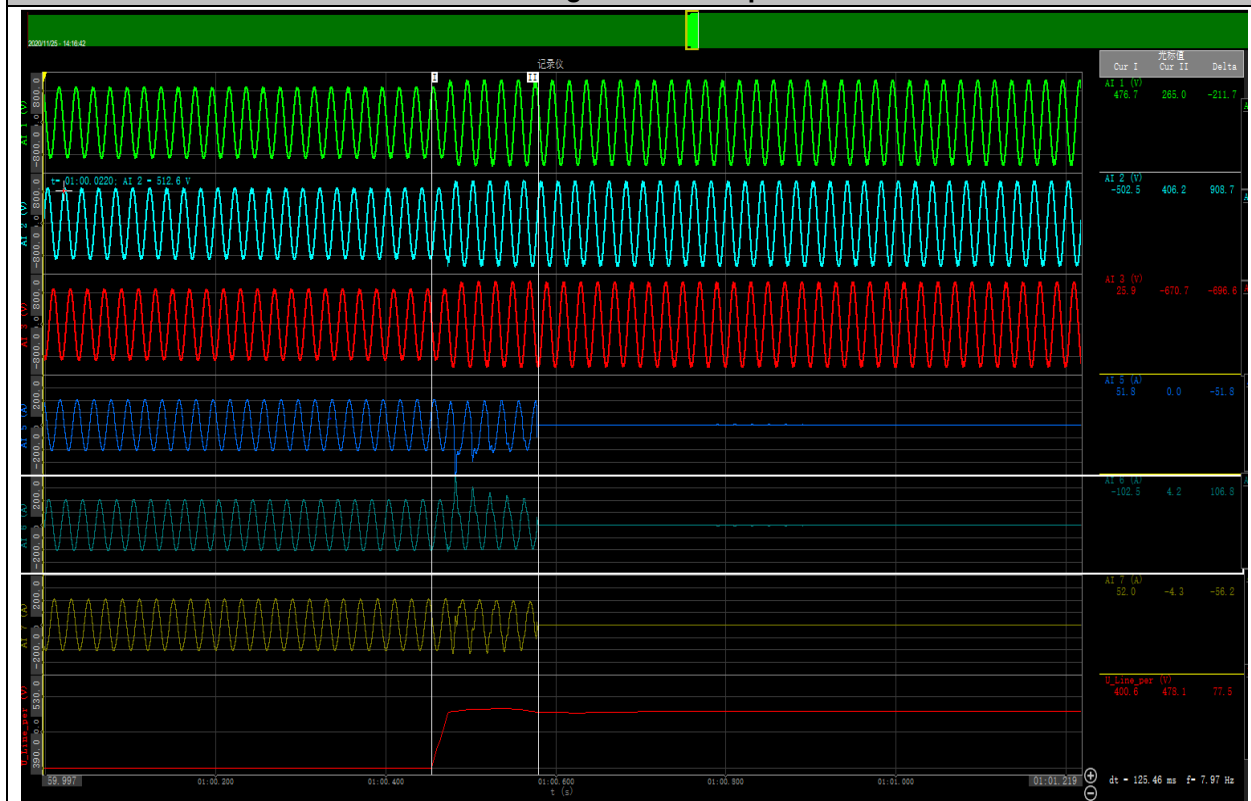
Overvoltage - Test 1: Trip time



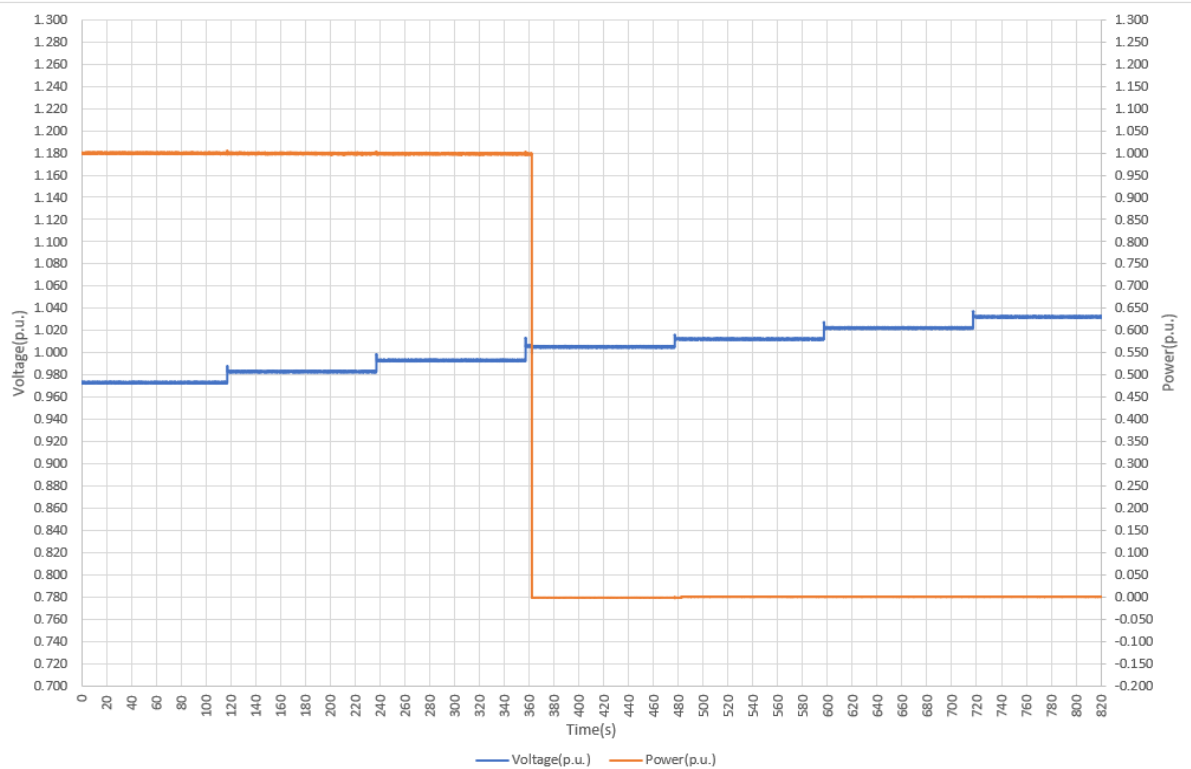
Overvoltage - Test 2: Trip value



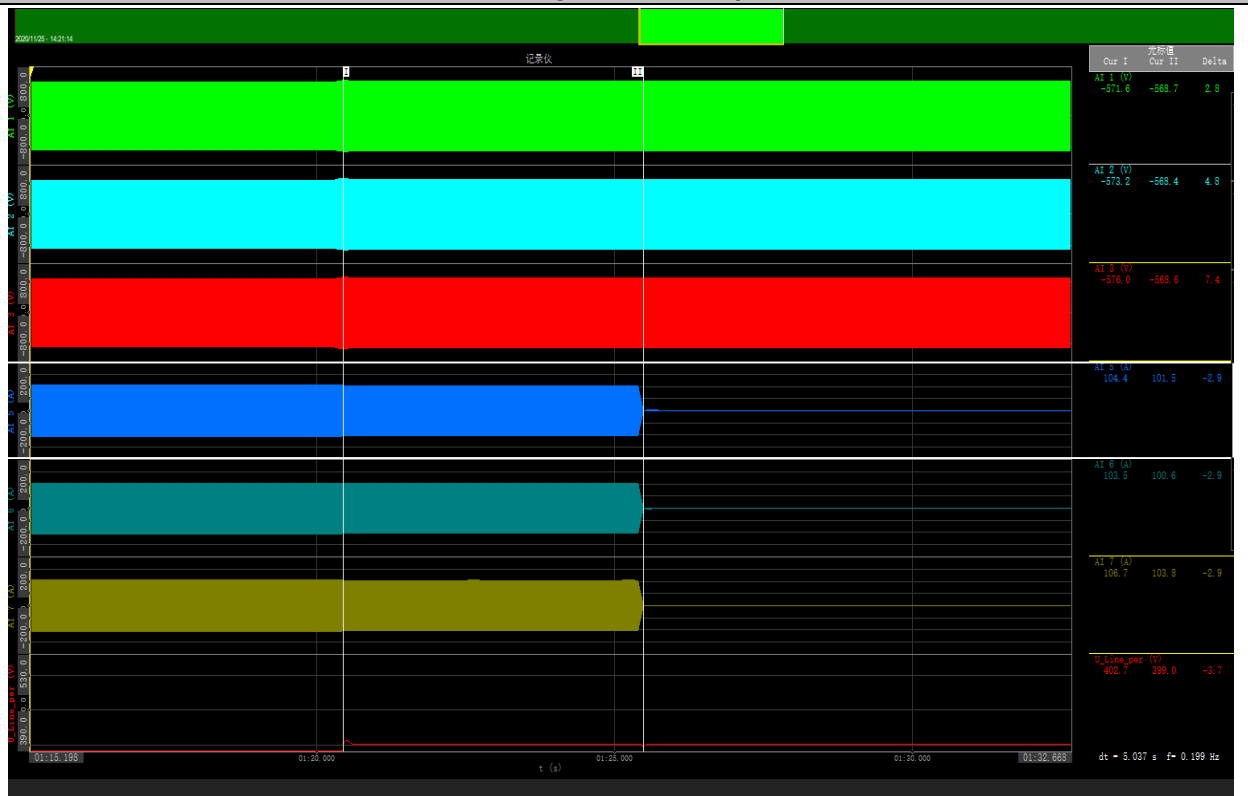
Overvoltage - Test 2: Trip time



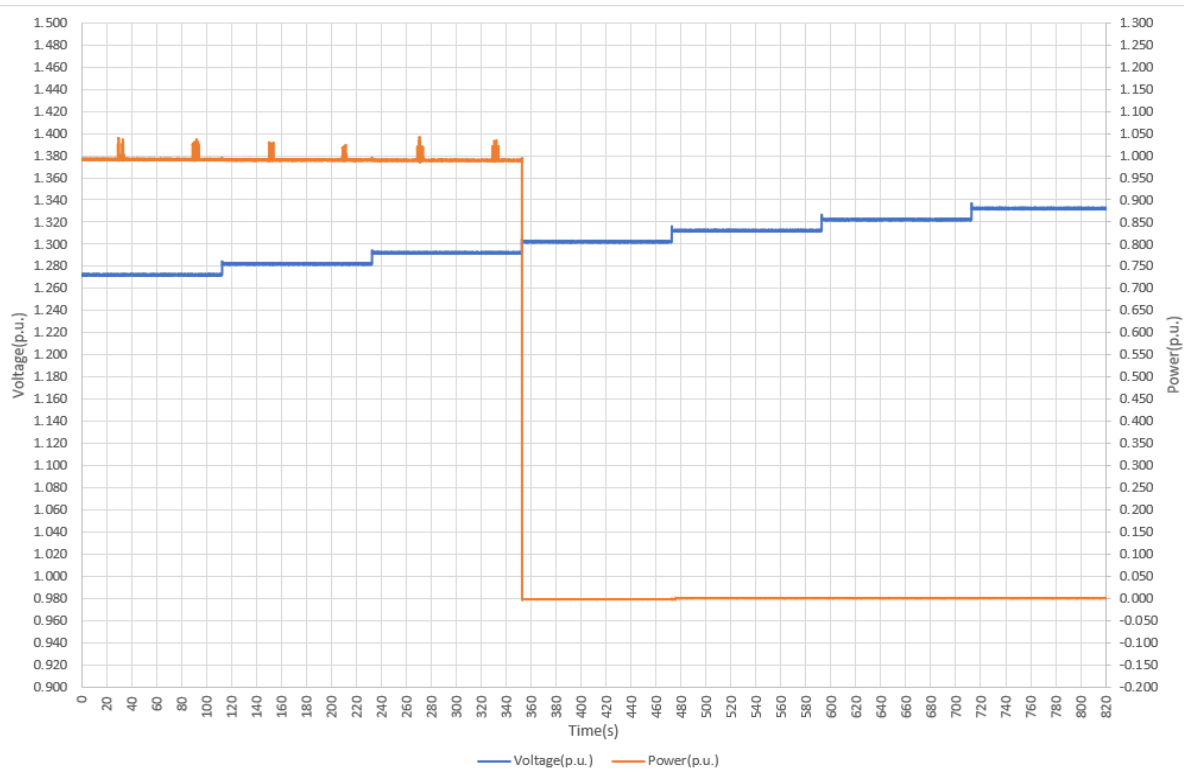
Overvoltage - Test 3: Trip value



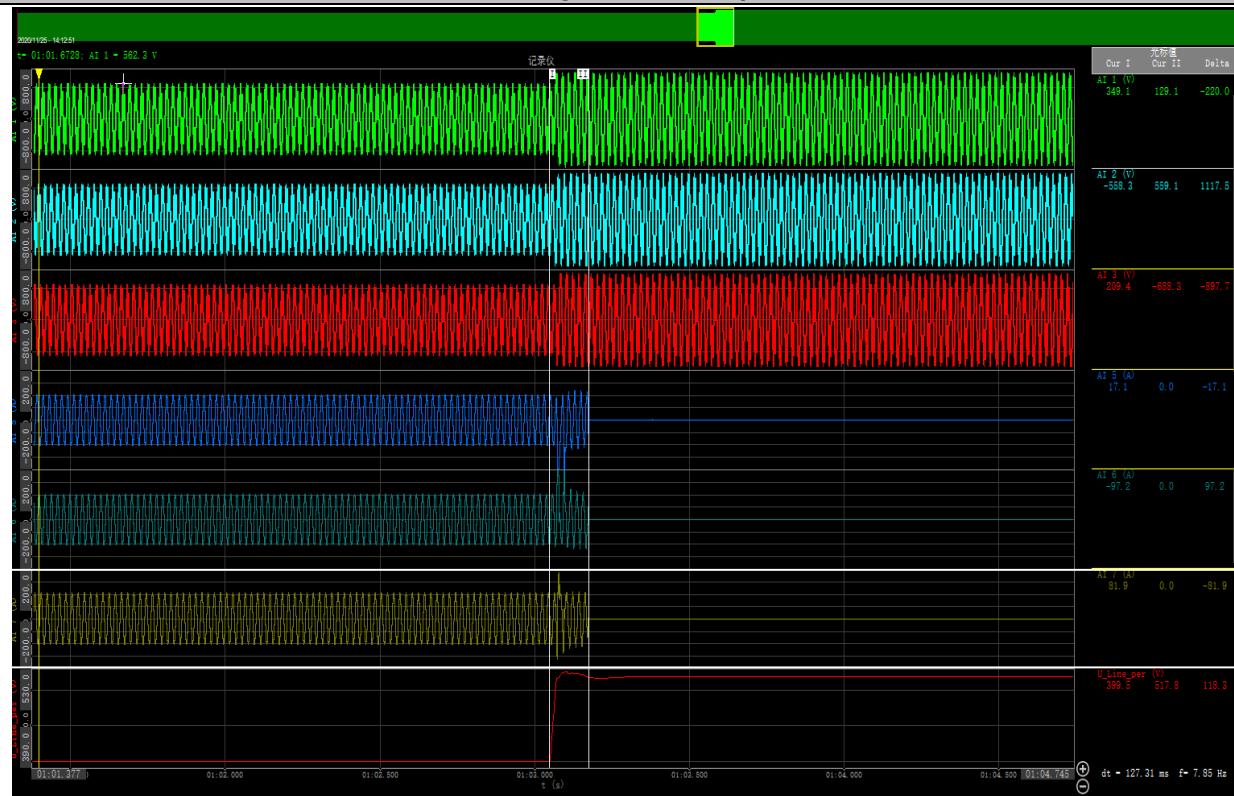
Overvoltage - Test 3: Trip time



Overvoltage - Test 4: Trip value



Overvoltage - Test 4: Trip time



4.6.1.3 Overvoltage 10 min mean protection

The function shall be based on the calculation of the square root of the arithmetic mean of the squared input values over 10 min. The calculation of a new 10 min value at least every 3 s is sufficient, which is then to be compared with the threshold value.

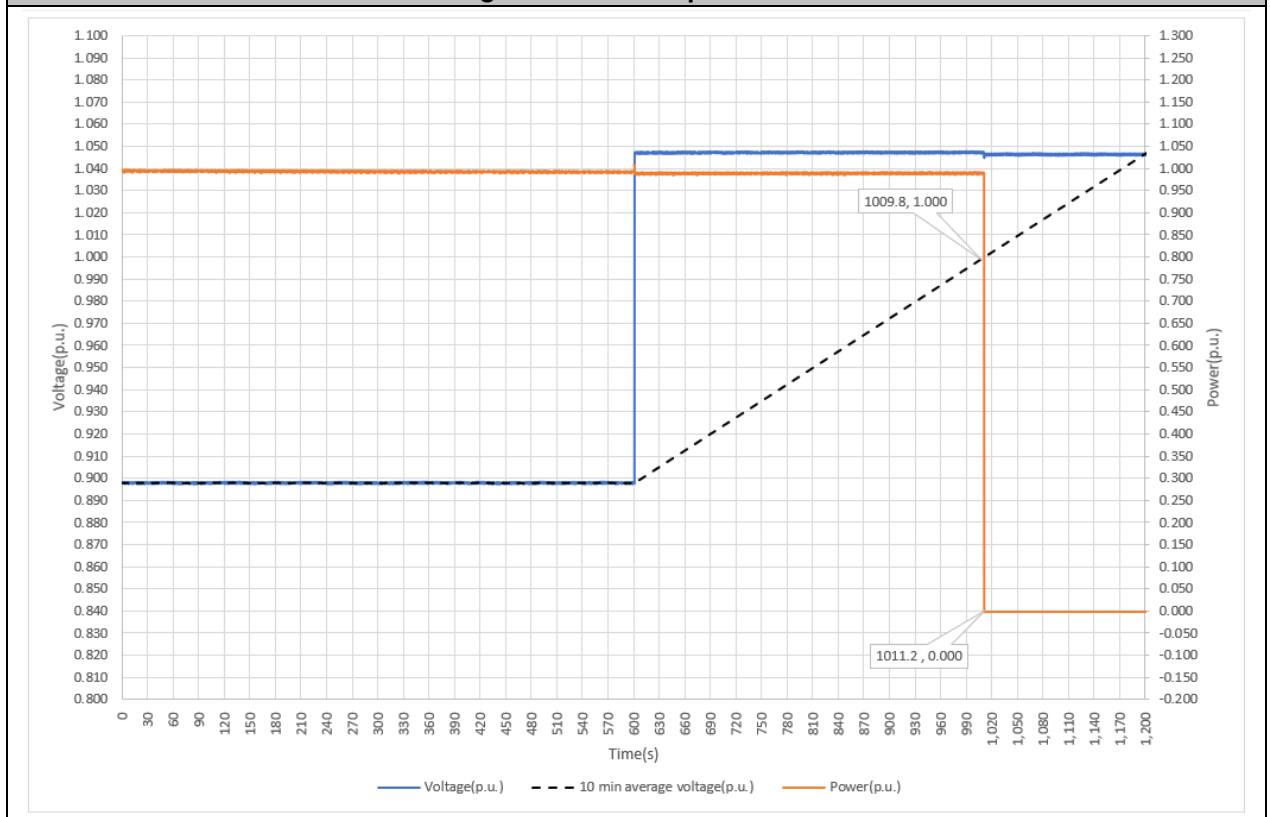
- Threshold (1.0 – 1.15) U_n adjustable by steps of 0.01 U_n
- Start time ≤ 3 s not adjustable
- Time delay setting = 0 ms

The following definitions apply to the test to verify the clause:

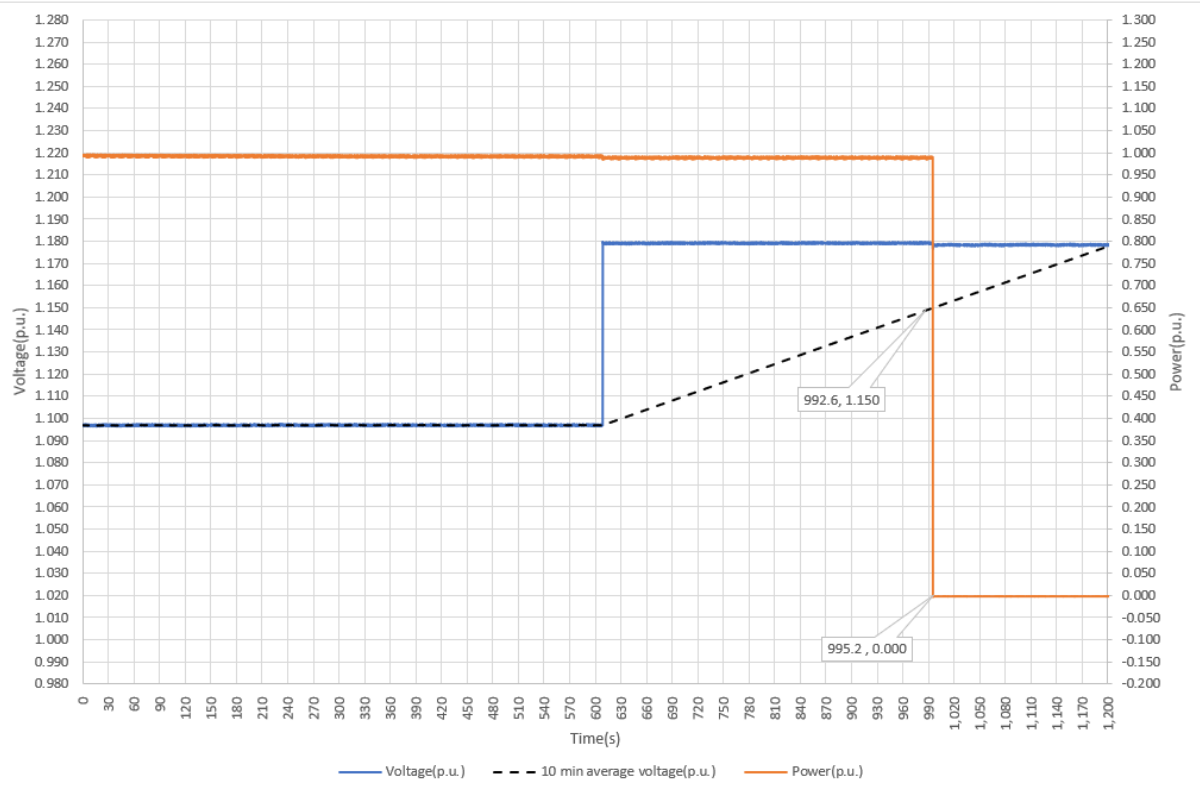
Test No.	Voltage setting (p.u.)	Voltage meas. (p.u.)	Voltage deviation (p.u.)	Trip time meas. (s)	Trip time limited
1	1.000	1.000	0.000	1.4	≤ 3 s
2	1.150	1.150	0.000	2.6	≤ 3 s

Note: The trip voltage accuracy tolerance is $\pm 0.01 U_n$

Overvoltage 10 min mean protection - Test 1



Overvoltage 10 min mean protection - Test 2



4.6.1.4 Underfrequency protection

Underfrequency protection may be implemented with two completely independent protection thresholds, each one able to be activated or not. The standard adjustment ranges are as follows.

Underfrequency threshold stage 1 [81 <]:

- Threshold (47.0 – 50.0) Hz adjustment by steps of 0.1 Hz
- Operate time (0.1 – 100) s adjustable in steps of 0.1 s

Underfrequency threshold stage 2 [81 < <]:

- Threshold (47.0 – 50.0) Hz adjustment by steps of 0.1 Hz
- Operate time (0.1 – 5) s adjustable in steps of 0.05 s

In order to use narrow frequency thresholds for islanding detection (see 4.9.3.3) it may be required to have the ability to activate and deactivate a stage by an external signal.

The frequency protection shall function correctly in the input voltage range between 20 % Un and 120 % Un and shall be inhibited for input voltages of less than 20 % Un.

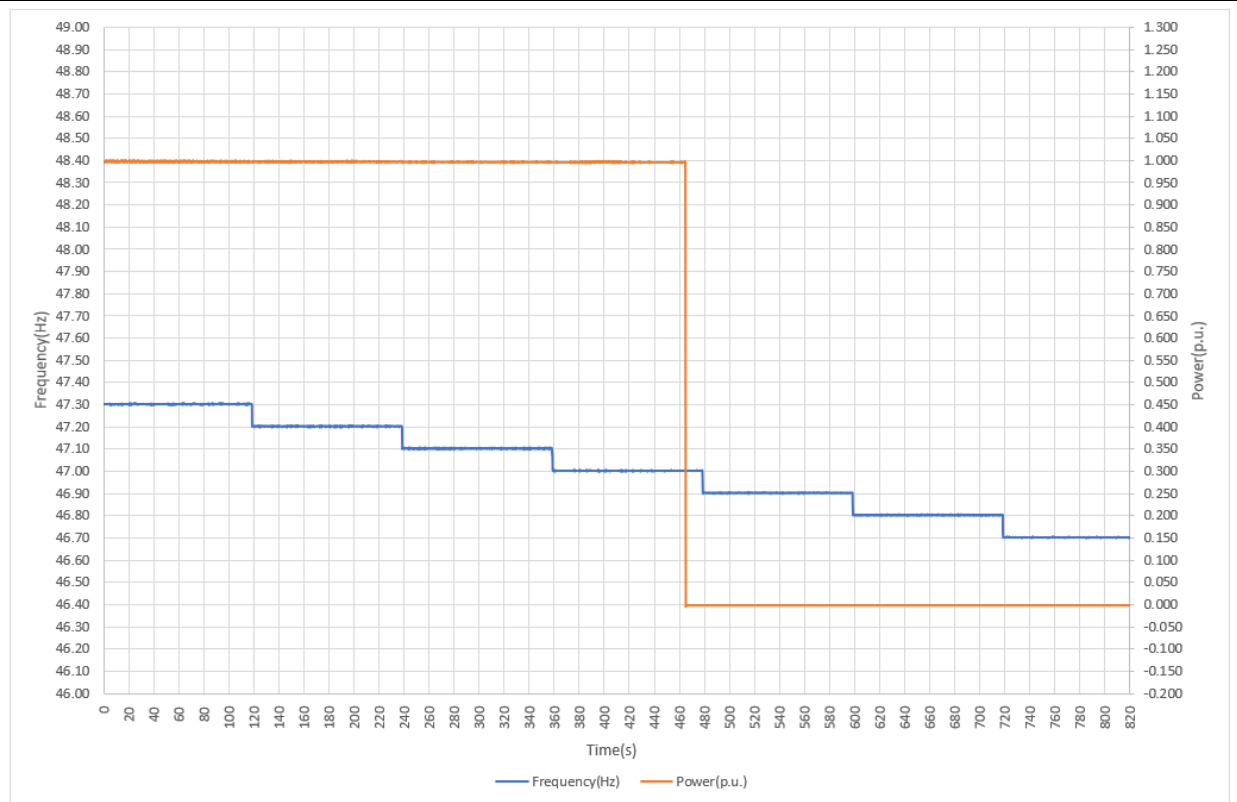
Under 0.2 Un the frequency protection is inhibited. Disconnection may only happen based on undervoltage protection.

The following definitions apply to the test to verify the clause:

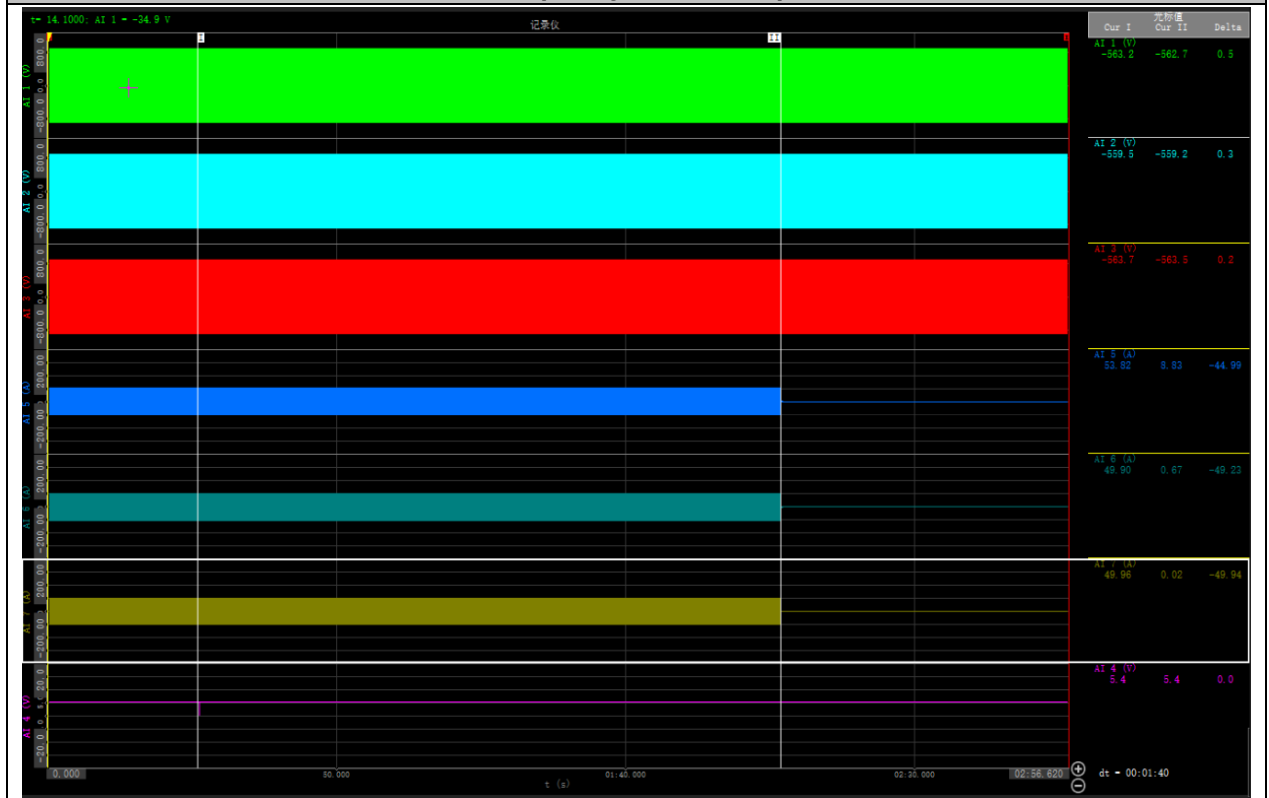
Under frequency	Test No.	Frequency setting (Hz)	Frequency meas. (Hz)	Frequency deviation (Hz)	Trip time setting (s)	Trip time meas. (s)	Trip time deviation (s)
Stage 1 [81 <]	1	47.00	47.00	0.00	100.000	100.000	0.000
	2	50.00	49.99	-0.01	0.100	0.121	0.021
Stage 2 [81 < <]	3	47.00	47.00	0.00	5.000	5.005	0.005
	4	50.00	49.99	-0.01	0.100	0.126	0.026

Voltage protection threshold setting (Un)	Test No.	Frequency setting (Hz)	Voltage setting (%Un)	Voltage meas. (Un)	Trip time setting (s)	Trip time meas. (s)	Trip time deviation (s)
0.18	5	47.00	0.21	0.21	5.000	5.000	0.000
	6	47.00	1.19	1.18	5.000	5.000	0.000
	7	47.00	0.19	0.19	Not protected	--	--
		47.00	0.17	0.17	5.000	4.950	-0.050

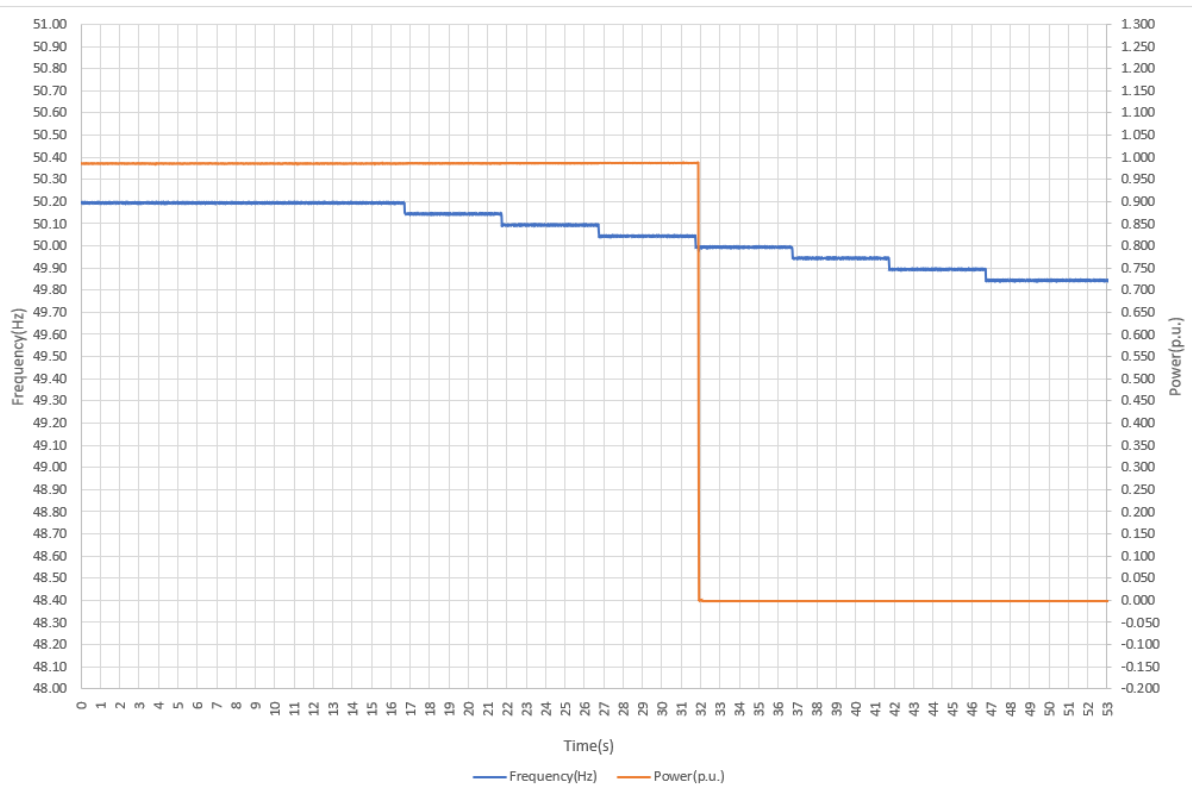
Underfrequency - Test 1: Trip value



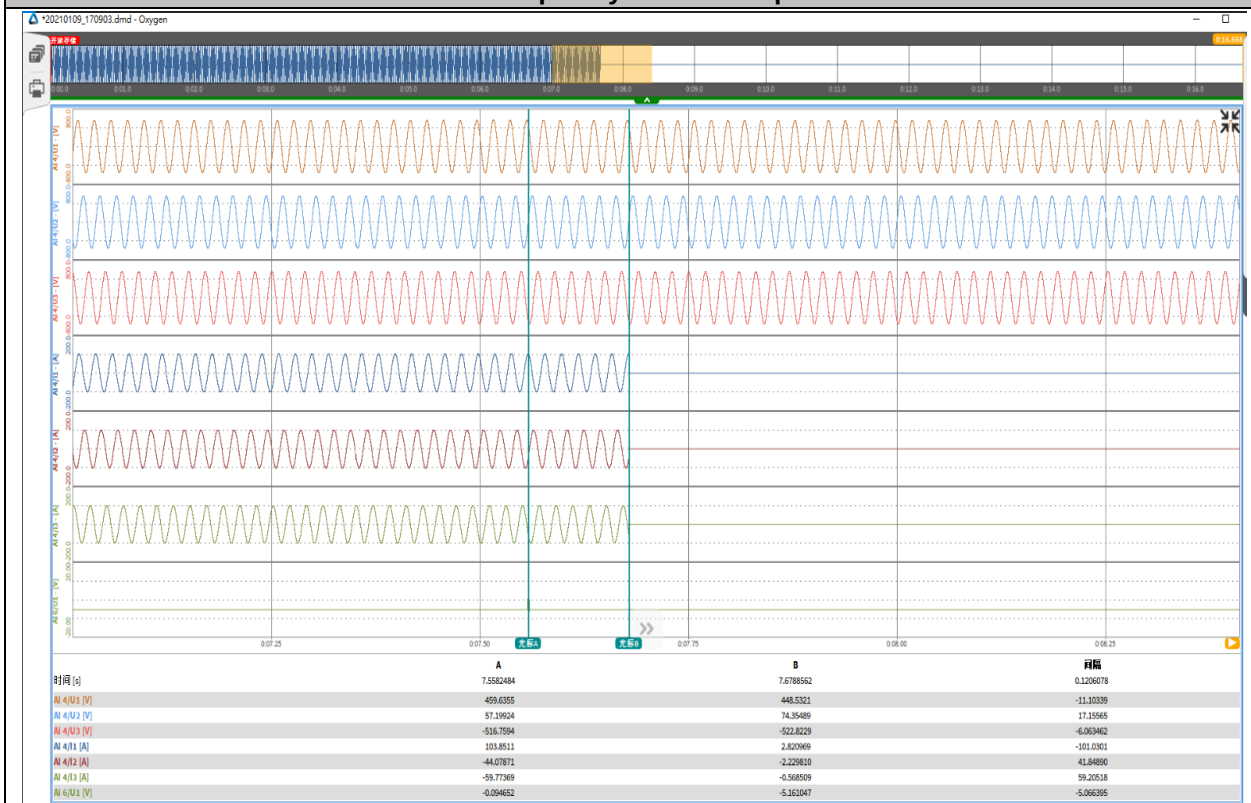
Underfrequency - Test 1: Trip time

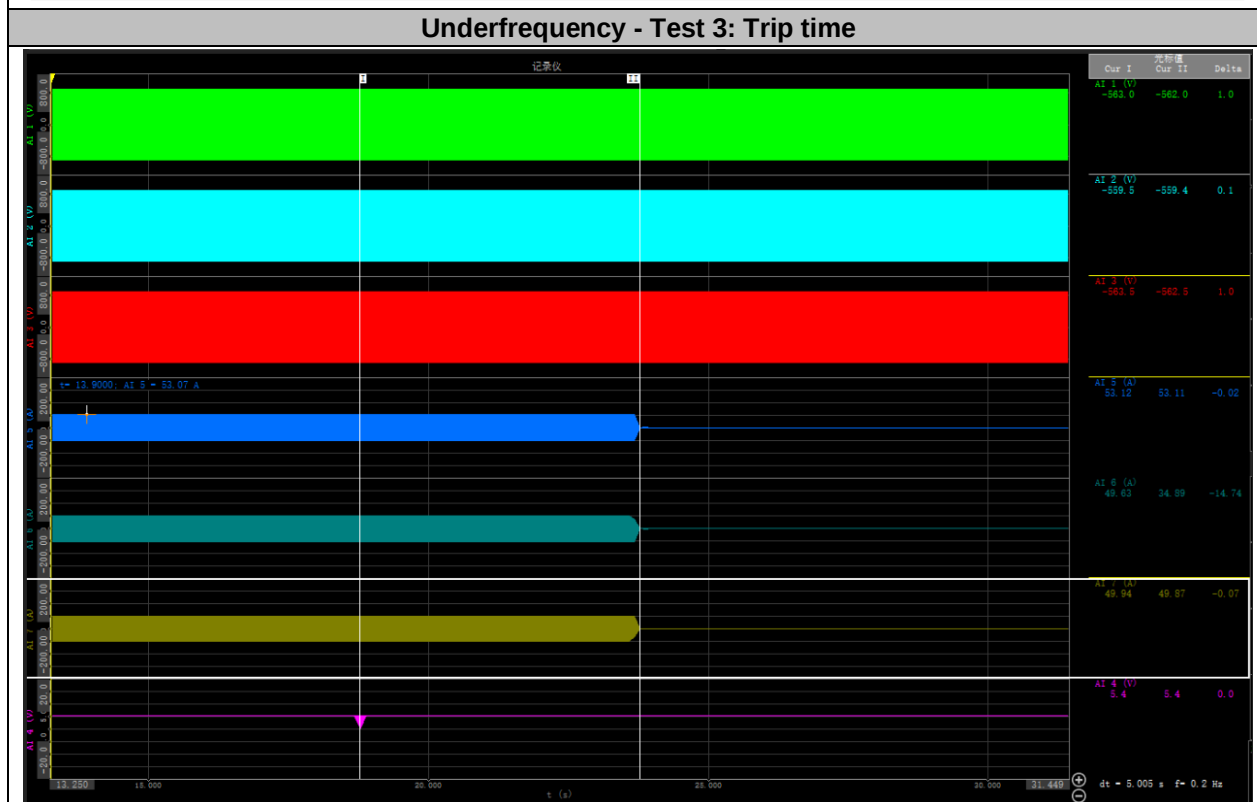
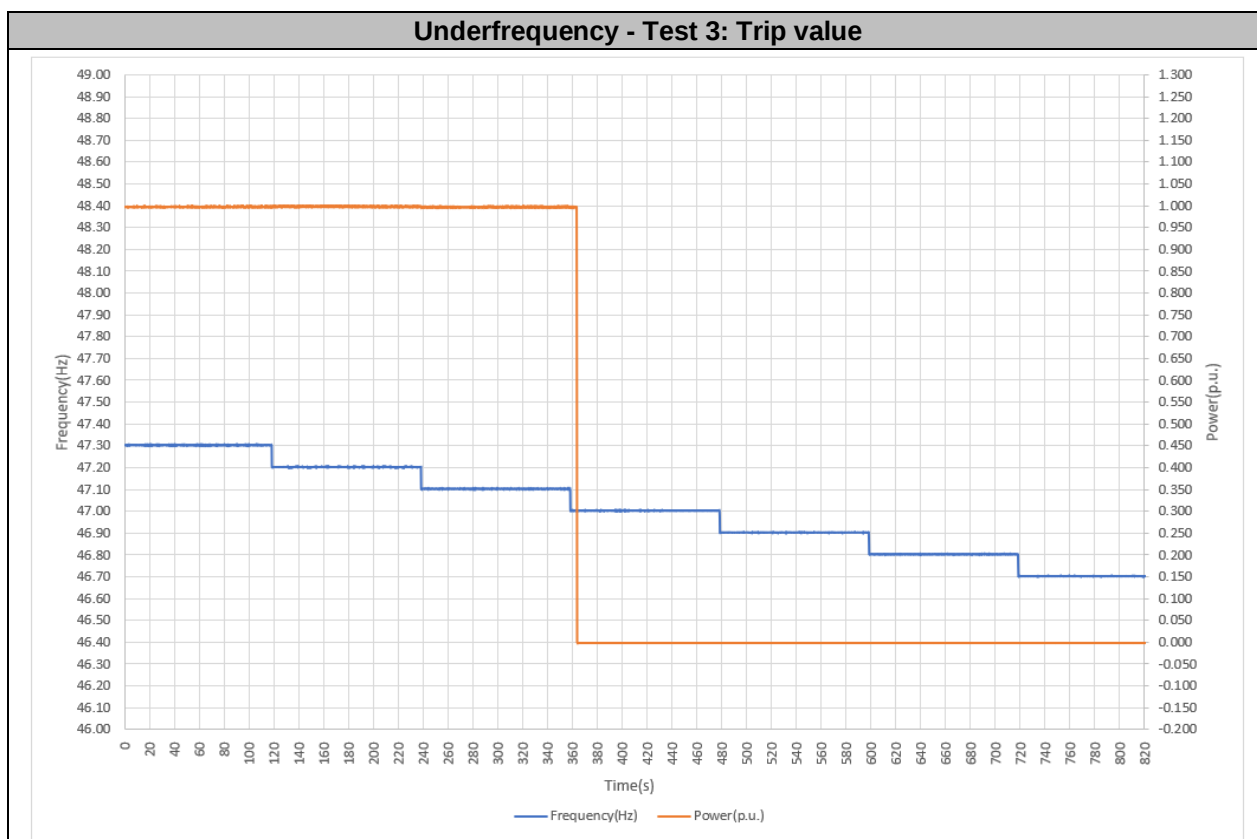


Underfrequency - Test 2: Trip value

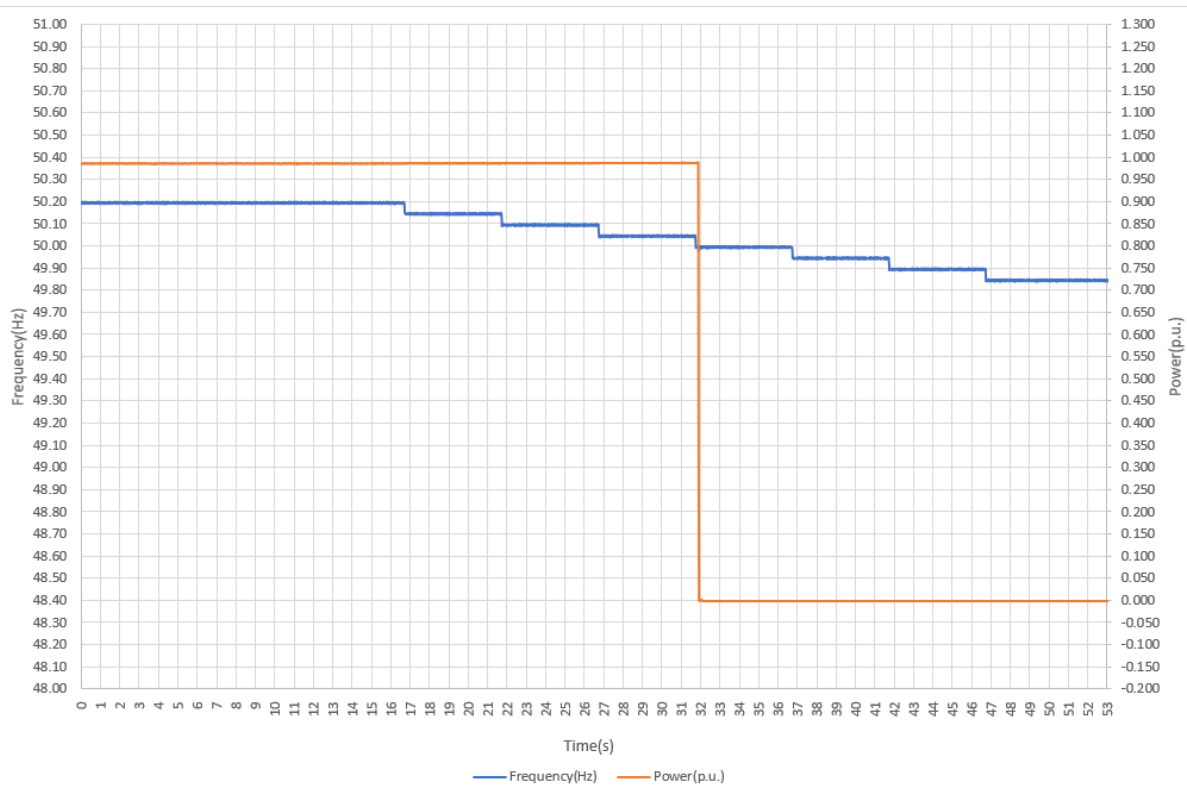


Underfrequency - Test 2: Trip time

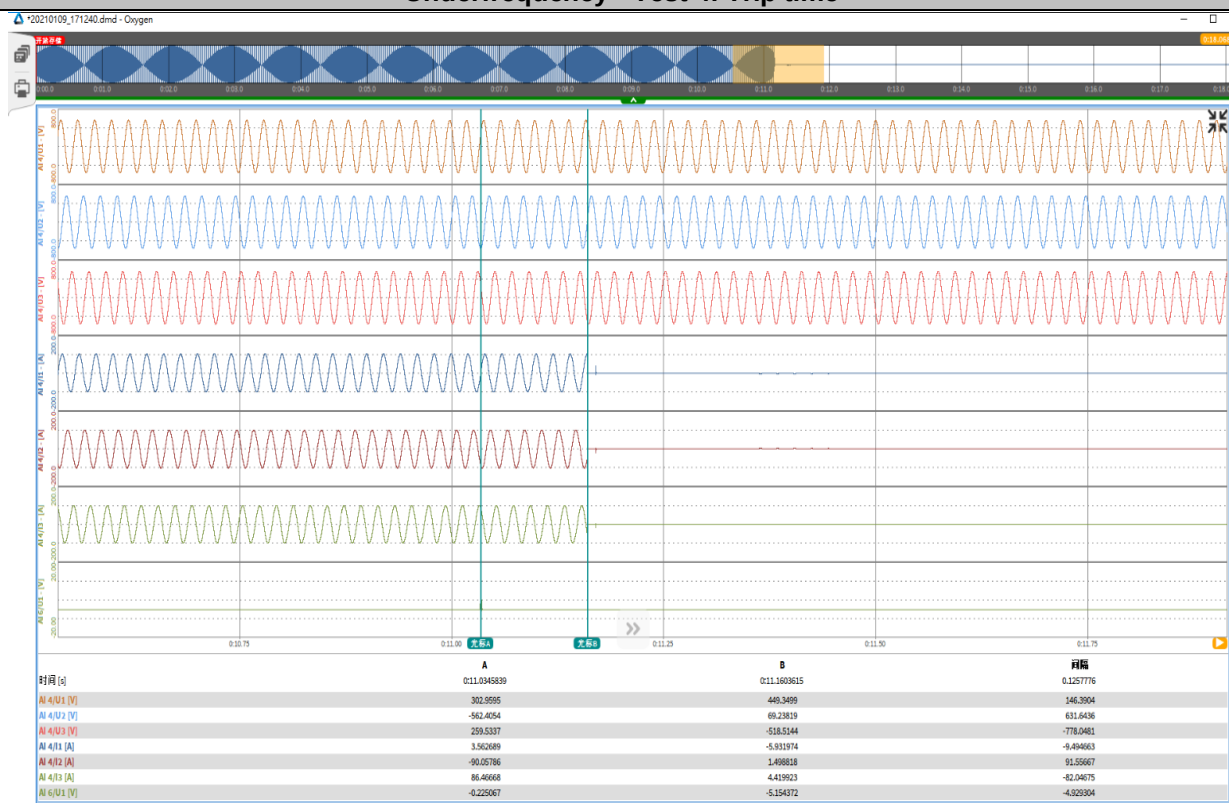




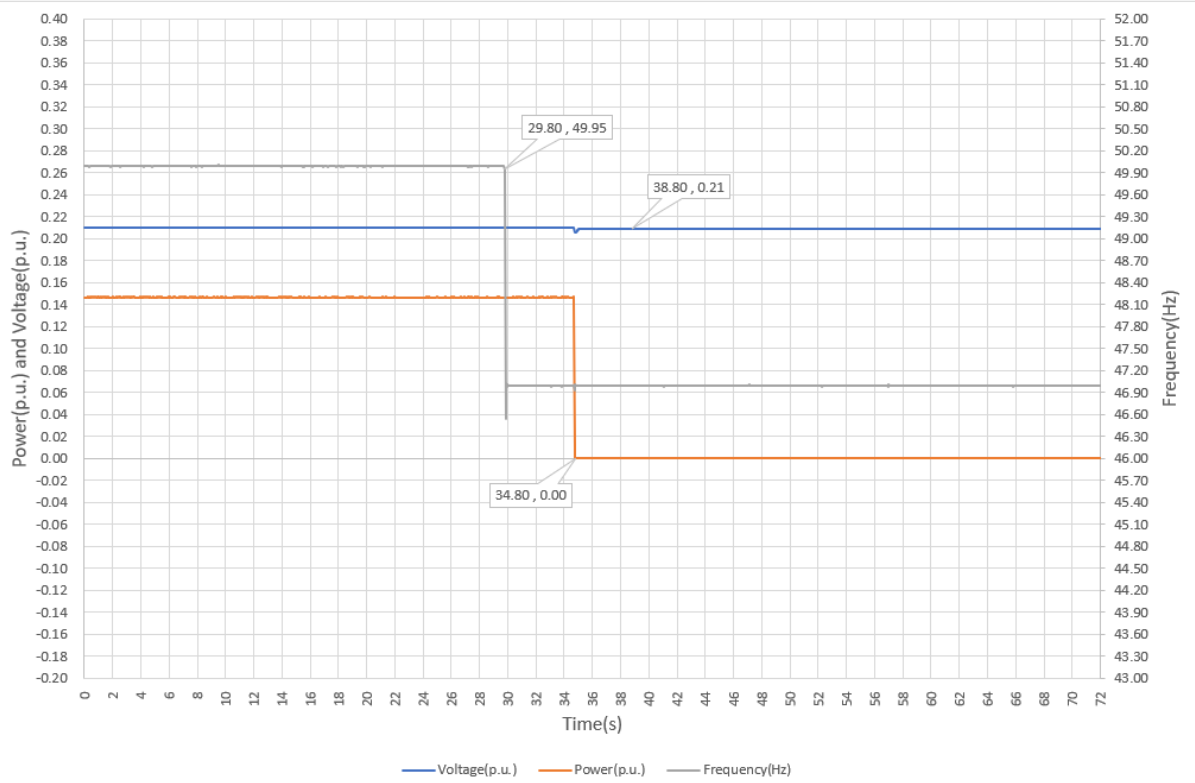
Underfrequency - Test 4: Trip value



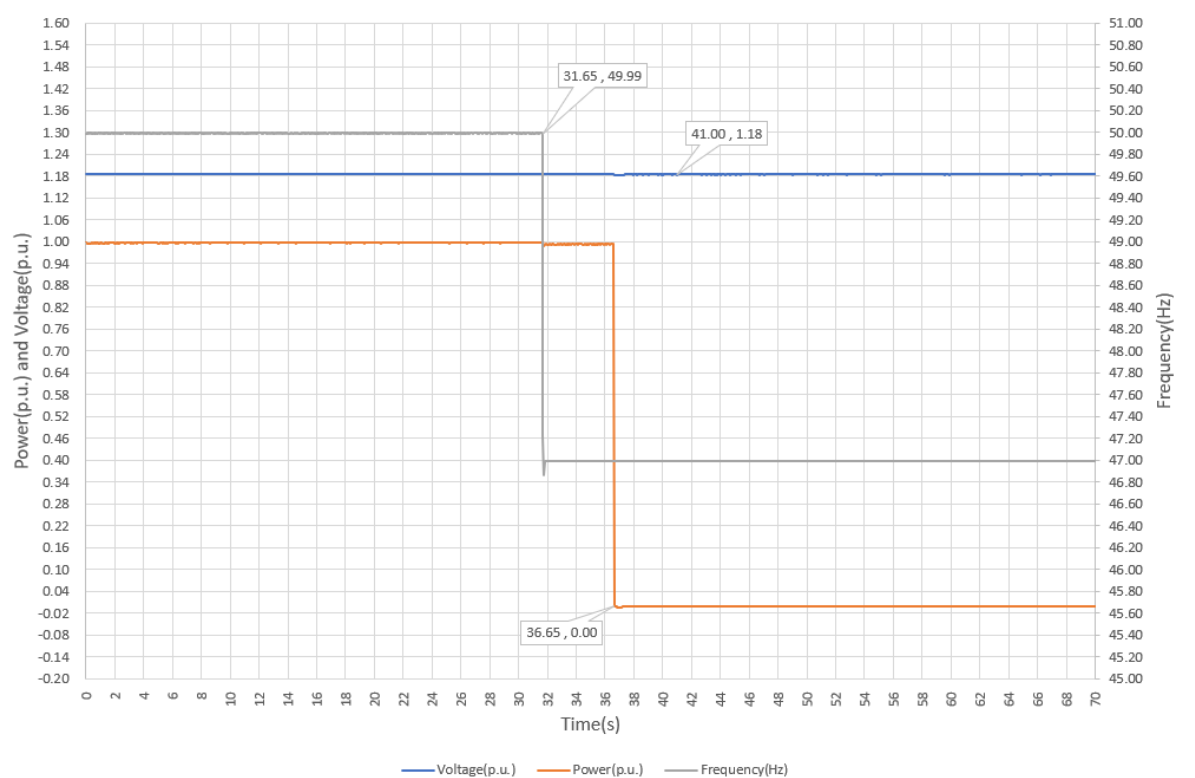
Underfrequency - Test 4: Trip time



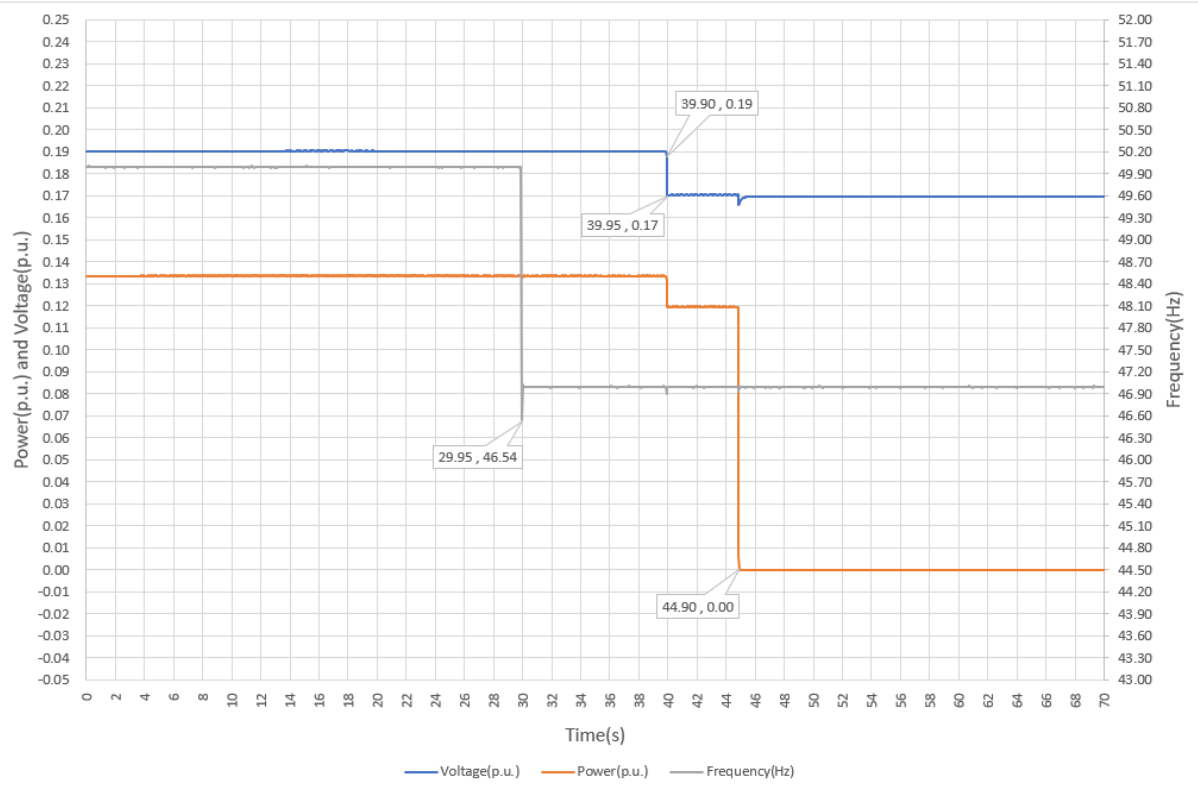
Test 5: Underfrequency 47Hz and Voltage 21%Un



Test 6: Underfrequency 47Hz and Voltage 119%Un



Test 7: Underfrequency 47Hz and Voltage 19%Un to 17%Un



4.6.1.5 Overfrequency protection

Overfrequency protection may be implemented with two completely independent protection thresholds, each one able to be activated or not. The standard adjustment ranges are as follows.

Overfrequency threshold stage 1 [81 >]:

- Threshold (50.0 – 52.0) Hz adjustment by steps of 0.1 Hz
- Operate time (0.1 – 100) s adjustable in steps of 0.1 s

Overfrequency threshold stage 2 [81 > >]:

- Threshold (50.0 – 52.0) Hz adjustment by steps of 0.1 Hz
- Operate time (0.1 - 5) s adjustable in steps of 0.05 s

In order to use narrow frequency thresholds for islanding detection (see 4.9.3.3) it may be required to have the ability to activate and deactivate a stage by an external signal.

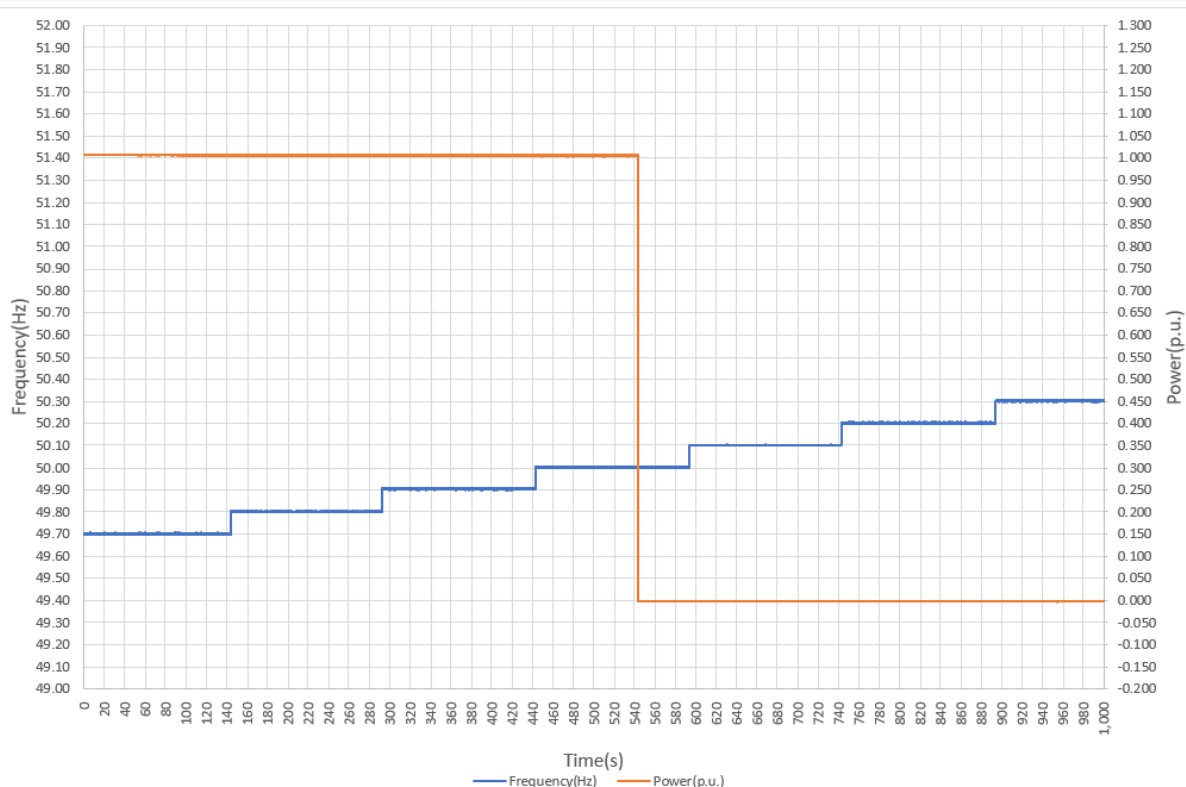
The frequency protection shall function correctly in the input voltage range between 20 %Un and 120 %Un and shall be inhibited for input voltages of less than 20 %Un.

The following definitions apply to the test to verify the clause:

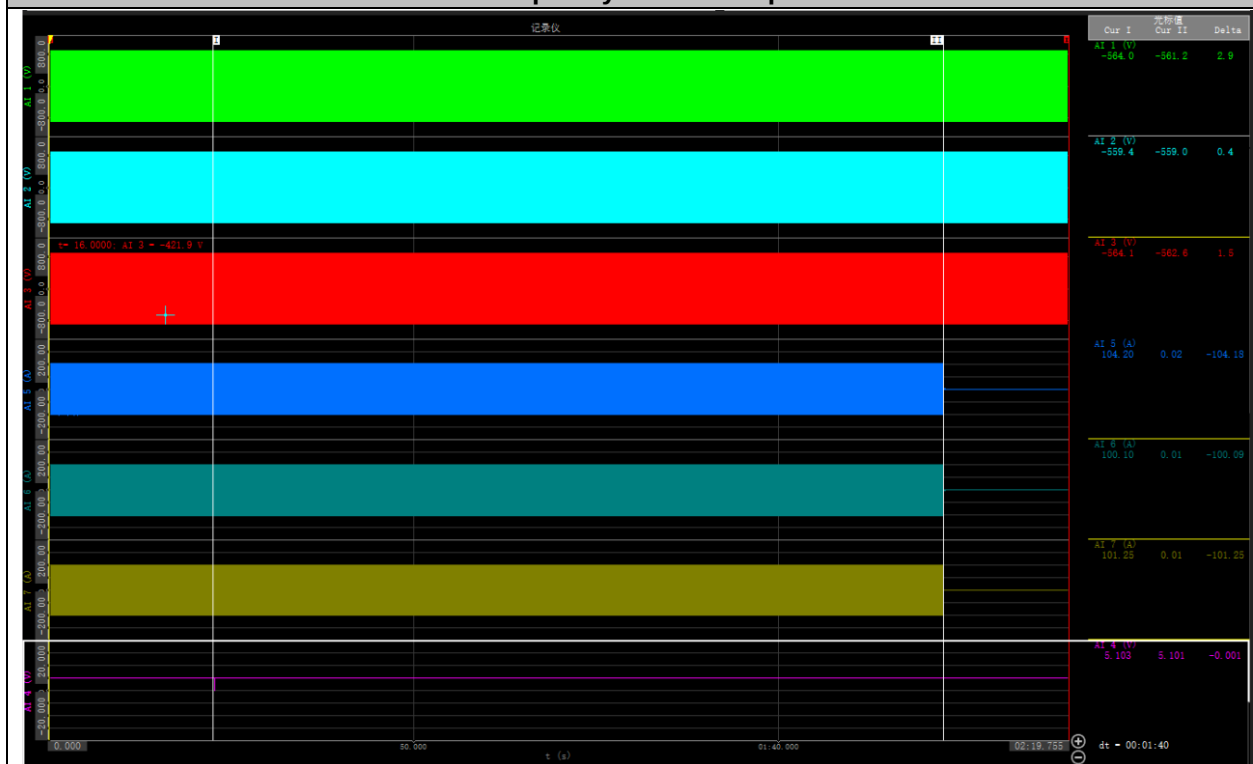
Over frequency	Test No.	Frequency setting (Hz)	Frequency meas. (Hz)	Frequency deviation (Hz)	Trip time setting (s)	Trip time meas. (s)	Trip time deviation (s)
Stage 1 [81 >]	1	50.00	50.00	0.00	100.000	100.000	0.000
	2	52.00	52.00	0.00	0.100	0.112	0.012
Stage 2 [81 > >]	3	50.00	50.00	0.00	5.000	5.003	0.003
	4	52.00	52.00	0.00	0.100	0.098	0.012

Voltage protection threshold setting (Un)	Test No.	Frequency setting (Hz)	Voltage setting (%Un)	Voltage meas. (Un)	Trip time setting (s)	Trip time meas. (s)	Trip time deviation (s)
0.18	5	52.00	0.21	0.21	5.000	4.950	-0.050
	6	52.00	1.19	1.19	5.000	4.950	-0.050
	7	52.00	0.19	0.19	Not protected	--	--
		52.00	0.17	0.17	5.000	4.950	-0.050

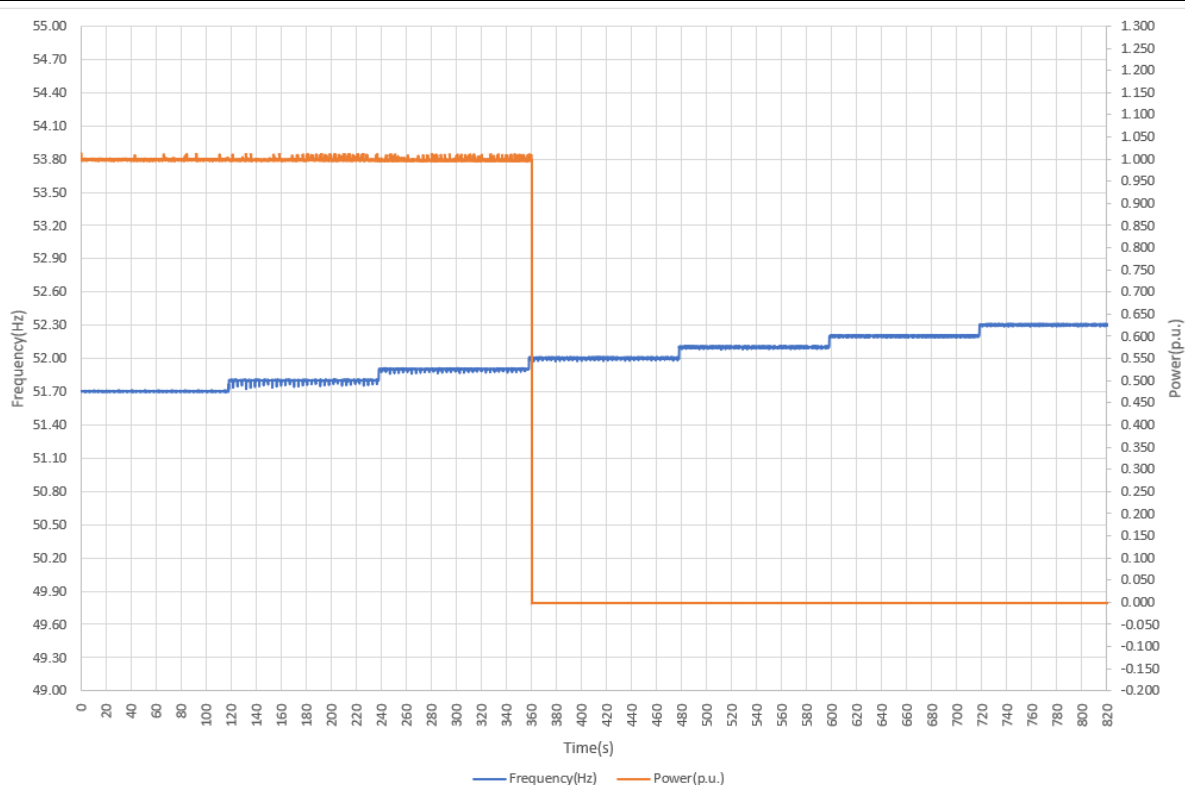
Overfrequency - Test 1: Trip value



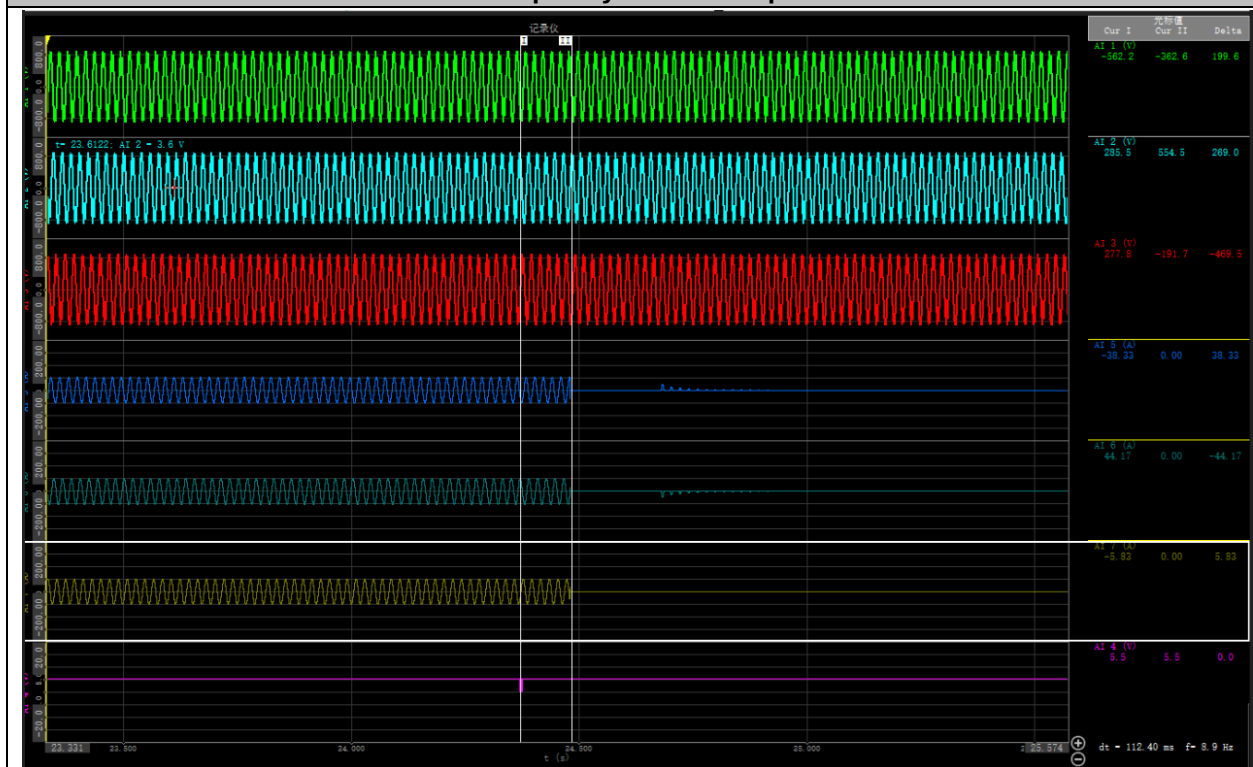
Overfrequency - Test 1: Trip time



Overfrequency - Test 2: Trip value



Overfrequency - Test 2: Trip time



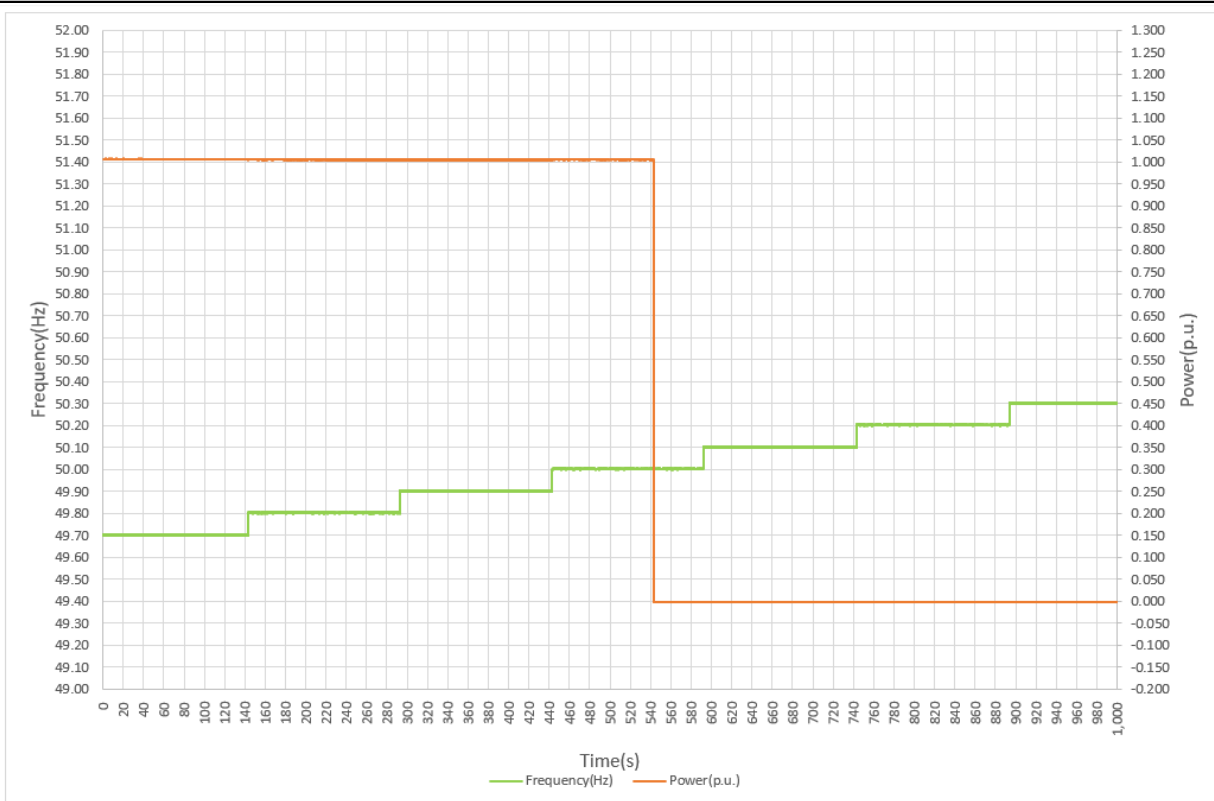
Overfrequency - Test 3: Trip value



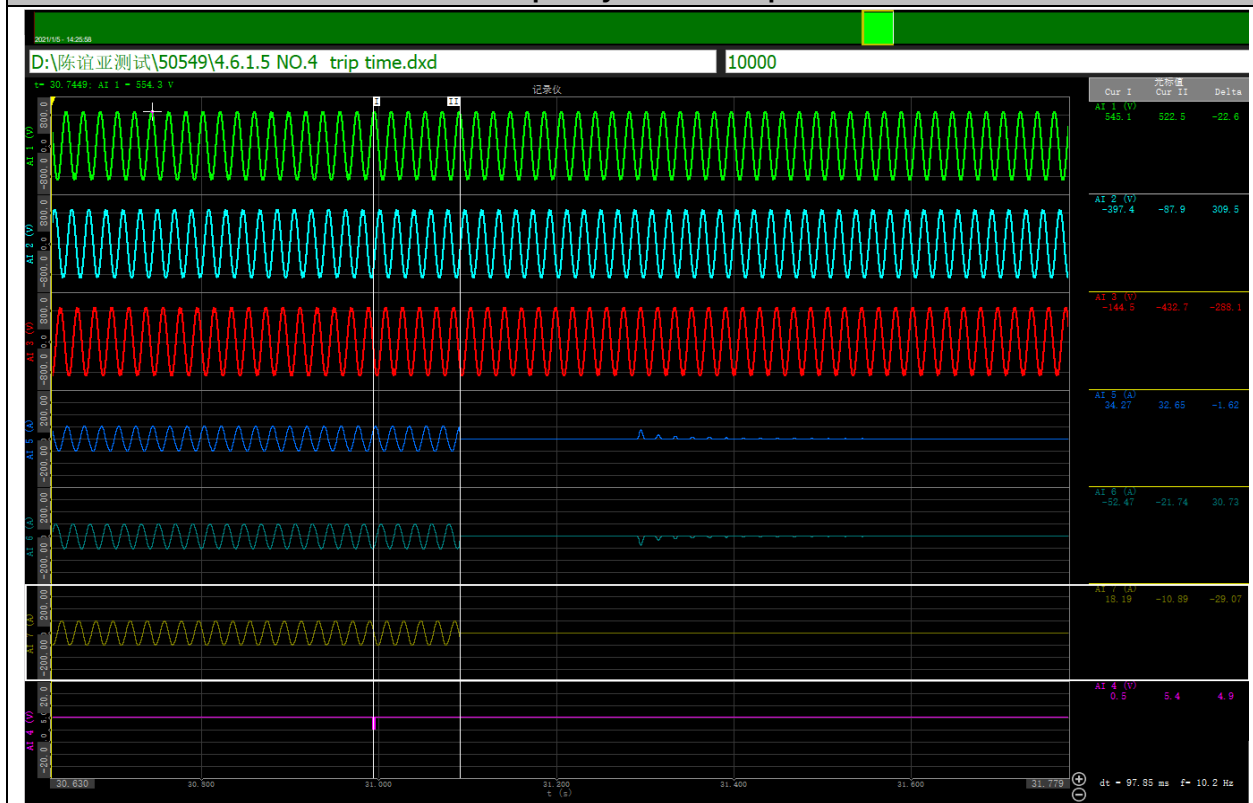
Overfrequency - Test 3: Trip time



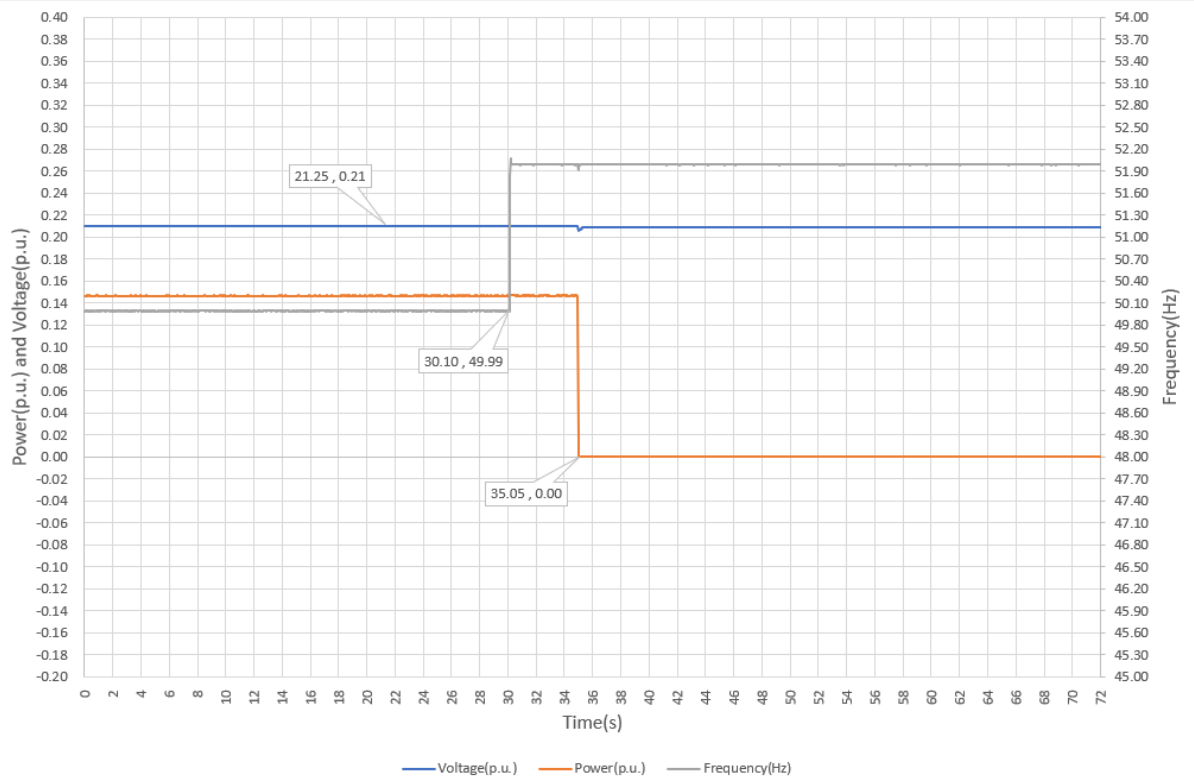
Overfrequency - Test 4: Trip value



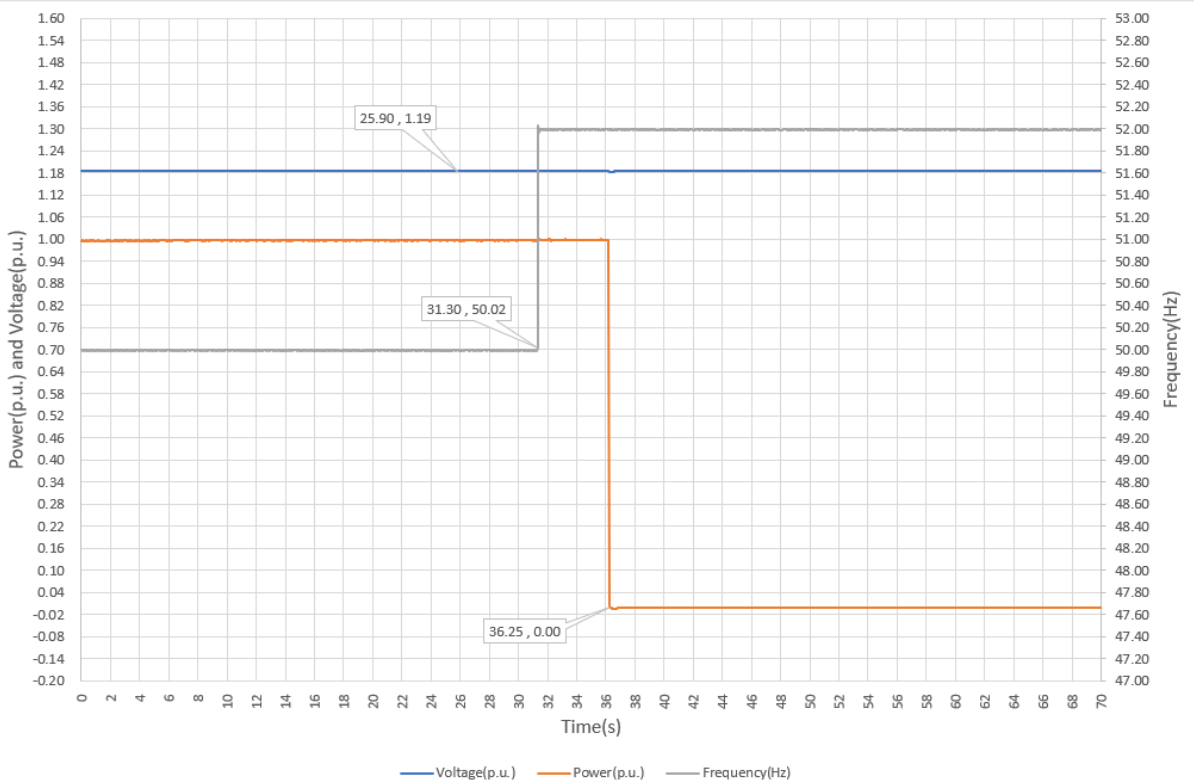
Overfrequency - Test 4: Trip time



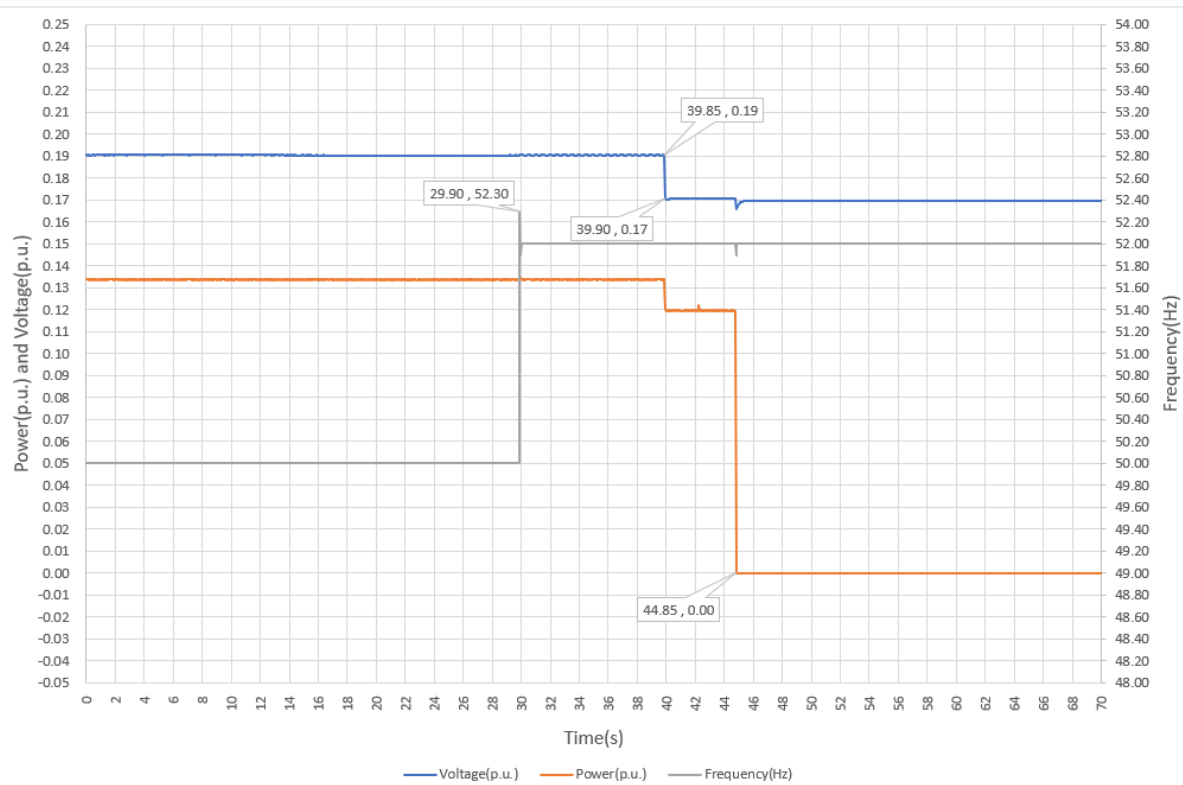
Test 5: Underfrequency 52Hz and Voltage 21%Un



Test 6: Underfrequency 52Hz and Voltage 119%Un



Test 7: Underfrequency 52Hz and Voltage 19%Un to 17%Un



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4.6.2. Means to detect island situation

According to the test requirements of standard 4.9.4, these methods have passed the EN 62116 photovoltaic inverter resonance circuit test.

Model HPS30

The compliance with these requirements is stated in page 30 to page 35 of the following test report:

IEC 62116:2014: Test Report n° ES200211002P which was issued by EMTEK (SHENZHEN) CO., LTD on Mar 01th, 2020.

Model HPS50

The compliance with these requirements is stated in page 25 to page 31 of the following test report:

IEC 62116:2014: Test Report n° ES190602003P which was issued by EMTEK (SHENZHEN) CO., LTD on Dec 18th, 2019.

4.6.3. Digital input to the interface protection

The test has been done according to the clause 4.9.5 of the standard.

The interface protection shall have at least two configurable digital inputs, EUT used active methods tested with a resonant circuit and ROCOF to comply to the clause.

4.7. CONNECTION AND STARTING TO GENERATE ELECTRICAL POWER

The test has been done according to the clause 4.10 of the standard.

4.7.1. Automatic reconnection after tripping

The test has been done according to the clause 4.10.2 of the standard.

The frequency range, the voltage range, the observation time shall be adjustable in the range according to Table 3 column 2. If no settings are specified by the DSO and the responsible party, the default settings for the reconnection after tripping of the interface protection are according to Table 3 column 3.

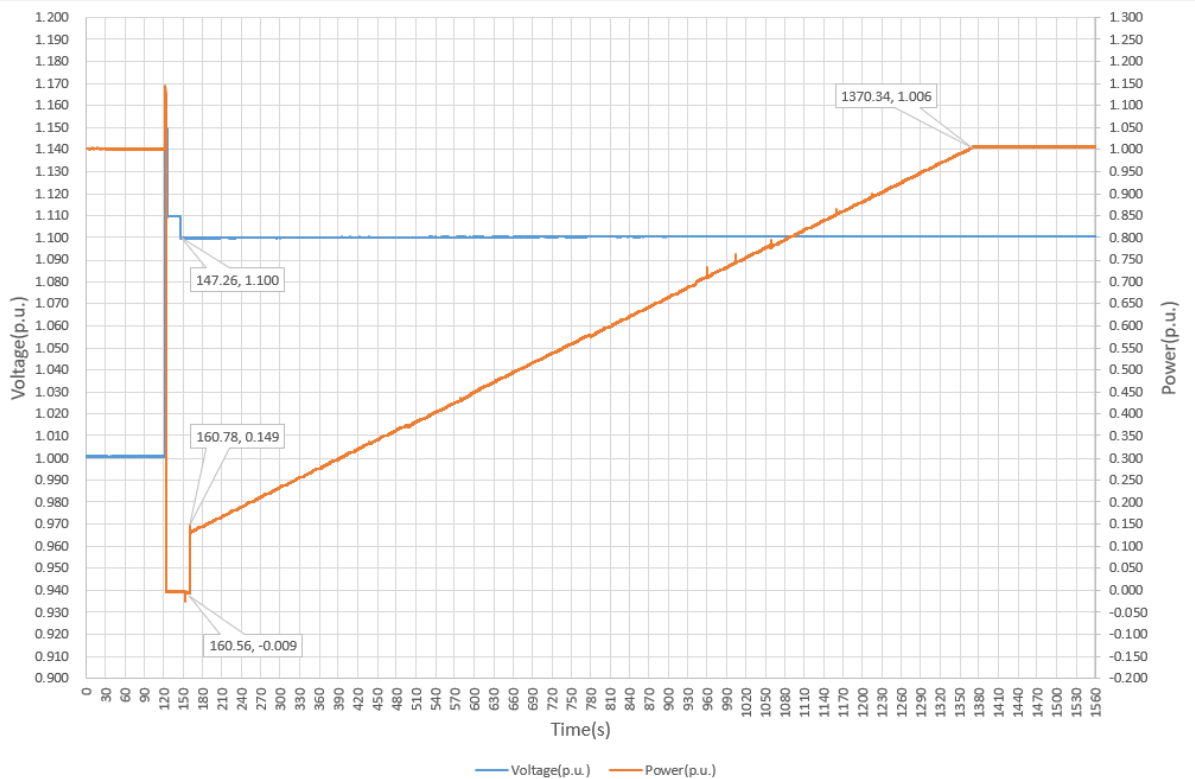
Table 3 — Automatic reconnection after tripping

Parameter	Range	Default setting
Lower frequency	47,0Hz – 50,0Hz	49,5Hz
Upper frequency	50,0Hz – 52,0Hz	50,2Hz
Lower voltage	50% – 100%U _n	85 % U _n
Upper voltage	100% – 120% U _n	110 % U _n
Observation time	10s – 600s	60s
Active power increase gradient	6% – 3000%/min	10%/min

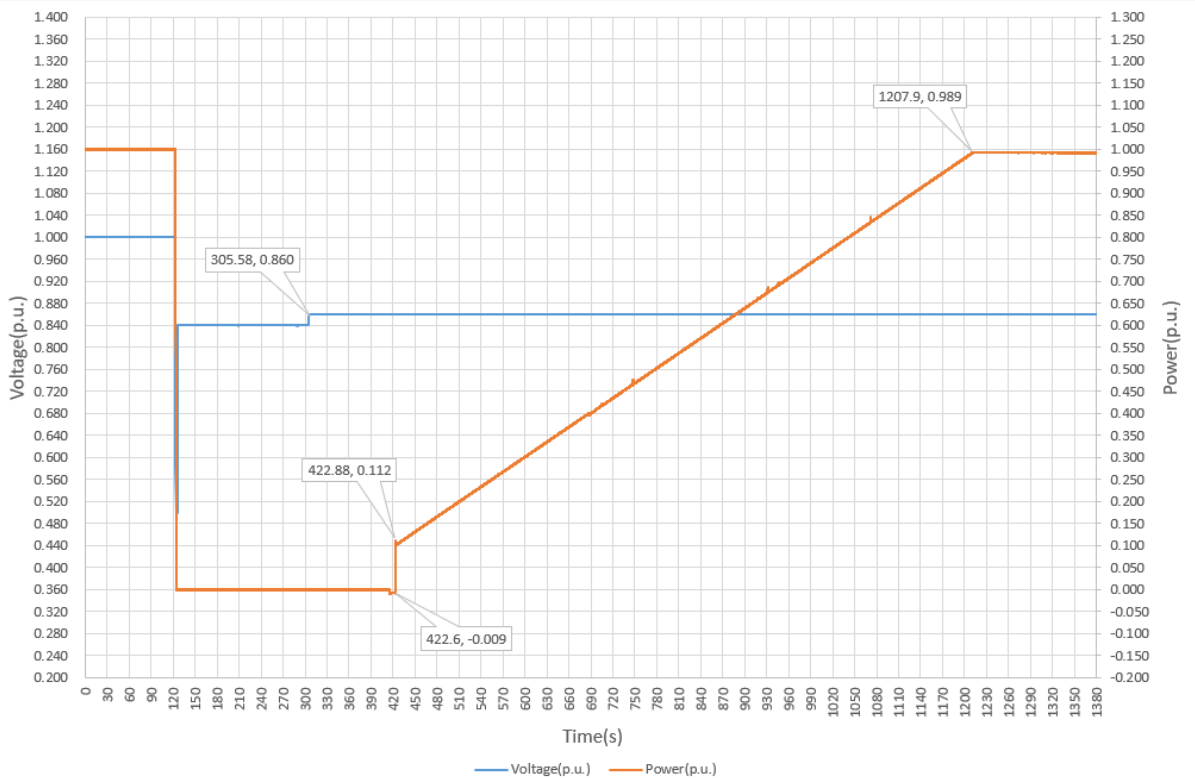
The following definitions apply to the test to verify the clause:

Disconnection Setting		Reconnection Setting		Setting Reconnection time (s)	Meas. Reconnection time (s)	Setting gradient (%Pn/min)	Meas. gradient (%Pn/min)
U= 115 %Un	Yes	U = 110 %Un	Yes	10.0	13.3	4.0	4.4
U = 50 %Un	Yes	U = 85 %Un	Yes	90.0	117.0	6.0	6.7
f = 52.00 Hz	Yes	f = 50.20 Hz	Yes	600.0	624.9	3000.0	2899
f = 48.00 Hz	Yes	f = 49.50 Hz	Yes	60.0	81.7	10.0	10.1

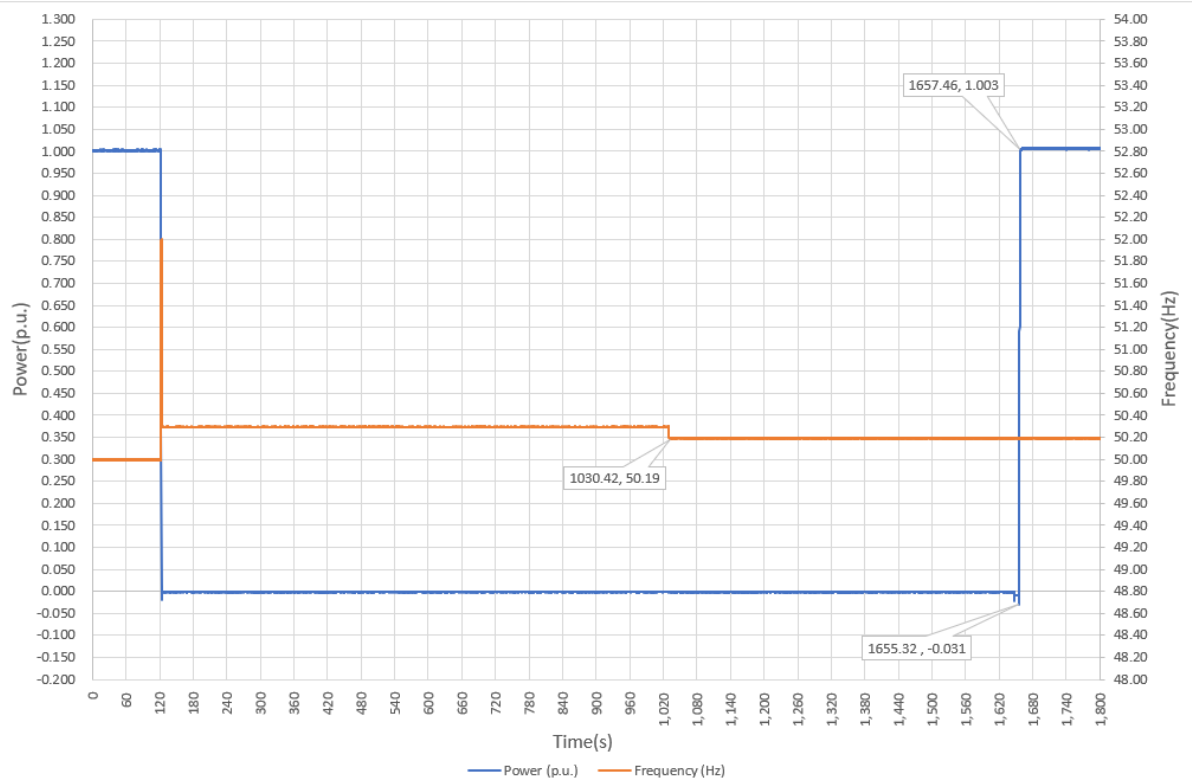
Overvoltage re-connection



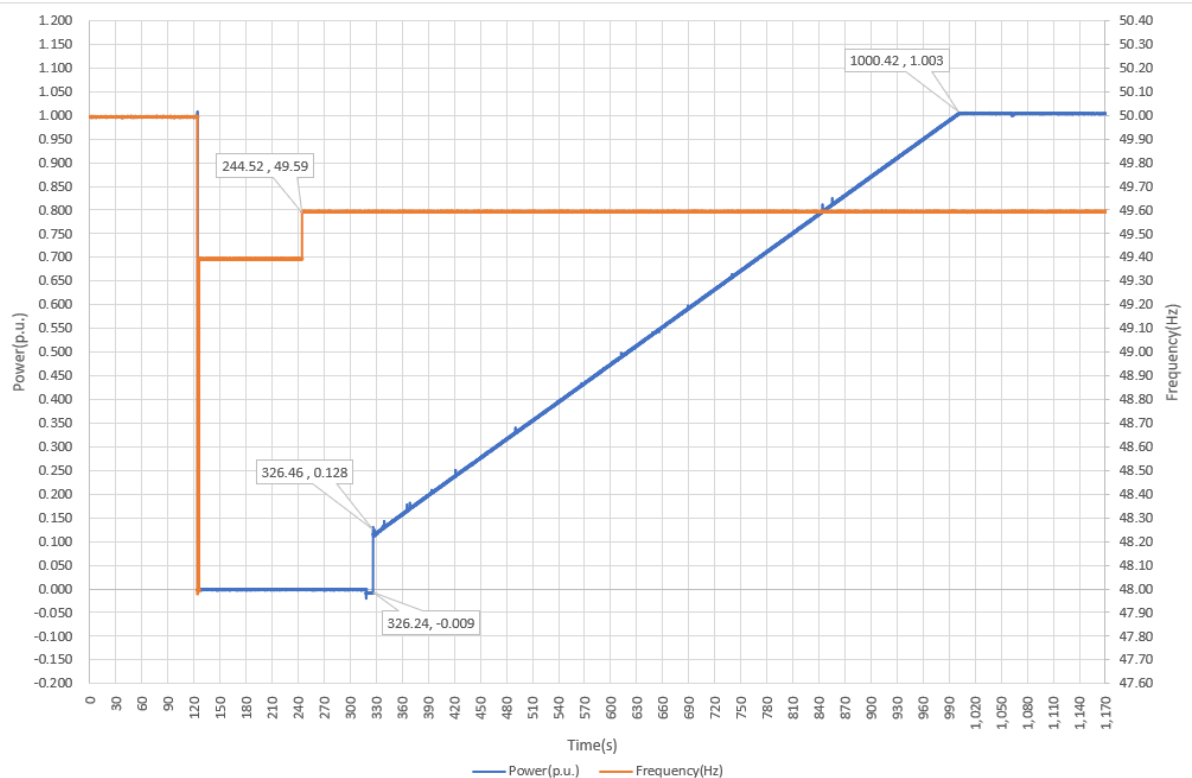
Undervoltage re-connection



Overfrequency re-connection



Underfrequency re-connection



4.7.2. Starting to generate electrical power

The test has been done according to the clause 4.10.3 of the standard.

The frequency range, the voltage range, the observation time shall be adjustable in the range according to Table 4 column 2. If no settings are specified by the DSO and the responsible party, the default settings for connection or starting to generate electrical power due to normal operational startup or activity are according to Table 4 column 3.

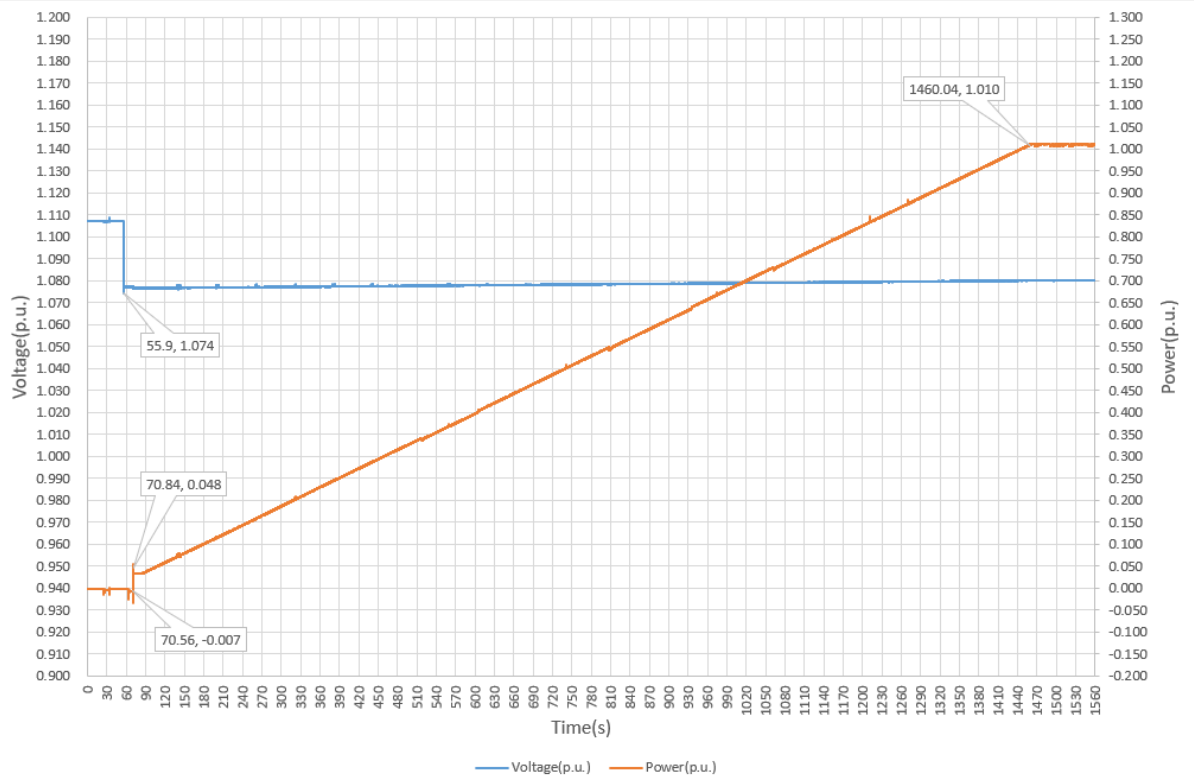
Table 4 — Starting to generate electrical power

Parameter	Range	Default setting
Lower frequency	47,0Hz – 50,0Hz	49,5Hz
Upper frequency	50,0Hz – 52,0Hz	50,1Hz
Lower voltage	50% – 100% U_n	85 % U_n
Upper voltage	100% – 120% U_n	110 % U_n
Observation time	10s – 600s	60s
Active power increase gradient	6% – 3000%/min	disabled

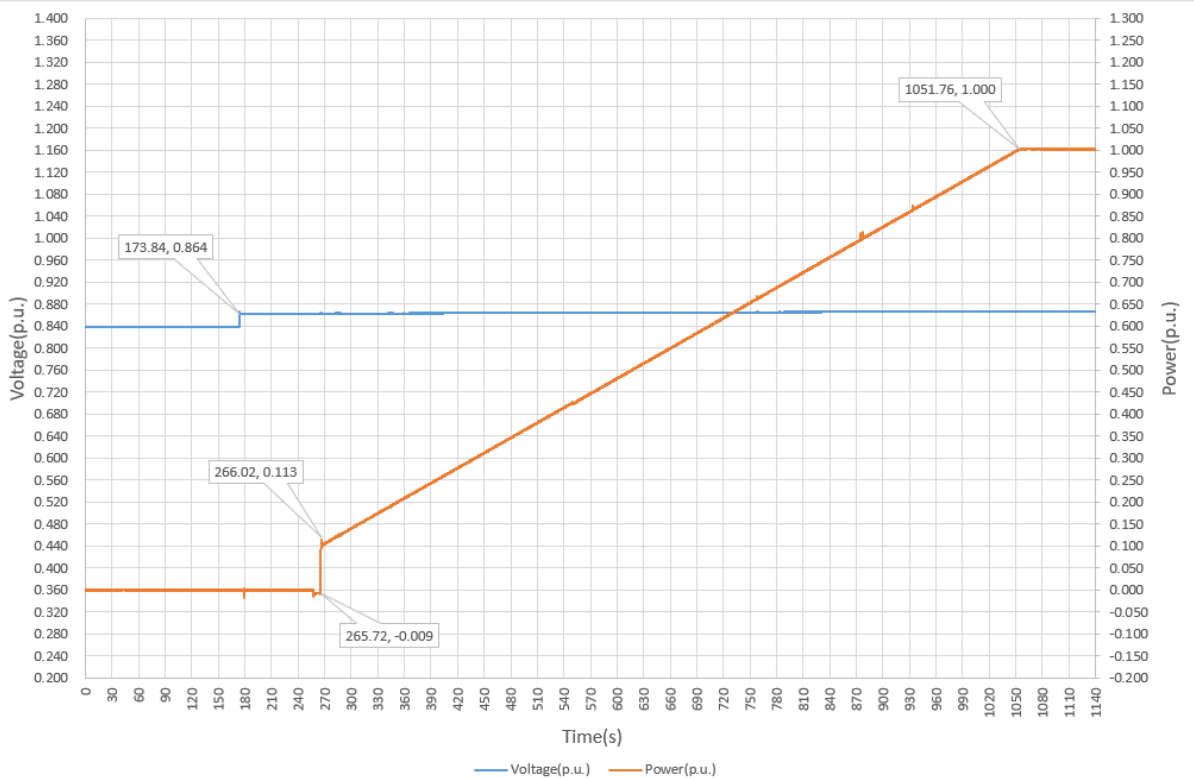
The following definitions apply to the test to verify the clause:

Connection		Setting Connection time (s)	Meas. Connection time (s)	Setting gradient (%Pn/min)	Meas. gradient (%Pn/min)
$U < 110 \% U_n$	Yes	10	14.7	4.0	4.2
$85 \% < U$	Yes	60	91.9	6.0	6.8
$f < 50.10 \text{ Hz}$	Yes	600	622.3	3000.0	2811.1
$49.50 < f$	Yes	60	81.7	10.0	9.9

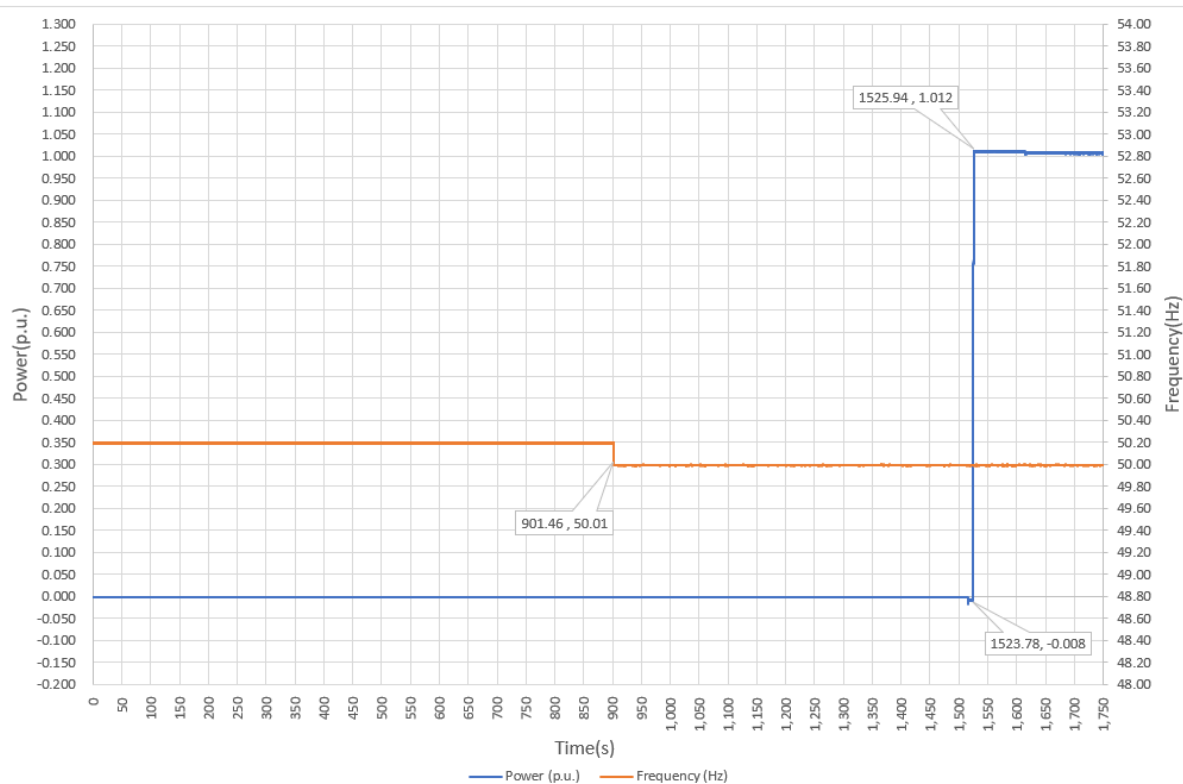
Overvoltage Connection



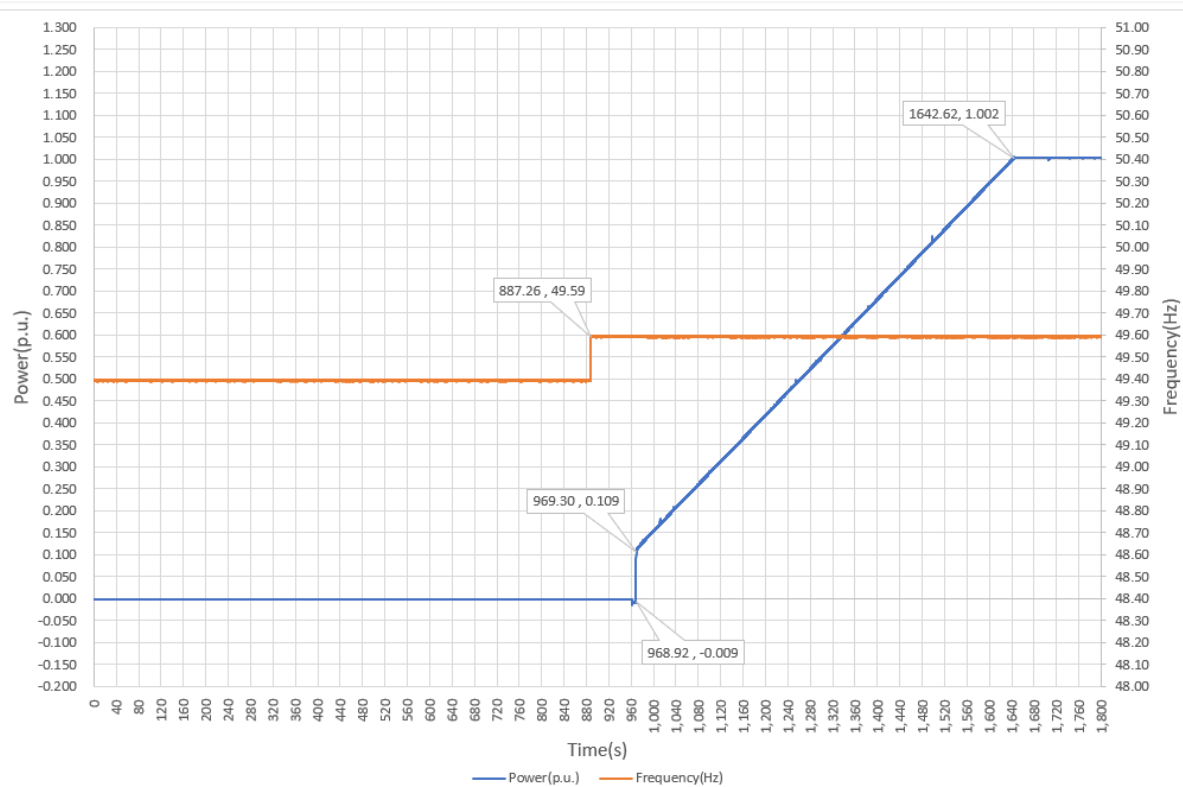
Undervoltage Connection



Overfrequency Connection



Underfrequency reconnection



4.7.3. Synchronization

The requirements are from clause 4.10.4 of the standard. Synchronizing a generating plant/unit with the distribution network shall be fully automatic.

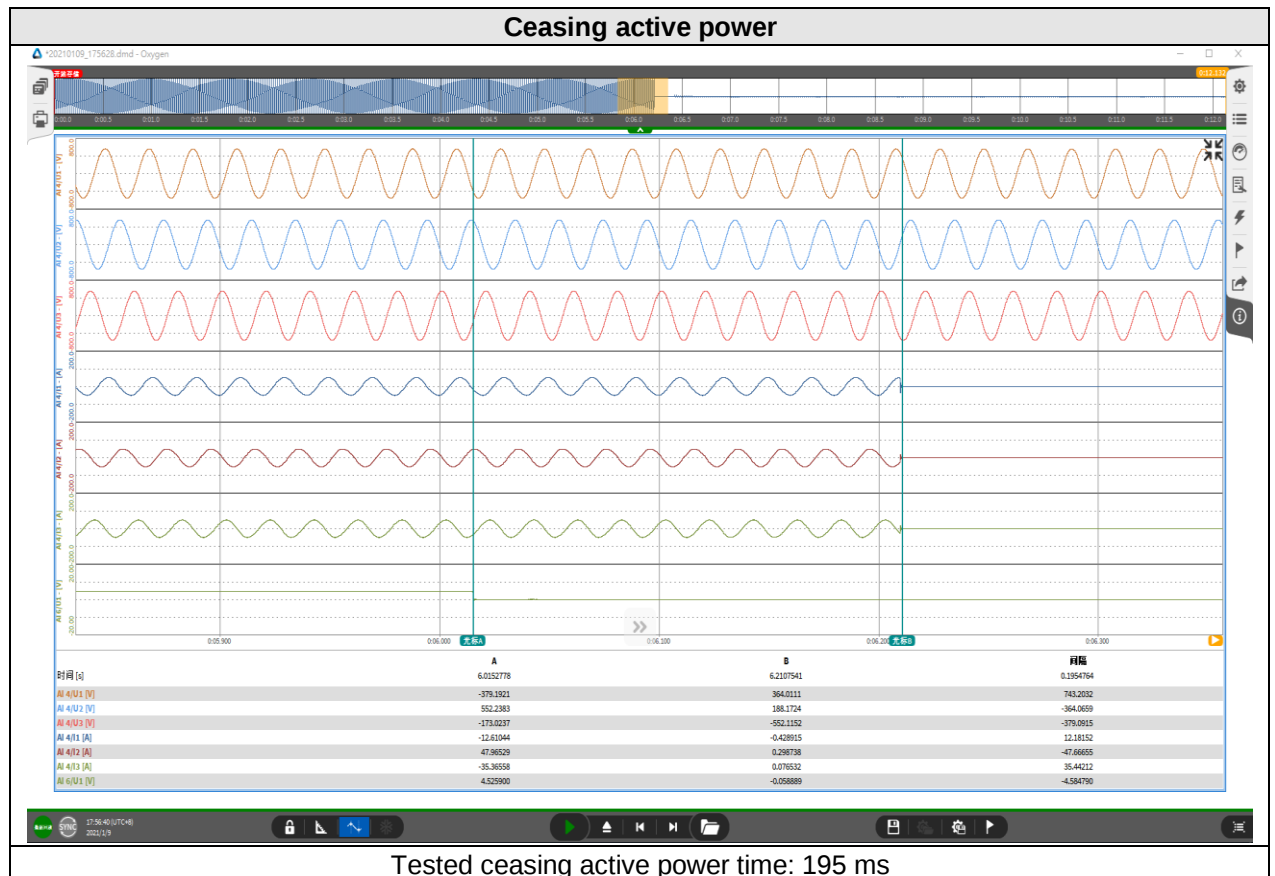
The EUT is fully automatic in the connection to the distribution network.

4.8. CEASING AND REDUCTION OF ACTIVE POWER ON SET POINT

4.8.1. Ceasing active power

The test has been done according to the clause 4.11.1 of the standard.

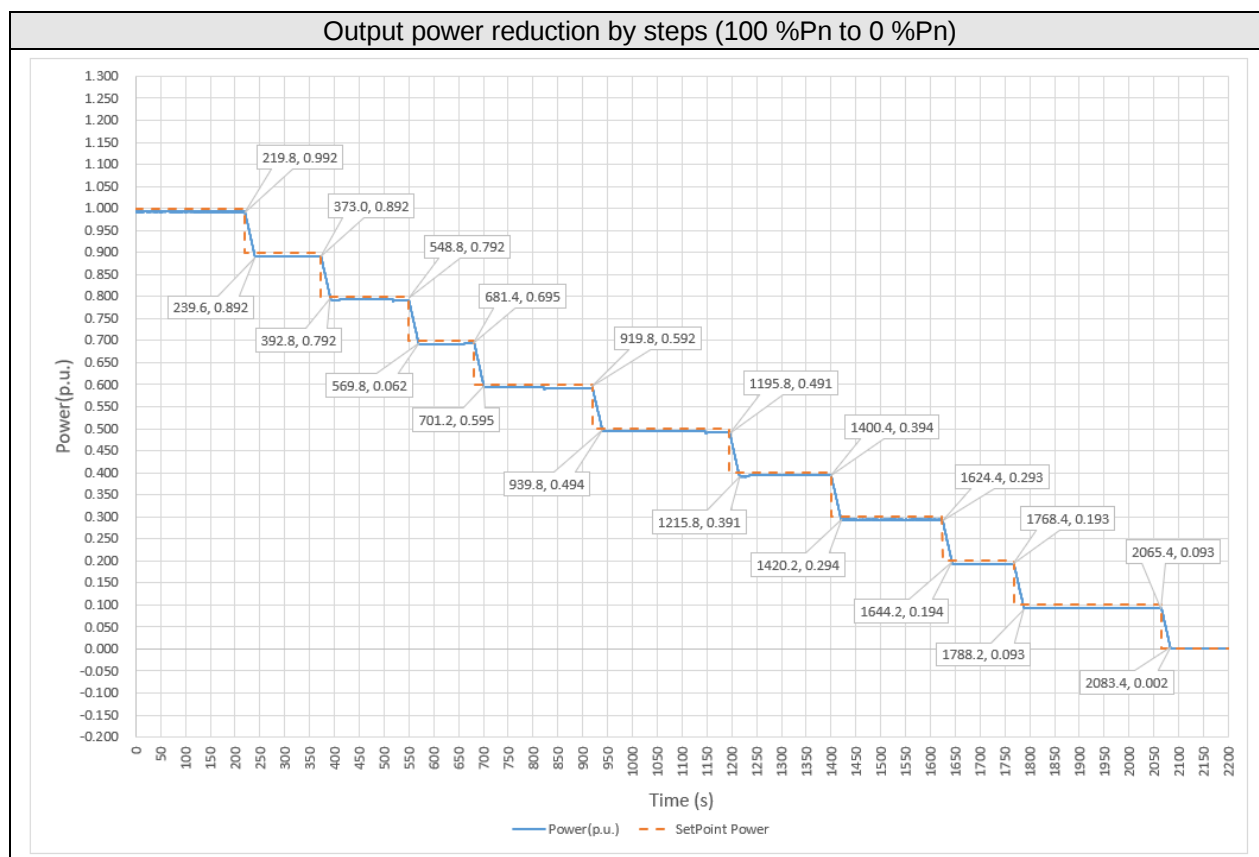
Generating plants with a maximum capacity of 0.8 kW or more shall be equipped with a logic interface (input port) in order to cease active power output within 5 seconds following an instruction being received at the input port. If required by the DSO and the responsible party, this includes remote operation.



4.8.2. Reduction of active power on set point

Test requirements according to the clause 4.11.2 of the standard.

Active Power step (%P _n)	Setpoint value		Actual value		Deviation <±5%		Gradient 0.66%P _n /s to 0.33%P _n /s (%P _n /s)
	(kW)	(%P _n)	(kW)	(%P _n)	(kW)	(%P _n)	
100%	50.00	100.00	49.61	99.23	-0.39	-0.77	--
90%	45.00	90.00	44.62	89.23	-0.38	-0.77	0.51
80%	40.00	80.00	39.71	79.42	-0.29	-0.58	0.51
70%	35.00	70.00	34.64	69.28	-0.36	-0.72	0.49
60%	30.00	60.00	29.67	59.34	-0.33	-0.66	0.51
50%	25.00	50.00	24.70	49.39	-0.30	-0.61	0.49
40%	20.00	40.00	19.70	39.40	-0.30	-0.60	0.51
30%	15.00	30.00	14.69	29.37	-0.31	-0.63	0.51
20%	10.00	20.00	9.68	19.36	-0.32	-0.64	0.50
10%	5.00	10.00	4.65	9.30	-0.35	-0.70	0.51
0%	0.00	0.00	0.09	0.17	0.09	0.17	0.52



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4.9. REQUIREMENTS REGARDING SINGLE FAULT TOLERANCE OF INTERFACE PROTECTION SYSTEM AND INTERFACE SWITCH

The requirements are from clause 4.13 of the standard.

The compliance with these requirements is stated in section 4.4 in page 63 to 64 page of the following test report:

IEC/EN 62109-1: 2010: Test Report n° ES170725041P-1 which was issued by EMTEK (SHENZHEN) CO., LTD on March 31th, 2020.

5. PICTURES

Front



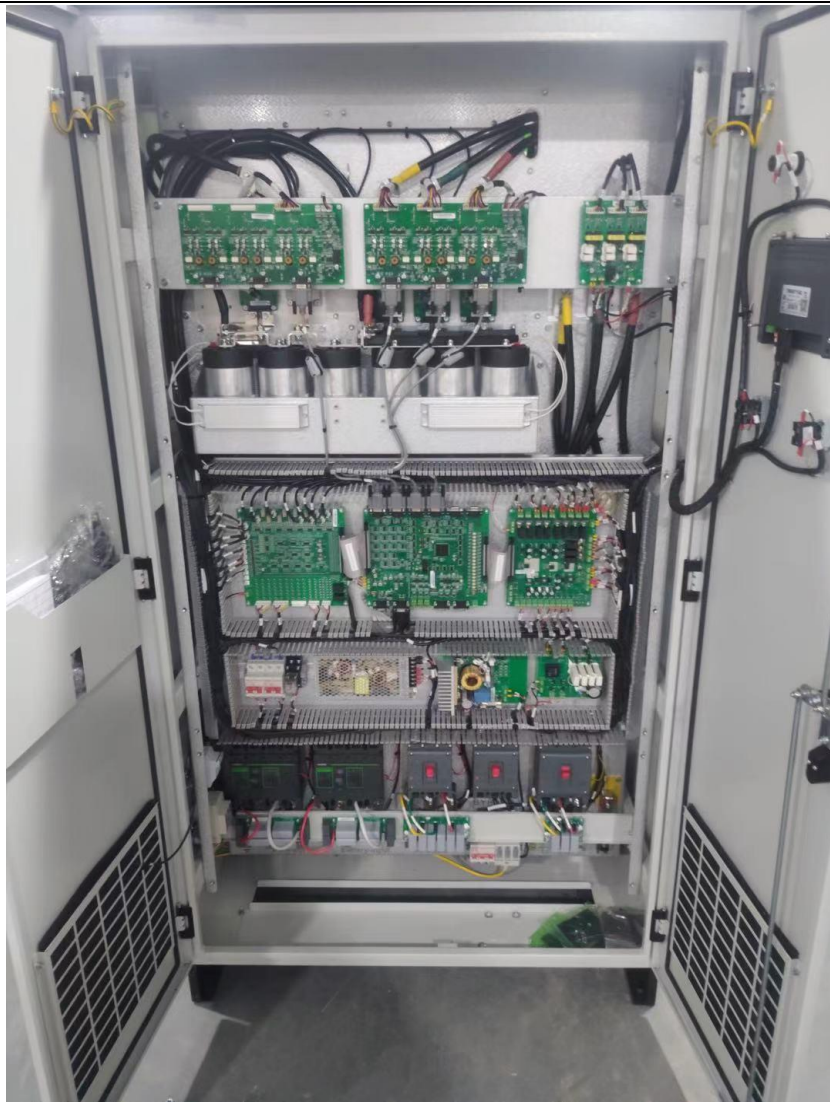
Back side



Side view



Internal of Front side



Internal of Back side



Serial Number	
	
Software Version	
Manufacturer	Shenzhen Ateess Power Technology Co.,Ltd
Serial Number	
DSP Software Version	HPS30K_150K_HV3_SV4.1.14_APP50549
LCD Software Version	HPS30_150K_WEINVIEWScreen_HV1.0_SV1.0.8
Hardware Version	SSCP_HW_V1.1.1

6. ELECTRICAL SCHEME

